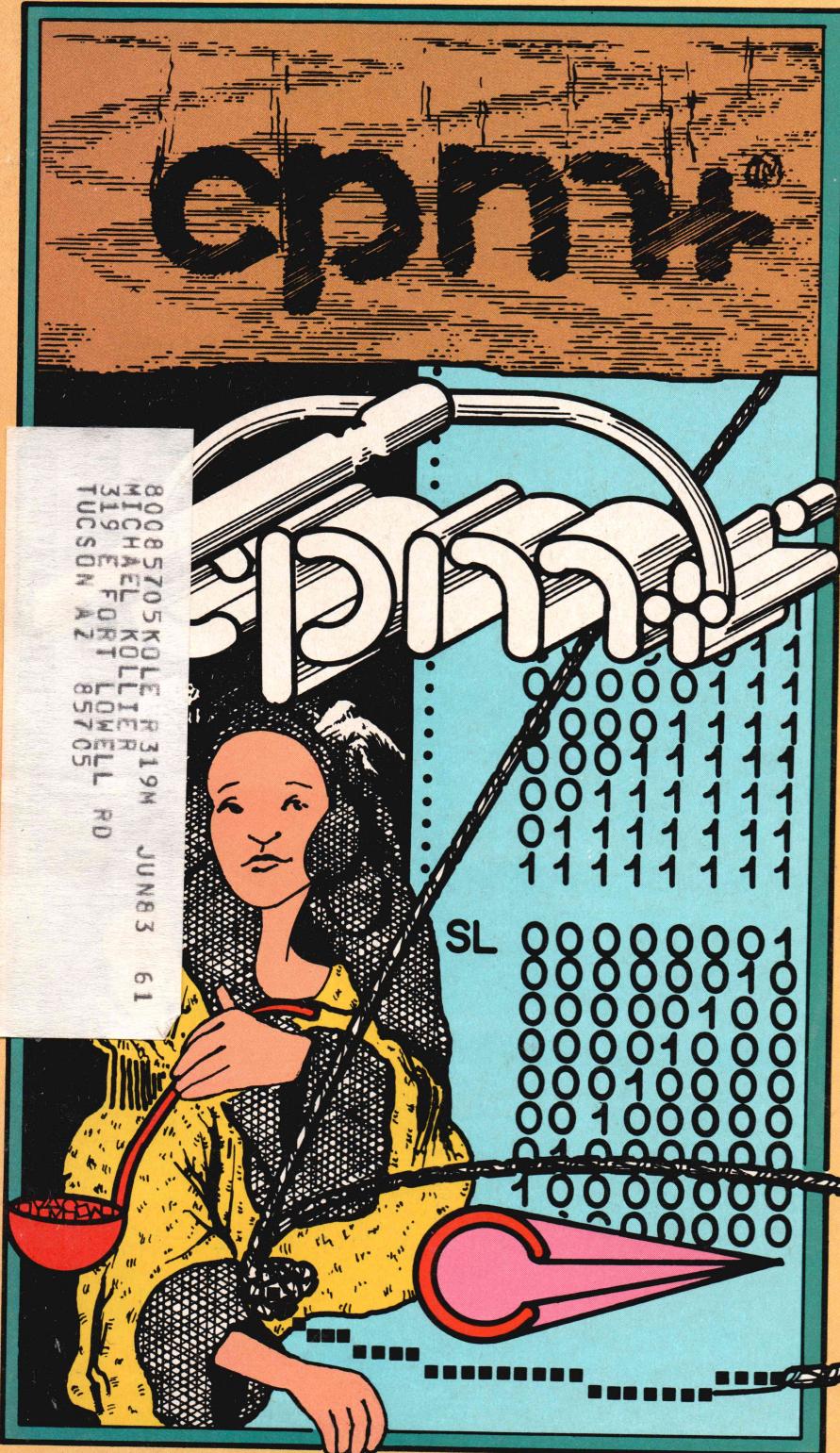


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Dr. Dobb's Journal

For Users of Small Computer Systems



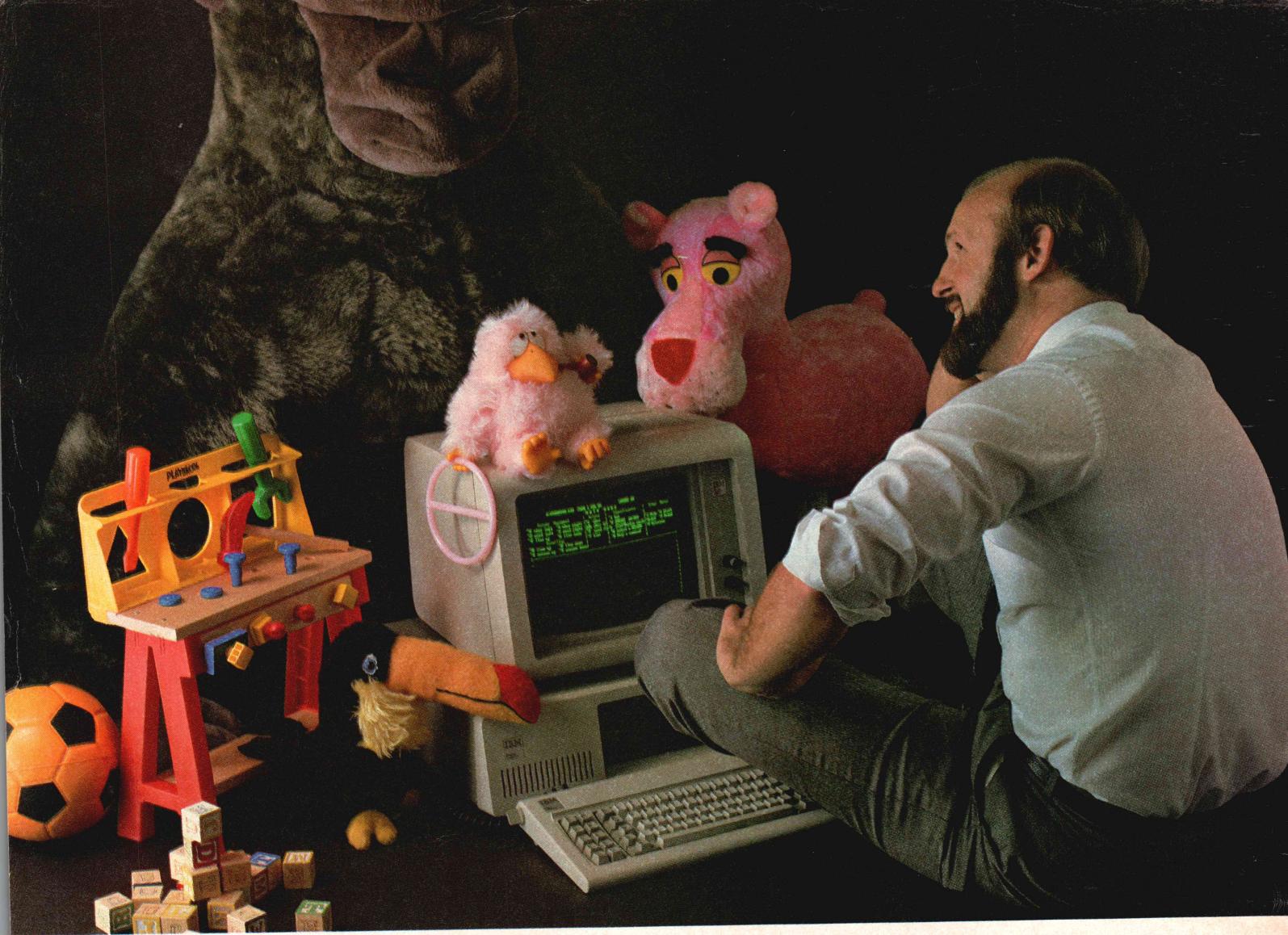
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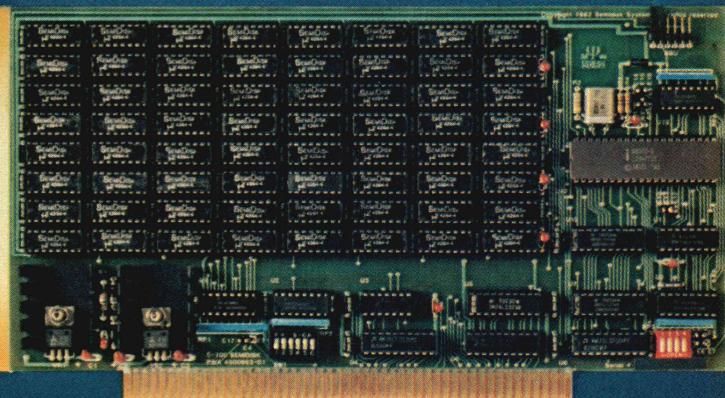
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For Users of Small Computer Systems

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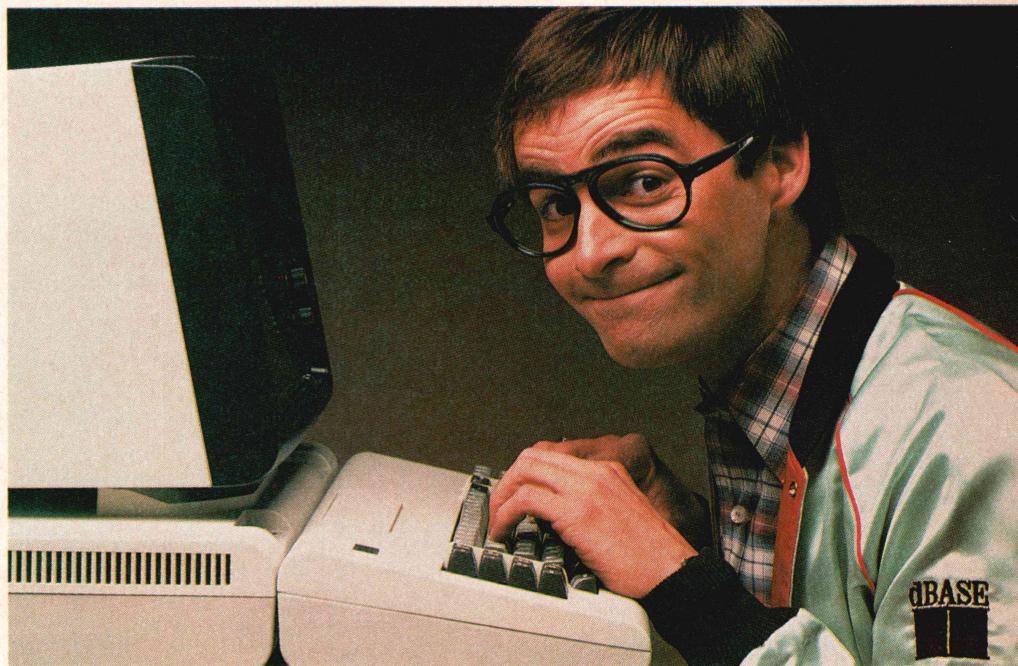
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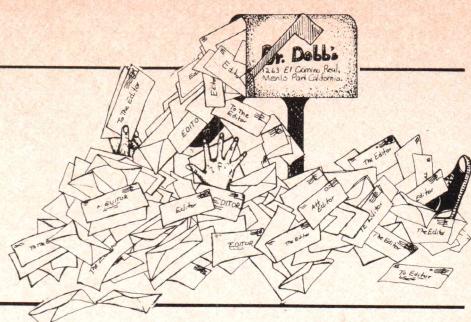
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LETTERS



Likes to C It Standard, Too

Dear Dr. Dobb,

I had to jump up and cheer when I read Richard Foulk's letter (*Standard Deviation*) in *DDJ* No. 77. I am a relative newcomer to C, so when I started seeing deviations from Kernighan & Ritchie, I was dismayed but figured maybe they knew what they were doing. I am glad to see that someone else feels the same way.

I bought Software Toolworks's C/80 which, though not the full implementation, does not deviate from K&R. My hat is off to Walt Bilofsky and his crew. Then I started transferring C User Group software from CP/M to HDSO (Heath) media so I could use it. The hardest part was translating the file-handling functions to the K&R standard to which C/80 adheres. (Are you listening, Leor Zolman? Please bring BDS C back to the K&R fold.)

I always thought that one of the greatest advantages (among several) of C over Pascal was the greater uniformity and portability among versions. Now, that seems to be fading away.

Meanwhile, take a pat on the back for putting out a fine journal, and keep those C features coming.

Sincerely,
Robert L. McClure, Jr.
Route 1, Box 158
Troy IL 62294

CREF Update

Dear People:

Several people have called with questions about "Cross-Reference Generator in C," in *DDJ* No. 68. The code fragment at the end of line 53 of "cref.c" is actually the end of line 99. The Whitesmith's implementation of `alloc()` takes two parameters. The first is the number of bytes of space to allocate and the second is put in the first word of the allocated space. All linked lists in the program use the first word in the structure as a link. The whole scheme works very nicely for linked lists implemented this way.

The recursive implementations of `lookup()` and `tree_walk()` are elegant, but can use large amounts of stack space. With large files on CP/M-80, this can cause the stack to overwrite the space allocated from the heap. Revised listings are included with this letter (see Figure 1, page 9). Although the revised `tree_walk()` occupies more space, the capacity of the revised program is larger. Algorithm T of *The Art of Computer Programming*, Volume 1 is the basis of the revised `tree_walk()`. The stack used in Knuth's version is implemented within the tree being traversed. The left link field is used as a pointer to the node under it on the stack. The tree is destroyed as it is traversed.

One additional trick: when more files are being cross referenced than will fit on a command line, use #include statements on the standard input.

Thank you to those people who called or wrote.

Sincerely,
Jeff Taylor
The Toolsmith
139 G Street, No. 151
Davis, CA 95616

More Benchmarks for the PC

Dear Sir:

We have performed a benchmark test using Fortran and BASIC on several computers. The program was a simulation of the dynamics of a "floating-ring seal" used in high-speed turbomachinery. This is a typical scientific/engineering application, which involves lots of number-crunching. The motivation here was to assess the speed of the IBM PC when used for engineering or scientific applications, and to compare it with other personal and mainframe computers. All tests were performed in single-precision. The results are shown in Figure 2 on page 9 (numbers in parentheses indicate timing with Intel 8087 Math Coprocessor and appropriate libraries).

In a further test we have run the

EDITORIAL

This issue we welcome two new faces to our ranks. Bob Blum takes over the CP/M Exchange column this month and presents his first installment of a series on CP/M Plus. He will be exploring the features of this new product over the next few months, as well as looking at how to get more from the CP/M 2.2 that you may already have and addressing the usual mailbag items. His knowledge of the ins and outs of CP/M should prove helpful to all of us.

The other new face is Craig LaGrow, our managing editor. Craig is a science writer finishing his master's degree in journalism at Stanford University. With a strong background in publishing and keen interest in computer technology, he will ensure a continued increase in the quality of our publication.

* * *

Many of you have discovered the "quick input" card that appears as an insert with the reader service card. For those of you who have not, let me point it out as an excellent way to drop us a quick note. There is no substitute for a full letter that can be shared, when appropriate, with the

readership at large; but this card is a fast, easy way to send suggestions or comments. Either way, we love hearing from you.

* * *

For those who have been wondering, we *are* planning our annual Forth issue. We think you will find it interesting, as always. Further down the road, we would like to publish another telecommunications issue. We have no definite date set, but are looking toward late 1983. Those interested in contributing to an issue on this timely topic should get in touch with us.

On the subject of general contributions, we would like to encourage articles on a wide range of topics. A few that come to mind immediately include the 68xx series of CPUs, 16-bit utilities and applications, C and Unix. If you have an idea or article in an area that might interest our readers, please contact us. We will be glad to discuss it with you.

— Reynold Wiggins,
Editor

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DD 583

Figure 1.

```
/* lookup - find pointer to node for "name" */
struct node **lookup(root, name)
    register struct node **root;
    register char *name;
{
    register int c;

    while(*root && (c = lexicmp(name, (*root)->name, 0)))
        root = c < 0 ? &(*root)->left : &(*root)->right;
    return root;
}

/* tree_walk - walk a binary tree, doing ftn to each node */
tree_walk(root, ftn)
register struct node *root;
int (*ftn)();
{
    register struct node *stack, *tmp;

    stack = NULL; /* stack initially empty */
for ( ; ; )
    if (root) {
        tmp = root; /* move to the left */
        root = root->left;
        tmp->left = stack; /* stack node */
        stack = tmp;
    }
    else if (stack) {
        root = stack; /* pop stack */
        stack = stack->left;
        (*ftn)(root);
        root = root->right;
    }
    else
        break;
}
```

NBS Methane properties package on the IBM-PC under Fortran 3.03. Execution time was 16.5 seconds without the 8087, and 2.5 seconds with the 8087. A time of 90 seconds was obtained for this program on the Xerox 820.

Two results deserve special comment. First, both the original IBM release of Microsoft Fortran and the new 3.03 release (presently under Beta Test) are laughably slow. Note that even the 3.03 version is practically as slow as the 8-bit Fortran of the Xerox 820! This result is presumably due to Microsoft's unwise decision to write their Fortran in Pascal — thus burdening it with all of Pascal's inefficiencies. Even Microsoft's own BASIC compiler is faster! These Fortrancs are therefore nearly useless as serious tools for engineering work, and we can only hope that some enterprising firm will fill the need for a fast Fortran for the IBM PC.

Next, the BASIC compiler with the 8087 does not achieve anything like the speed improvement that Fortran does. Whether this is due to inefficiencies in the MicroWare BASIC87 library or to problems with the BASIC compiler itself is an open question.

Sincerely,
Jim Glass
Chris Landis
4747 Orion Avenue, Apt. C
Sherman Oaks, CA 91403

DDJ

Figure 2.

Machine	Language	Approx. Execution Time Seconds
CDC-176	FORTRAN	0.8
IBM-3033	FORTRAN	1.63
UNIVAC 1100/83	ASCII FORTRAN	12
TEKTRONIX 4081	FORTRAN	196
PDP-11/34	FORTRAN	94
IBM-PC	BASIC Compiler	866 (773)*
Xerox-820	FORTRAN	3386
IBM-PC	IBM FORTRAN (original)	4440 (1711)**
IBM-PC	FORTRAN 3.03 (new Microsoft release)	3284 (585)**
IBM-PC	BASIC Interpreter	11935
Apple	BASIC Compiler	5153
Apple	BASIC Interpreter	16937

* Using MicroWare 8087 Library

** Using Microsoft 8087 Library

by D.E. Cortesi, Resident Intern

Disk Problems, Continued

Back in February we presented Loren Amelang's problem of backup diskettes that would often be unreadable after a few months of storage. Randolph Fritz, of Mahwah, New Jersey, writes with a number of suggestions. "Problems like Amelang's," he says, "are often caused by mechanical disk problems. Loren's drives might just have dirty heads. If so, the normal decay of disk magnetization, most of which takes place within a few days of writing, could make reading erratic."

"Then, if there are two drives, they may be subtly incompatible. This is especially likely if one is used as a system disk and the other to write backup copies. Finally, the disk drive's head positioner may be worn. In this case, track positioning becomes erratic and dependent on the order in which tracks are read and the rate at which tracks are read."

The net of Fritz's suggestions is that Amelang's disk drives should be taken to a competent repair shop for cleaning, inspection, and alignment. He gives some suggestions on how to do it yourself, but ends by saying, "Disk drive maintenance is a complex subject. Perhaps one of your readers would suggest a good book on the matter?" Indeed. Is there a book, or a manufacturer's manual, that has a good description of how to do routine maintenance on disk drives? Or is there a drive mechanic in the house who'd like to do an article on the subject?

Disk Format Survey

Aubrey Hutchison wants to know if anyone has ever gathered and published a listing of the disk formats used by different machines. We don't know of any; does a reader? If not, it would be neat to publish a really comprehensive list, especially of five-inch formats, in *DDJ*. Why not send us a description of the disk format(s) supported by your system, something like this.

tracks	:	0..39
sectors/track	:	1..10
bytes/sector	:	512
skew sequence	:	1,4,7,10,3,6,9,2,5,8
DPB.OFF	:	2 (reserved tracks)
DBP.DRM	:	3F (64 directory entries)
DPB.DSM	:	C5 (198 data blocks)
blocksize	:	1024

The last four items apply only to CP/M, of course; they are the numbers from the Disk Parameter Block which determine

how the BDOS allocates space on the drive. If you don't use CP/M, say what the OS is, and include whatever software values are important or would vary from disk to disk.

Hang 'em High

Another continuing problem is how Ernest Knipp's Z80 could get hung up so high that he has to power the system down and up to restart it. Don Taylor recalls a similar problem when he built "a homebrew disk interface with a DMA controller. Every time I tried to do a transfer, the machine would lock up and reset would not help. It turned out that the BUSACK signal was not getting to the controller, but the CPU had shut down waiting for the controller to release BUSRQ. BUSRQ, it seems, has higher priority than a reset signal."

Alan Behler of Newport, North Carolina, has another suggestion. He passes on a tip from an early edition of *Computer Notes*, a newsletter published by MITS for the Altair: "Hold the RUN/STOP switch on the front panel to STOP, and at the same time press RESET." Behler says that the same tip applies to the IMSAI 8800. The problem seems to be a conflict between the CPU and the front panel hardware.

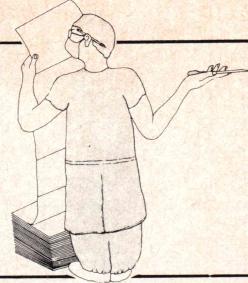
Controlling MBASIC

A while ago, we passed on Aubrey Hutchison's tip that control-A could be used to make MBASIC duplicate a line of a program. William McWorter explored it and found that "it appears that you can edit whatever happens to be in the input buffer at the time control-A is entered. Thus you can re-execute an immediate line by typing control-A followed by E."

Harvey Hahn knew more about control-A. He writes, "I have been using the control-A technique for a number of years with my Altair 8800. It was explained in a full-page article in *Computer Notes*, Volume 3, Number 7 (Jan/Feb 1978), page 18." Hahn sent along the lead paragraph of that article:

"CTRL-A, a feature of MITS® BASIC, has become a powerful programming aid, due to an undocumented feature discovered by Donald Fitchhorn of MITS. If CTRL-A is typed immediately after EDITing a program line, the edited line is returned as a command to be edited. Thus CTRL-A can be used to shuffle program lines, break apart multiple statement lines, and isolate program errors."

Undocumented in 1978, and undocumented still. Oh, well.



The Buffered Keyboard

Our shiny new BIOS is now working. Absolutely its best feature is an interrupt-driven, buffered keyboard. When a key is pressed, an interrupt occurs, and the input byte is saved in a ring buffer. This cures a problem that has plagued several of our favorite applications.

Our H19, like many terminals, generates a two-byte sequence when one of its function or cursor-move keys is hit. The first byte is ESC; the second is a printable character. An application like SuperCalc can be customized to handle these sequences, and as long as keys are hit sloooowly, everything works fine. But if a special key is hit twice, rapidly, the program may not have finished its response to the first key when the second key's bytes start to arrive. The ESC is replaced in the I/O port's buffer by the second character, and is lost. When the program finally gets around to polling the console, it finds only the printable character. Among our programs, SuperCalc was the worst afflicted, because it updates several places on the screen whenever a cursor-move key is hit.

The interrupt-driven BIOS cures the problem for all programs, because it takes control within microseconds of a byte's arrival and puts it away for later use. Now we can run off a string of cursor moves with the repeat key, and sit back to watch SuperCalc huffing and puffing to catch up. But it wasn't easy to get this feature. We had to make several hardware fixes.

CCS vs. CompuPro

The first problem was that our CCS 2810 Z80 board would not work with the interrupt controller on our CompuPro System Support board. The CompuPro board monitors the vectored interrupt lines of the S-100 bus and generates a single INT signal when one is pulled. The CCS board would recognize INT and go into an interrupt acknowledgement cycle, but what it fetched from the data lines was always an RST 7, not the three-byte call instruction that the CompuPro was supposed to generate. Reset 7 is FFh, and it really indicated that there was no data at all on the bus at the time the CPU was looking for it.

Let's skip lightly over several hours of fairly intense work and move to the

conclusion. The CCS 2810, or at least our elderly example of it, was not kosher by IEEE 696 standards. It was generating sINTA, the interrupt acknowledgement, much too late in the interrupt cycle for the CompuPro board to respond (two clocks later than pSYNC, in fact). Oddly enough, it was generating a perfectly good INTA status signal; however, this signal is gated onto the data bus along with the other simulated 8080 status bits, as a compatibility feature. The CompuPro, as you might expect, wouldn't have anything to do with such non-IEEE frippery. It expects sINTA to become valid, along with the other S-100 status bits, when pSYNC comes up. We were able to get the good INTA signal onto the sINTA bus line by cutting one trace and soldering in one jumper on the CCS board.

(That sounds so cool. Just cut a trace, solder a jumper, pooh, no problem. Listen, no diamond cutter ever braced as carefully as we did before cutting that trace. And the sweat was rolling in streams when we powered up afterward. Would our one and only CPU board come up, or would some chip quietly go phht and leave us without a computer? And it did not come up! PANIC! Then, looking down from the ceiling, we noticed that we had forgotten to connect the ribbon cable that runs from the CPU board to the CRT. With that hooked up, it ran. Phew.)

CompuPro vs. Itself

We were using one port of our old CompuPro Interfacer II board as a source of test interrupts. A Diablo printer with a keyboard was hooked to it, so we could produce a byte and an interrupt, while a test program reported the results on the CRT. The Interfacer II generated interrupts just as expected, provided we enabled only received-data interrupts. If we enabled transmitted-data interrupts, the test program would report a continuous stream of them, indicating that the Interfacer II was holding its vectored interrupt line in a low state all the time.

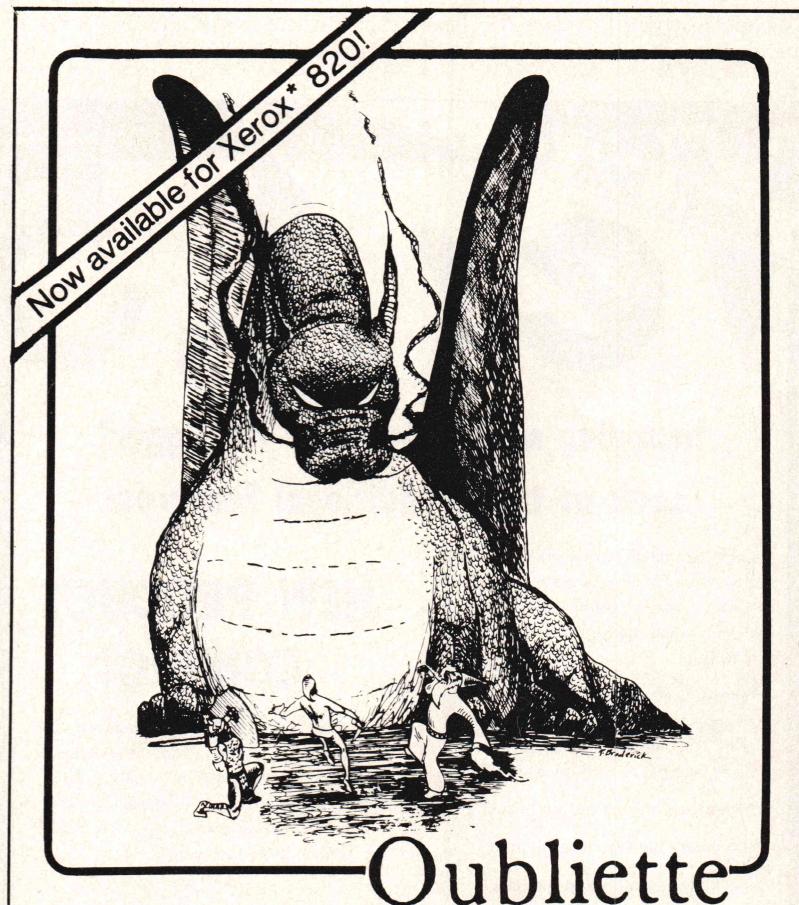
This should not have been the case. According to the schematic, the Interfacer II should reset its interrupt signal when it sees sINTA. We pulled the board to see if there was a chip we could change easily. Then the problem became clear: the board didn't match the schematic that came with it. Where there should have been a chip U13, there was only blank green board; the inverted Transmit Buffer Empty signal from the UART went straight to the interrupt line and there was no reset circuitry at all! We never did find out whether we got an early board with a late schematic, or vice versa. The Interfacer II isn't in production anymore, and thank goodness we didn't need its interrupts.

CCS vs. The World

The interrupt we really wanted was one from the UART on the 2810 CPU board, to which the CRT is attached. Unfortunately, CCS designed their board assuming that only polled I/O would be used. They used the nice 8250 UART (the IBM PC uses it, too), which has an elaborate structure of prioritized interrupts built into it. However, CCS left the 8250's interrupt pin unconnected. We'd inquired about this when first looking into interrupts a year ago, and were then given instructions on how to access the 8250's interrupt ability. Here, for the information of those who have a 2810 CPU board, is how to do it.

The 8250 generates an interrupt signal on its pin 30. This is a positive-going, CMOS signal. It has to be buffered and inverted before it can be applied to the S-100 bus. Near it on the board is U7, which contains an unused XOR gate (make sure it really is unused); this can be used as an inverter. We were able to connect U5 (the 8250) pin 30 to U7 pin 12, and U7 pin 13 to U7 pin 14 (tying it to +5 volts). Then we ran a rather lengthy jumper from U7 pin 11 up and over the top edge of the board, and all the way down to the solder pad for S-100 line 4, VIO*.

It worked perfectly the first time. During a cold start, our BIOS has to prime



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the CompuPro interrupt controller (it takes hours of study to figure out which of the 8259's many options to program) and also load the 8250's interrupt control register to allow received-data interrupts. Information on 8250 interrupts has been omitted from the CCS manual; you have to have the 8250 data sheet (or use the Technical Reference manual for the PC, which we had). We also allow the 8250 to interrupt when the Break key is hit. Our BIOS takes this as a signal to discard any buffered input, but it could be used as a hyper-escape into a monitor of some kind.

Software vs. Common Sense

Then we worked out the software problems of using a buffered keyboard. The first was the CCS disk formatter, whose second instruction was a DI. That made it hard to respond to its prompts

for keyboard input. But a stickier problem arose.

If the keyboard is buffered, when is a byte of input "ready"? The BIOS has to answer this question in its routine for Console Input Status. The simple answer is for the BIOS to report "ready" whenever the keyboard buffer is not empty. That's the answer we were forced back to after trying more clever schemes.

If the simple answer is implemented, then you can't use the keyboard buffer to type ahead of the system. It would be nice if you could type one PIP transfer, say, while PIP was working on the previous one. But if that PIP transfer is to a character device like LST:, PIP checks the console input status after every line of output and aborts if a key has been hit. So you can't type ahead of a PIP to LST: because PIP will quit.

Things get worse. The BDOS polls the console status routine every time it sends a byte of output to the console. If an input byte is ready, the BDOS reads it and tests to see if it is a control-S. If it is not, the BDOS tucks it away in a one-byte buffer. If you try to type ahead of any command that does console output, you will lose one byte of typed-ahead material for every byte of output the program displays. For example, you might start PIP on a disk transfer:

```
A>pip b:=a:somethin.dat
```

and while it is working, you type "stat b:.*" as your next command. But when PIP ends, the two-byte prompt "A>" is typed. Before writing the "A" the BDOS polls the console and reads your typed-ahead "s." Before writing the ">" the BDOS reads your typed-ahead "t" and stores it over the "s." What appears on the console is

```
A>tat b:.*
```

Our first version of buffered input used a smarter solution. The BIOS reported that console input was "ready" only when there was data in the buffer and the first byte of data was a control character or DEL. That worked, in that it kept the BDOS from swallowing typed-ahead bytes. And it allowed us to use control-S to hold output, control-Q to release it, and DEL to cancel a submit file. It would have been the perfect solution if it weren't for the foolishness of certain programmers.

In two major packages that we know of, the code for console input looks like this:

repeat

```
    Test console status
    until console is ready;
        read console input
```

We cannot comprehend why anyone would do this. In a polled system, it is needlessly complicated, but it works. With a buffered keyboard it is a disaster. The program won't read until the console is ready, but the console shouldn't appear ready until a control character is at the head of the buffer. The result is that when one of these programs starts up, the system dies. It took a little time to figure out what was going on, the first time. We patched that program. Then the second one turned up, and we gave up and went back to the simple answer for console input status.

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AUGUSTA, Part III

Recursive Descent Compilers

Compilers are computer programs that perform language translation. Typical compilers translate a high-level language, such as BASIC or Pascal into the machine language of the computer that the program will run on (see Figure 1). Part III of this four-part series describes how one type of compiler works.

Compilers as Language Translators

The compiler doesn't have to translate directly into machine code. Some compilers output assembly language that must be assembled later. Others output an intermediate language that, in turn, is compiled to machine code or interpreted at run-time. There are even compilers that translate from one high-level language into another. For example, the RATFOR language is compiled into Fortran.

Augusta programs are compiled into an intermediate language called "pseudo-code" ("p-code" for short). In effect, the p-codes are the machine language of a hypothetical or pseudo-machine. The compiler uses a technique known as "recursive descent parsing" to perform the translation. Compilers, and in particular recursive-descent compilers, are the subject of this article. Included in this part is the first half of the Augusta compiler source listing (see page 20). Part IV contains the rest of the compiler listing and a detailed description of the compiler's operation.

The Compilation Process

Compilation is divided into three steps: (1) lexical analysis, (2) syntactic analysis, and (3) semantic analysis and code generation. Some compilers make several passes through the source program, perhaps performing one of the above steps on each pass. Augusta is a one-pass

by Edward Mitchell

Edward Mitchell, Box 390145, Mountain View, CA 94039.

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compiler, so all three steps occur simultaneously.

Lexical analysis carves the source program into logical entities called "tokens." To the computer, a source file is just a long sequence of characters. The lexical analyzer breaks the characters into recognizable groups such as "IF", "THEN", "+", "(", "ALPHA", and so on.

Syntactic analysis or "parsing" ensures that the tokens returned by the lexical analyzer appear in the correct order. The syntax diagrams presented in Part I of this series describes the order of tokens for all valid Augusta programs.

When the syntax analyzer recognizes a valid sequence of tokens, the compiler outputs p-codes. During compilation, these three steps are intermingled. When the syntax analyzer needs a new token, it calls the lexical analyzer. When enough tokens have been read so that the parser can recognize a statement or phrase of the language, then the code is generated.

Lexical Analysis

The lexical analyzer reads the source program character by character. Once it recognizes a valid sequence of characters, it returns the entire group as a token.

The classical approach to designing a lexical analyzer is to draw a *state diagram*. The state diagram is really just a special kind of flowchart where circles replace the usual rectangular boxes and diamonds. The diagram describes what the lexical analyzer should do for each character that it sees. Figure 2 (page 14) shows a diagram to separate digits from letters in a stream of characters like "ABC5347 PWX943ZW1F0". That input would produce the following sequence of tokens: "ABC", "5347", "PWX", "943", "ZW", "1", "F", and "0".

Each circle inside the diagram represents a *state*. The diagram begins with state 1. On seeing the letter "A", the lexical analyzer reads a new character from

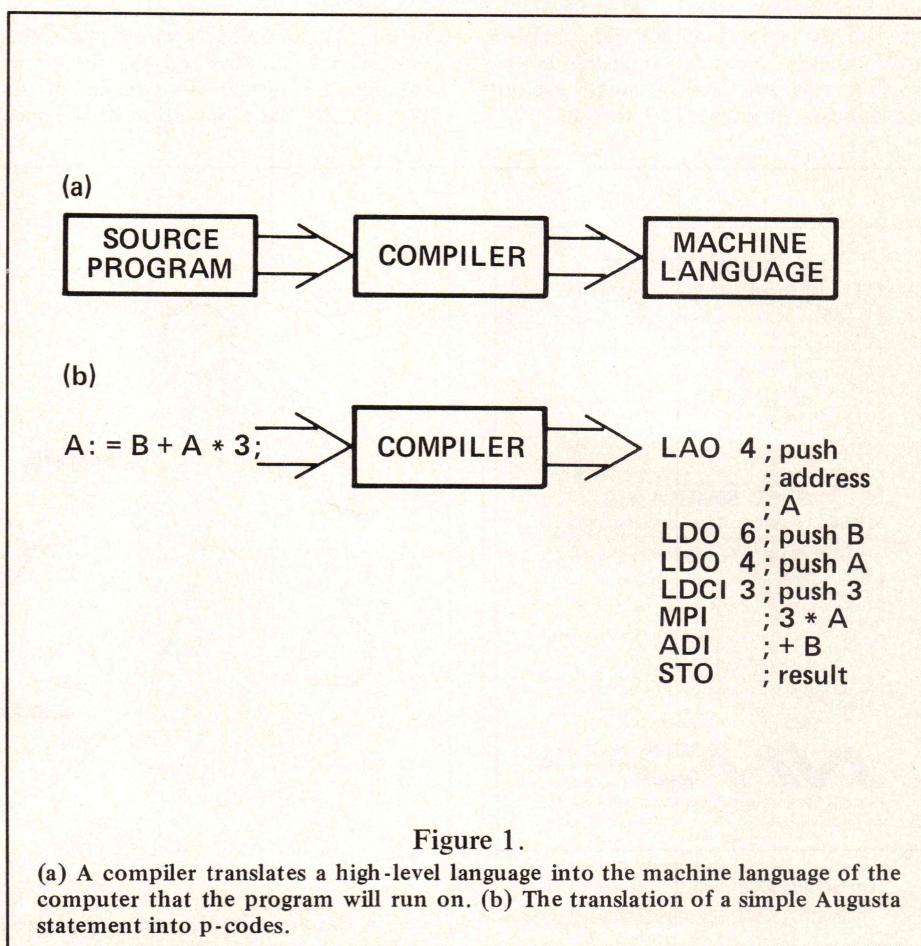


Figure 1.

(a) A compiler translates a high-level language into the machine language of the computer that the program will run on. (b) The translation of a simple Augusta statement into p-codes.

the input and jumps to state 3. The asterisk ("*") indicates that a character is read before entering the new state.

The analyzer remains in state 3 as long as it continues to see an alphabetic character. Upon reading "5" from the input, the analyzer jumps to state 4. At this point, it has identified the first complete token "ABC". Returning to state 1, the analyzer is still looking at a "5" so it jumps to state 2 and processes the digit characters. Listing 1 (page 17) shows how the state diagram can be converted to a BASIC program.

A compiler, such as Augusta, must recognize a large number of tokens — many more than just letters and numbers — for example, all of the arithmetic operators, "+", "-", "*", and "/", plus all of the punctuation symbols like "(", ";", ")", and ":=". Some symbols are harder than others to recognize. For example, "/" by itself is the division symbol. But if it is immediately followed by "=". as in "/=", it becomes the "not equal" relational operator. Similarly, some tokens can be quite lengthy. Take the character string, for example. It begins with a double quote ("") followed by a large number of characters that are terminated by a trailing quote.

Just as flowcharts are unnecessary for coding many programs, state diagrams are not really needed for many lexical analyzers. Instead, a practiced programmer can directly code an appropriate analyzer fairly quickly. To wit, Augusta's lexical analyzer was written in a single evening and appears in lines 1280 through 1775

of Listing 2 (page 20).

The lexical analysis stage of the compiler will often do additional work to aid the rest of the compilation process. Numbers are converted from their character representation (e.g., "197") into an internal numeric form (e.g., TN=197, where TN is a variable). All identifiers (any token beginning with an alphabetic character) are looked up in the keyword table. If the identifier is a keyword like "AND", "PROCEDURE", or "ELSE", then the lexical analyzer marks the token as a keyword. Unquoted lower case letters are converted to upper case.

In most compilers, the lexical analyzer is a subroutine. At each call, it returns the next token from the program source.

Syntax Analysis

The syntax analyzer or "parser" checks that the tokens appear in the proper order. The "proper order" is determined by the language's syntax which is the set of rules that defines the language. The syntax diagrams in Part I (January 1983) of this series show what valid Augusta statements look like.

How does syntax analysis work? There are actually several techniques for parsing a computer program. Augusta uses a method called recursive-descent parsing, which is a variation of "top-down" parsing. The names "recursive descent" and "top down" come from how the parser builds a *parse tree*. (You don't need to understand the theory in great detail. However, it's convenient for following the basic operation of the par-

ser. I'll keep this theoretical section to a minimum.)

A tree is a special type of data structure that is normally depicted upside down as in Figure 3 (page 15). This implies that the "root" is at the top of the tree and the "branches" are at the bottom. Top-down parsing is so named because it produces a tree-like structure from the top and going downwards as it parses a statement. Compare the syntax diagrams in Figure 4 (page 16) to the parse tree in Figure 3(b) (page 15). The parse tree represents the path that the compiler follows through the syntax diagrams as it parses an expression.

To see how syntax analysis works, we'll construct a parser for the simplified expressions described by the syntax diagrams in Figure 4 (page 16). These diagrams recognize the algebraic hierarchy of operations: "*" and "/" come before "+" and "-".

The syntax diagrams are recursive because one of the definitions is defined in terms of itself. EXPR is defined using SE; SE is defined using FACTOR; and in the case of a parenthesized expression, FACTOR is defined using EXPR.

The diagrams can be translated into a set of procedure calls. A simple implementation of the parser written in Augusta is shown in Listing 3 (page 27). Procedure GETTOKEN (not shown in Listing 3) reads a single token of input and places it in the global variable TOKEN. TOKEN is a character containing "C" if a constant was read, "I" if an identifier was read, or

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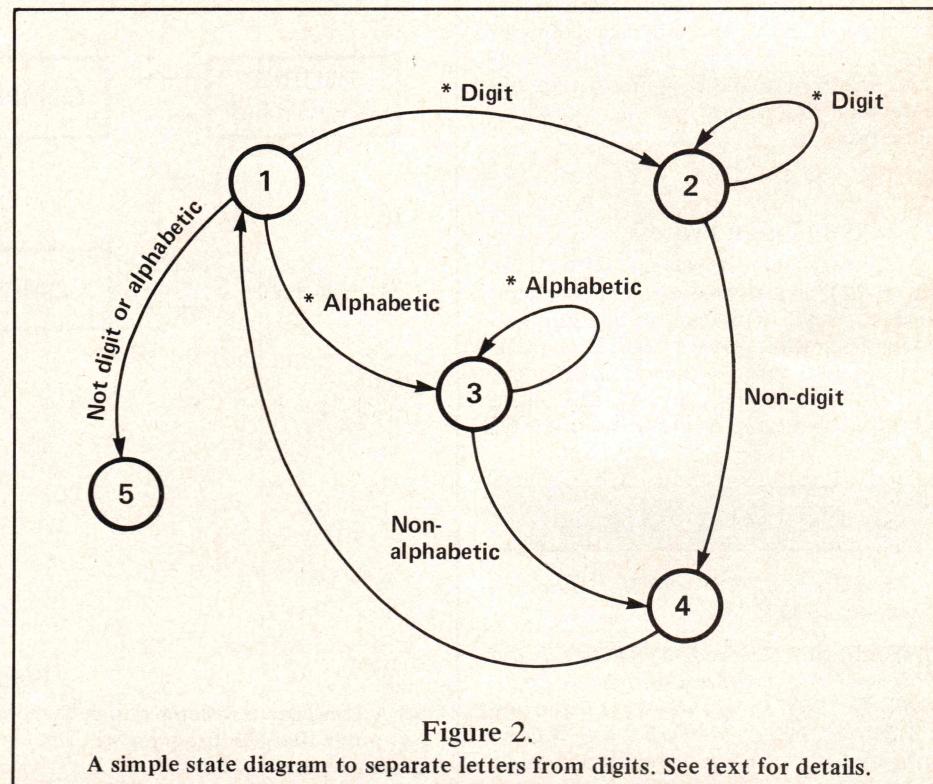


Figure 2.

A simple state diagram to separate letters from digits. See text for details.

one of the punctuation symbols "(", ")", "*", "/", "+", "-".

To parse the expression, 3 * (5+6), GETTOKEN reads the "3". EXPR calls SE to evaluate a "simple expression." SE, in turn, calls FACTOR. Seeing a constant "C", FACTOR reads the next token and returns to SE. Since the token "*" is not one of "+" or "-", control returns to EXPR. But "*" is recognized by EXPR, so EXPR reads the next token, a left parenthesis "(", and calls SE again. SE calls FACTOR where the parenthesis is handled. FACTOR scans past the "(" by calling GETTOKEN and calls EXPR to evaluate the subexpression. After return-

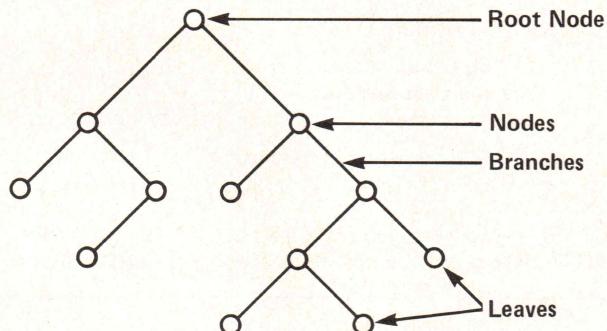
ing from EXPR, FACTOR sees the right parenthesis ")" and returns to SE, which returns to EXPR.

Semantic Analysis

Statements that are syntactically valid may still not make sense. Variable identifiers such as A and B can be multiplied together if and only if A and B are numeric variables. If A and B are strings, then the statement is meaningless. For example,

- (1) A := "String 1";
- (2) B := "String 2";
- (3) C := A * B;

(a)



(b) Parse Tree for $3 + 5 * (8 - 3)$

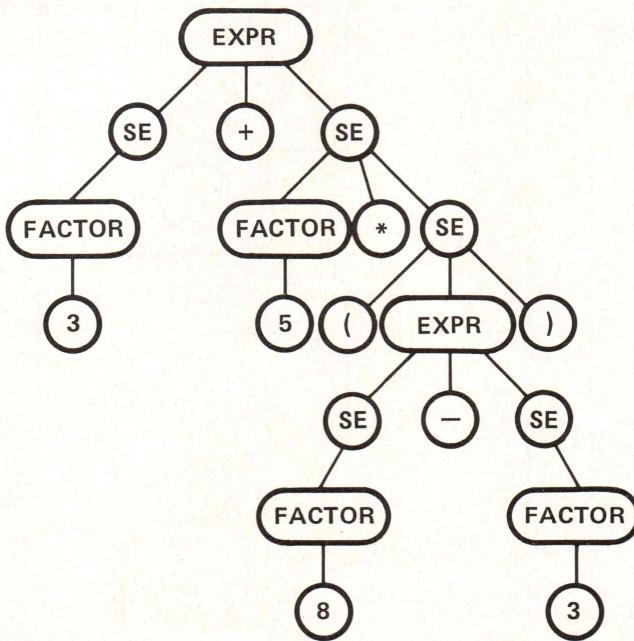


Figure 3.

(a) shows a data structure called a *tree*. Because the tree is depicted upside down, the root is on the top. (b) shows a *parse tree* produced by parsing a simple expression according to the syntax diagrams in Figure 4. Parse trees are the basis of recursive-descent parsing, the technique used by the Augusta compiler.

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Statement 3 is syntactically correct but semantically wrong.

Semantic analysis attempts to infer meaning from the statement; e.g., C is to be assigned the quantity of A multiplied by B. Once the compiler understands the statement, it can generate the corresponding p-code translation.

Syntax analysis only cares about finding the correct tokens, such as a constant, an identifier, a keyword, or so on. Semantic analysis, on the other hand, examines the value of the tokens. For example, the identifier "ALPHA" must be looked up in a symbol table to see if it is defined. If it is defined, then the compiler must determine that it can be used correctly in the current context.

Code Generation

Augusta generates code as it does the parsing. An example, using the simple expression parser, shows how this is done.

Let

X := 5 * 4;

be a simple assignment statement. The compiler, upon seeing an identifier followed by ":" begins parsing the assignment statement. The symbol "X" is looked up in the symbol table, which

tells the compiler that X is an integer variable stored at global offset 10. Augusta generates

LDA 10 ; Load address of variable X

The compiler next scans across the expression. But note that because Augusta generates code for a stack machine, both operands (the "5" and the "4") must be pushed on the stack before performing the multiplication. This means that the following sequence of p-codes will be created:

LDCI 5 ; Load constant 5 on to the
; stack
LDCI 4 ; Load constant 4 on to the
; stack
MPI ; Pop top 2 words, multiply
; together, and push result

Finally, the top of stack value is stored at X, by emitting the instruction

STO ; Store top of stack value in
; to the address contained in
; the next word on the stack

Listing 4 (page 28) shows the simple expression parser modified to generate p-codes.

Two new procedures, EMITBYTE and EMITWORD, have been created. The

parsing routines call EMITBYTE and EMITWORD when they need to output p-codes or p-code parameters. For example, the LDCI opcode is output with the following calls:

EMITBYTE (1) ; - LDCI p-code
; value
EMITWORD (5) ; - constant 5 fol-
; lows LDCI

Code Generated for FOR Loops

The previous section gave an overview of the code generation scheme. We will now look at some specific statements and see what the corresponding p-code translation looks like.

The FOR loop has the form

FOR <Identifier> IN
 <Starting Value> ..
 <Ending Value>
LOOP
 <Sequence of Statements>
END LOOP;

For example,

FOR I IN 1 .. 10
LOOP
 PUTINT(I);
 NEWLINE;
END LOOP;



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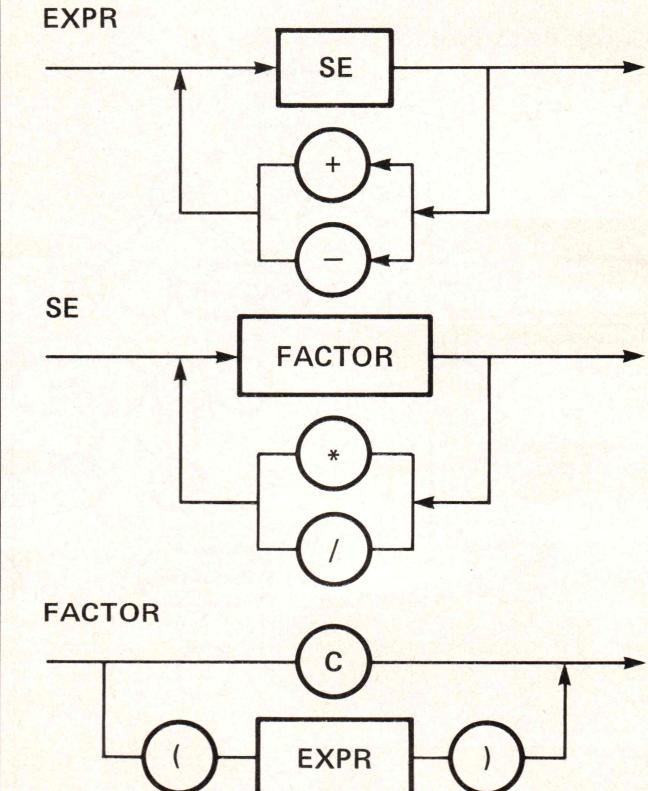


Figure 4.
Syntax diagrams for a simple expression parser.

prints the integers 1 through 10.

For the loop above, the compiler must emit code to set I:=1 at the beginning of the loop to check to determine if I is greater than 10 and to increment I each time the loop is executed. Listing 5 (page 29) shows sample code for the loop shown above.

When the FJP instruction at (1) in Listing 5 is emitted, the compiler does not yet know where the program should jump to. Until the code for the enclosed sequence of statements is generated, the compiler doesn't know how many bytes the FJP instruction should jump past. Instead, it must remember where the FJP instruction is located, generate the remaining code, and then go back and fix the FJP instruction so that it will jump to the correct location. This technique is called "back patching" and is used extensively when compiling long sequences of IF-THEN-ELSEIF and CASE statements.

The WHILE Statement

The WHILE statement is translated into a conditional test, followed by the enclosed statements and ending with a jump back to the conditional test. Listing 6(b) (pages 28 and 29) shows the code produced for WHILE statement shown in 6(a) (page 28).

IF-THEN Statements

Listing 7(a) (page 29) shows sample code for a simple IF-THEN-ELSE statement. Back patching is done for all the forward jump instructions, which includes both the FJP and UJP p-codes in Listing 7(b) (page 29).

"Short circuit" boolean expressions, whether used in assignment statement or as part of an IF-THEN test, use the FJP instruction to jump over portions of the expression code. Listing 8(b) (page 30) shows the code generated for the statements in 8 (a) (page 30).

The CASE Statement

The CASE statement is unusual in that it uses the XJP (indirect jump) opcode to branch to the appropriate section of code. XJP is a seven-byte opcode, having the format

XJP w1, w2, w3

where w1, w2, and w3 are 16-bit words. XJP uses the value on the top of the stack as an index into a jump table that appears after the code for each of the cases. w1 and w2 are the minimum and the maximum case selections, respectively. For example, in a CASE statement like

```
CASE I IS
WHEN 7 => ...
WHEN 11 => ...
WHEN 19 => ...
WHEN 23 => ...
END CASE;
```

the minimum selector, w1, is 7, and the maximum selector, w2, is 23. Word w3 is the offset to the indirect jump table. Listing 9 (page 30) shows the basic format for the case statement.

The jump table has the following format,

```
→ UJP      instruction
      address of OTHERS condition
      address of case w1
      address of case w1+1
      address of case w1+2
      :
      address of case w2-1
      address of case w2
```

For the CASE statement shown previously, with selectors 7, 11, 19, and 23, an entry is made in the jump table corresponding to each selector. But the jump table has sixteen entries (maximum selector minus minimum selector or 23 - 7) and so only

four entries are accounted for. All other, unused selectors are filled with the address of the UJP instruction at the beginning of the table. Then, when a case value for I is not one of 7, 11, 19, or 23, the program does an automatic jump to the OTHERS clause. The first entry in the table is the address of the code to handle the OTHERS condition. In the event that there is no OTHERS clause, this entry simply jumps to the first instruction after the jump table.

In the July Issue

Part IV contains the final half of the Augusta compiler source listing. It concludes with some personal comments on the subjects of Ada, the rationale for creating Augusta, the DoD, and Microsoft BASIC.

DDJ

(Listing One below)

(Listings 2 through 9 on pages 20 through 30)

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Augusta Part III

Listing One

```
10 'SIMPLE LEXICAL ANALYZER
20 '
30 DIGITS$="0123456789"
40 ALPHA$="ABCDEFGHIJKLMNOPQRSTUVWXYZ"
50 INPUT "ENTER STRING: ", S$
60 P=1          ' P POINTS TO NEXT CHAR TO READ
70 GOSUB 800    ' READ CHAR INTO CH$
100 'STATE 1 - DECIDE IF ALPHABETIC OR DIGIT
110 IF INSTR(DIGITS$,CH$) THEN GOSUB 800: GOTO 200
120 IF INSTR(ALPHA$,CH$) THEN GOSUB 800: GOTO 300
130 GOTO 500
200 'STATE 2 - READ IN DIGITS
210 IF INSTR(DIGITS$,CH$) THEN GOSUB 800: GOTO 200
220 GOTO 400
300 'STATE 3 - READ IN ALPHABETIC CHARS
310 IF INSTR(ALPHA$,CH$) THEN GOSUB 800: GOTO 300
320 GOTO 400
400 'STATE 4 - OUTPUT THE EXTRACTED TOKEN
410 PRINT TOKEN$
420 TOKEN$=""
430 GOTO 100
500 'STATE 5 - INVALID CHARACTER STOPS
510 STOP
800 'READ CHARACTER OF INPUT
810 IF P>LEN(S$) THEN CH$=".": RETURN
820 TOKEN$=TOKEN$+CH$    ' ACCUMULATE A TOKEN
830 CH$=MID$(S$,P,1)     ' GET NEXT CHARACTER
840 P=P+1
850 RETURN
```

End Listing One

(Listing Two begins on page 20)

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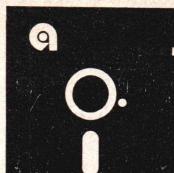
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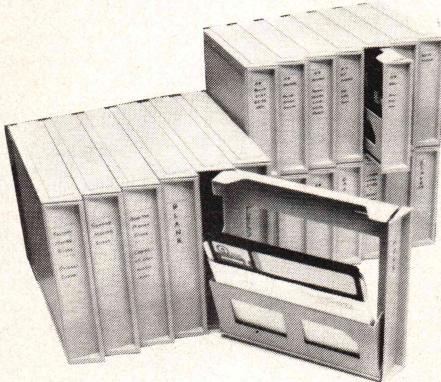


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Augusta Part III Listing Two

Source listing of the Augusta Compiler
(Part 1 of 2 parts).

(Text begins on page 13)

Augusta compiler, copyright © 1983 by Computer Linguistics. Permission is granted to individuals to use the compiler for their personal use. Unauthorized distribution is expressly prohibited. Augusta is a trademark of Computer Linguistics.

```

BAR=15;
ID=16;
SC=17;
COMM=18;
SEMICOLON=19;
COLON=20;
EQGT=21;
EQEQ=22;
PDI=23;
PDV=24;
PINQ=25;
PGEQ=26;
PLEQ=27;
PSLD=61;
PINC=80;
PDEC=81;
PIESI=28;
KARRAY=29;
PKBEGIN=29;
KCASE=30;
KCONST=31;
KDECLARE=32;
KELSE=33;
KELSEIF=34;
KEND=35;
KEXT=36;
KFOR=37;
KFUNC=38;
KIF=39;
KIN=40;
KIS=41;
KL0OP=42;
KLAST=43;
KLEN=44;
KM0D=45;
KN0T=46;
KNULL=47;
KOF=48;
KQR=49;
KOTHERS=50;
KOUT=51;
KRET=54;
KREVERSE=55;
KTHEN=56;
KWHEN=57;
KWHILE=58;

1135 SC=17;
1135 SEMICOLON=19;
1135 COLON=20;
1135 EQGT=21;
1135 EQEQ=22;
1135 PDI=23;
1135 PDV=24;
1135 PINQ=25;
1135 PGEQ=26;
1135 PLEQ=27;
1135 PSLD=61;
1135 PINC=80;
1135 PDEC=81;
1140 KAND=27;
1140 KARRY=28;
1140 KBEGIN=29;
1140 KCASE=30;
1140 KCONST=31;
1140 KDECLARE=32;
1140 KELSE=33;
1140 KELSEIF=34;
1140 KEND=35;
1140 KEXT=36;
1140 KFOR=37;
1140 KFUNC=38;
1140 KIF=39;
1140 KIN=40;
1140 KIS=41;
1140 KL0OP=42;
1140 KLAST=43;
1140 KLEN=44;
1140 KM0D=45;
1140 KN0T=46;
1140 KNULL=47;
1140 KOF=48;
1140 KQR=49;
1140 KOTHERS=50;
1140 KOUT=51;
1140 KRET=54;
1140 KREVERSE=55;
1140 KTHEN=56;
1140 KWHEN=57;
1140 KWHILE=58;

1145 INPUT"Source File ? ",S$;
1145 PC6P=41;
1145 PCSP=42;
1145 PRET=43;
1145 PM0D1=45;
1145 PCIP=46;
1145 PRNP=47;
1145 PEOP=45;
1145 PSLDCN1=63;
1145 PIXA=48;

1150 INPUT"Code File ? ",C$;
1150 OPENR">#1,C$,128;
1150 CLOSE 1;
1150 KILL C$;
1150 OPENR">#1,C$,128;
1150 R0=16;
1150 M0=R0;
1150 IF T1$="Y" THEN P1ST=-1;
1150 LPRINT LPT;
1150 GOSUB 1290;
1150 PRINT FRE(" ");" Bytes for Symbols";
1150 GOSUB 1980;
1150 PUT #1, R0;

1160 INPUT"Listng(Y/CR)? ",T1$;
1160 IF T1$="Y" THEN P1ST=-1;
1160 KPROGMA=52;
1160 KPROC=53;
1160 KOUT=51;
1160 KRET=54;
1160 KREVERSE=55;
1160 KTHEN=56;
1160 KWHEN=57;
1160 KWHILE=58;

1170 PSLDO=57;
1170 PSLAO=58;
1170 PSLL4=59;
1170 PSLDL0=49;
1170 PSLDL1=60;

1175 RETURN
1220 RETURN

```

```

FIELD #1, 2 AS T1$, 2 AS T2$, 2 AS T3$,
2 AS D$, 2 AS S$;
1070 LSET T1$=MK1$(GC);
LSET T2$=MK1$(M0);
LSET T3$=MK1$(PROC);
LSET D$=MK1$(0);
LSET S$=MK1$(1113)

1080 PUT #1,1;
FIELD #1,128 AS D$;
FOR I=1 TO MB;
IF B(I)<>0 AND B(I)<>RO THEN LSET
D$=B$(I);
PUT #1, B(I)
NEXT I;
CLOSE 1;
PRINT;
PRINT "Compiled OK";
PRINT LN;"Lines.";6C-1920;"Bytes";
GOTO 32767

1090 INIT
QUOTE$=CHR$(34);
LEXCH$=ALF+$+DIG$+"#"+"--< / : ; ) , "+"
QUOTE$+"#!" +CHR$(3)+CHR$(96)+CHR$(9):
CLST=-1
1130 SQUOTE=0;
EQ1=1;
C=2;
LP=3;
RP=4;
MUL=5;
DIV=6;
ADD=7;
SURT=8;
LES=9;
LE0=10;
GT=11;
GE0=12;
EQ=13;
NEQ=14;

1160 ADDOPS$=CHR$(ADD)+CHR$(SUBT):
MULOPS$=CHR$(MUL)+CHR$(DIV)+CHR$(KMD):
LOGICALOPS$=CHR$(KAND)+CHR$(KOR):
RELOPS$=CHR$(LES)+CHR$(SUBT)+CHR$(KNOT):
CHR$(GEQ)+CHR$(EQ)+CHR$(NEQ)
1170 DECLPARTS$=CHR$(ID)+CHR$(KPROC)+
CHR$(KFUNC)+CHR$(KPRAGMA)
1180 STMTS$=CHR$(WHILE)+CHR$(KFOR)+CHR$(KLOOP)+CHR$(KDECLARE)+CHR$(KBEGIN)+CHR$(KEXIT)+CHR$(KRET)+CHR$(KTF)
1185 STMTS$=STMTS$+CHR$(KCASE)+CHR$(KNULL)+CHR$(ID)+CHR$(KPRAGMA)
1190 LN=1;
EQ1=0;
LN=0;
CPROC=0;
PROC=0;
GD=1920;
VLOC=VARPTR(V):
VLOC1=VLOC+1;
ISTR=0;
TINT=1;
TCHR=2;
TBOL=4;
FMS7=14
1200 PLDCI=1;
PLDL=2;
PLLA=3;
PLDB=4;
PLDO=5;
PLAO=6;
PNUP=7;
PLDD=8;
PLDA=9;
PPQP=10;
PS0=11;
PSIND0=12;
PLCA=13;
PSAS=14;
PAND=16;
PORG=17;

```

```

1230 'OPEN SRC
1240 SI=SI+1;
OPEN "I",#SI,$$;
RETURN

1165 UNARYOPS$=CHR$(ADD)+CHR$(SUBT)+CHR$(KNOT):
RELOPS$=CHR$(LES)+CHR$(SUBT)+CHR$(GT)+CHR$(EQ):
CHR$(GEQ)+CHR$(EQ)+CHR$(NEQ)
1250 'LC TO UC
1260 IF INSTR(LC$,CH$) THEN CH$=CHR$(ASC(CH$)-32)
1270 RETURN

1280 'GETLINE
1290 LN=LN+1;
IF EOF(SI) THEN CLOSE SI;
SI=SI-1;
IF SI>1 AND PLST THEN LPRINT
TAB(26);"END OF INCLUDE"
1300 IF SI=1 THEN EQ1=-1;
RETURN

1310 LINE INPUT #SI, BUF$
1320 IF PLST=0 GOTO 1330 ELSE LPRINT
USING "***** ##### ##### ##### ";LN,
CPROC,CP,OFST;;
LPRINT BUF$;
1325 IF (LN MOD 60)=0 THEN LPRINT CHR$(12);
LPRINT;
LPRINT
1330 IF CLST>0 THEN PRINT BUF$ ELSE IF
(LN AND 63)=63 THEN PRINT LN;"...";
1340 IF LEN(BUF$)=0 GOTO 1290 ELSE
BUF$=BUF$+CHR$(5);
B=1;
WHILE MID$(BUF$,B,1)=" ";
B=B+1;
WEND;
CH$=MID$(BUF$,B,1);
B=B+1;
RETURN

1350 QUOTE$=CHR$(34);
LEXCH$=ALF+$+DIG$+"#"+"--< / : ; ) , "+"
QUOTE$+"#!" +CHR$(3)+CHR$(96)+CHR$(9):
CLST=-1
1110 INIT
1120
PLDL=2;
PLLA=3;
PLDB=4;
PLDO=5;
PLAO=6;
PNUP=7;
PLDD=8;
PLDA=9;
PPQP=10;
PS0=11;
PSIND0=12;
PLCA=13;
PSAS=14;
PAND=16;
PORG=17;

```

Augusta Part III

Listing Two

(Continued, text begins on page 13)

```

1795 D$=S$:
1796 LOC1=0;
1797 LOC2=0;
1798 V=0;
1799 VLOC1=0;
1800 VLOC=0;
1801 TN=0;
1802 TT$=S$;
1803 HASH=0;
1804 ID$=S$;
1805 RBUF$=S$;
1806 T1=0;
1807 T2=0;

1808 NKEY=33;
1809 SSP=1;
1810 MB=3;
1811 FOR I=0 TO MB;
1812 B$(I)=SPACE$(128);
1813 B(I)=0;
1814 NEXT I

1815 OPEN "I",#1,"KEYWORDS.TXT";
1816 LINE INPUT #1,LCS$;
1817 T1=1;
1818 WHILE T1>0;
1819 INPUT #1,T1;
1820 LP$=LP$+CHR$(T1);
1821 WEND

1822 INPUT#1,DIG$,HDIG$,ALF$,LCS$,ANS$;
1823 FOR I=1 TO 26;
1824 INPUT #1,MAP(I);
1825 NEXT I

1826 I=1;
1827 LOC1=0;
1828 LOC2=0;
1829 V=0;
1830 VLOC1=0;
1831 VLOC=0;
1832 TN=0;
1833 TT$=S$;
1834 HASH=0;
1835 ID$=S$;
1836 RBUF$=S$;
1837 T1=0;
1838 T2=0;

1839 LOC1=0;
1840 LOC2=0;
1841 V=0;
1842 VLOC1=0;
1843 VLOC=0;
1844 TN=0;
1845 TT$=S$;
1846 HASH=0;
1847 ID$=S$;
1848 RBUF$=S$;
1849 T1=0;
1850 T2=0;

1851 LOC1=0;
1852 LOC2=0;
1853 V=0;
1854 VLOC1=0;
1855 VLOC=0;
1856 TN=0;
1857 TT$=S$;
1858 HASH=0;
1859 ID$=S$;
1860 RBUF$=S$;
1861 T1=0;
1862 T2=0;

1863 LOC1=0;
1864 LOC2=0;
1865 V=0;
1866 VLOC1=0;
1867 VLOC=0;
1868 TN=0;
1869 TT$=S$;
1870 HASH=0;
1871 ID$=S$;
1872 RBUF$=S$;
1873 T1=0;
1874 T2=0;

1875 LOC1=0;
1876 LOC2=0;
1877 V=0;
1878 VLOC1=0;
1879 VLOC=0;
1880 TN=0;
1881 TT$=S$;
1882 HASH=0;
1883 ID$=S$;
1884 RBUF$=S$;
1885 T1=0;
1886 T2=0;

1887 LOC1=0;
1888 LOC2=0;
1889 V=0;
1890 VLOC1=0;
1891 VLOC=0;
1892 TN=0;
1893 TT$=S$;
1894 HASH=0;
1895 ID$=S$;
1896 RBUF$=S$;
1897 T1=0;
1898 T2=0;

1899 LOC1=0;
1900 LOC2=0;
1901 V=0;
1902 VLOC1=0;
1903 VLOC=0;
1904 TN=0;
1905 TT$=S$;
1906 HASH=0;
1907 ID$=S$;
1908 RBUF$=S$;
1909 T1=0;
1910 T2=0;

1911 LOC1=0;
1912 LOC2=0;
1913 V=0;
1914 VLOC1=0;
1915 VLOC=0;
1916 TN=0;
1917 TT$=S$;
1918 HASH=0;
1919 ID$=S$;
1920 RBUF$=S$;
1921 T1=0;
1922 T2=0;

1923 LOC1=0;
1924 LOC2=0;
1925 V=0;
1926 VLOC1=0;
1927 VLOC=0;
1928 TN=0;
1929 TT$=S$;
1930 HASH=0;
1931 ID$=S$;
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1933 T1=0;
1934 T2=0;

1935 LOC1=0;
1936 LOC2=0;
1937 V=0;
1938 VLOC1=0;
1939 VLOC=0;
1940 TN=0;
1941 TT$=S$;
1942 HASH=0;
1943 ID$=S$;
1944 RBUF$=S$;
1945 T1=0;
1946 T2=0;

1947 LOC1=0;
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1949 V=0;
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1951 VLOC=0;
1952 TN=0;
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1955 ID$=S$;
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1959 LOC1=0;
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1963 VLOC=0;
1964 TN=0;
1965 TT$=S$;
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1975 VLOC=0;
1976 TN=0;
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1982 T2=0;

1983 LOC1=0;
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1987 VLOC=0;
1988 TN=0;
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1990 HASH=0;
1991 ID$=S$;
1992 RBUF$=S$;
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1994 T2=0;

1995 LOC1=0;
1996 LOC2=0;
1997 V=0;
1998 VLOC1=0;
1999 VLOC=0;
2000 TN=0;
2001 TT$=S$;
2002 HASH=0;
2003 ID$=S$;
2004 RBUF$=S$;
2005 T1=0;
2006 T2=0;

2007 LOC1=0;
2008 LOC2=0;
2009 V=0;
2010 VLOC1=0;
2011 VLOC=0;
2012 TN=0;
2013 TT$=S$;
2014 HASH=0;
2015 ID$=S$;
2016 RBUF$=S$;
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2018 T2=0;

2019 LOC1=0;
2020 LOC2=0;
2021 V=0;
2022 VLOC1=0;
2023 VLOC=0;
2024 TN=0;
2025 TT$=S$;
2026 HASH=0;
2027 ID$=S$;
2028 RBUF$=S$;
2029 T1=0;
2030 T2=0;

2031 LOC1=0;
2032 LOC2=0;
2033 V=0;
2034 VLOC1=0;
2035 VLOC=0;
2036 TN=0;
2037 TT$=S$;
2038 HASH=0;
2039 ID$=S$;
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2042 T2=0;

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2044 LOC2=0;
2045 V=0;
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2047 VLOC=0;
2048 TN=0;
2049 TT$=S$;
2050 HASH=0;
2051 ID$=S$;
2052 RBUF$=S$;
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2054 T2=0;

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2056 LOC2=0;
2057 V=0;
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2059 VLOC=0;
2060 TN=0;
2061 TT$=S$;
2062 HASH=0;
2063 ID$=S$;
2064 RBUF$=S$;
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2066 T2=0;

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2068 LOC2=0;
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2071 VLOC=0;
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2078 T2=0;

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2081 V=0;
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2084 TN=0;
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2092 LOC2=0;
2093 V=0;
2094 VLOC1=0;
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2096 TN=0;
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2098 HASH=0;
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2114 T2=0;

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2123 ID$=S$;
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2126 T2=0;

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2129 V=0;
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2131 VLOC=0;
2132 TN=0;
2133 TT$=S$;
2134 HASH=0;
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2136 RBUF$=S$;
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2138 T2=0;

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2143 VLOC=0;
2144 TN=0;
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2146 HASH=0;
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2150 T2=0;

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2152 LOC2=0;
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2155 VLOC=0;
2156 TN=0;
2157 TT$=S$;
2158 HASH=0;
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2160 RBUF$=S$;
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2170 HASH=0;
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2176 LOC2=0;
2177 V=0;
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2179 VLOC=0;
2180 TN=0;
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2182 HASH=0;
2183 ID$=S$;
2184 RBUF$=S$;
2185 T1=0;
2186 T2=0;

2187 LOC1=0;
2188 LOC2=0;
2189 V=0;
2190 VLOC1=0;
2191 VLOC=0;
2192 TN=0;
2193 TT$=S$;
2194 HASH=0;
2195 ID$=S$;
2196 RBUF$=S$;
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2198 T2=0;

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2203 VLOC=0;
2204 TN=0;
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2206 HASH=0;
2207 ID$=S$;
2208 RBUF$=S$;
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2210 T2=0;

2211 LOC1=0;
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2213 V=0;
2214 VLOC1=0;
2215 VLOC=0;
2216 TN=0;
2217 TT$=S$;
2218 HASH=0;
2219 ID$=S$;
2220 RBUF$=S$;
2221 T1=0;
2222 T2=0;

2223 LOC1=0;
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2225 V=0;
2226 VLOC1=0;
2227 VLOC=0;
2228 TN=0;
2229 TT$=S$;
2230 HASH=0;
2231 ID$=S$;
2232 RBUF$=S$;
2233 T1=0;
2234 T2=0;

2235 LOC1=0;
2236 LOC2=0;
2237 V=0;
2238 VLOC1=0;
2239 VLOC=0;
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2253 TT$=S$;
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2263 VLOC=0;
2264 TN=0;
2265 TT$=S$;
2266 HASH=0;
2267 ID$=S$;
2268 RBUF$=S$;
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2270 T2=0;

2271 LOC1=0;
2272 LOC2=0;
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2274 VLOC1=0;
2275 VLOC=0;
2276 TN=0;
2277 TT$=S$;
2278 HASH=0;
2279 ID$=S$;
2280 RBUF$=S$;
2281 T1=0;
2282 T2=0;

2283 LOC1=0;
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2285 V=0;
2286 VLOC1=0;
2287 VLOC=0;
2288 TN=0;
2289 TT$=S$;
2290 HASH=0;
2291 ID$=S$;
2292 RBUF$=S$;
2293 T1=0;
2294 T2=0;

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2297 V=0;
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2299 VLOC=0;
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2306 T2=0;

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2311 VLOC=0;
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2313 TT$=S$;
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2335 VLOC=0;
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2347 VLOC=0;
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2361 TT$=S$;
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2376 RBUF$=S$;
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2383 VLOC=0;
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2393 V=0;
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2395 VLOC=0;
2396 TN=0;
2397 TT$=S$;
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2403 LOC1=0;
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2419 VLOC=0;
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2421 TT$=S$;
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2510 T2=0;

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2515 VLOC=0;
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2522 T2=0;

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2527 VLOC=0;
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2774 T2=0;

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2792 TN=0;
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2814 VLOC1=0;
2815 VLOC=0;
2816 TN=0;
2817 TT$=S$;
2818 HASH=0;
2819 ID$=S$;
2820 RBUF$=S$;
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2822 T2=0;

2823 LOC1=0;
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2825 V=0;
2826 VLOC1=0;
2827 VLOC=0;
2828 TN=0;
2829 TT$=S$;
2830 HASH=0;
2831 ID$=S$;
2832 RBUF$=S$;
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2834 T2=0;

2835 LOC1=0;
2836 LOC2=0;
2837 V=0;
2838 VLOC1=0;
2839 VLOC=0;
2840 TN=0;
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2842 HASH=0;
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2844 RBUF$=S$;
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2846 T2=0;

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2849 V=0;
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2851 VLOC=0;
2852 TN=0;
2853 TT$=S$;
2854 HASH=0;
2855 ID$=S$;
2856 RBUF$=S$;
2857 T1=0;
2858 T2=0;

2859 LOC1=0;
2860 LOC2=0;
2861 V=0;
2862 VLOC1=0;
2863 VLOC=0;
2864 TN=0;
2865 TT$=S$;
2866 HASH=0;
2867 ID$=S$;
2868 RBUF$=S$;
2869 T1=0;
2870 T2=0;

2871 LOC1=0;
2872 LOC2=0;
2873 V=0;
2874 VLOC1=0;
2875 VLOC=0;
2876 TN=0;
2877 TT$=S$;
2878 HASH=0;
2879 ID$=S$;
2880 RBUF$=S$;
2881 T1=0;
2882 T2=0;

2883 LOC1=0;
2884 LOC2=0;
2885 V=0;
2886 VLOC1=0;
2887 VLOC=0;
2888 TN=0;
2889 TT$=S$;
2890 HASH=0;
2891 ID$=S$;
2892 RBUF$=S$;
2893 T1=0;
2894 T2=0;

2895 LOC1=0;
2896 LOC2=0;
2897 V=0;
2898 VLOC1=0;
2899 VLOC=0;
2900 TN=0;
2901 TT$=S$;
2902 HASH=0;
2903 ID$=S$;
2904 RBUF$=S$;
2905 T1=0;
2906 T2=0;

2907 LOC1=0;
2908 LOC2=0;
2909 V=0;
2910 VLOC1=0;
2911 VLOC=0;
2912 TN=0;
2913 TT$=S$;
2914 HASH=0;
2915 ID$=S$;
2916 RBUF$=S$;
2917 T1=0;
2918 T2=0;

2919 LOC1=0;
2920 LOC2=0;
2921 V=0;
2922 VLOC1=0;
2923 VLOC=0;
2924 TN=0;
2925 TT$=S$;
2926 HASH=0;
2927 ID$=S$;
2928 RBUF$=S$;
2929 T1=0;
2930 T2=0;

2931 LOC1=0;
2932 LOC2=0;
2933 V=0;
2934 VLOC1=0;
2935 VLOC=0;
2936 TN=0;
2937 TT$=S$;
2938 HASH=0;
2939 ID$=S$;
2940 RBUF$=S$;
2941 T1=0;
2942 T2=0;

2943 LOC1=0;
2944 LOC2=0;
2945 V=0;
2946 VLOC1=0;
2947 VLOC=0;
2948 TN=0;
2949 TT$=S$;
2950 HASH=0;
2951 ID$=S$;
2952 RBUF$=S$;
2953 T1=0;
2954 T2=0;

2955 LOC1=0;
2956 LOC2=0;
2957 V=0;
2958 VLOC1=0;
2959 VLOC=0;
2960 TN=0;
2961 TT$=S$;
2962 HASH=0;
2963 ID$=S$;
2964 RBUF$=S$;
2965 T1=0;
2966 T2=0;

2967 LOC1=0;
2968 LOC2=0;
2969 V=0;
2970 VLOC1=0;
2971 VLOC=0;
2972 TN=0;
2973 TT$=S$;
2974 HASH=0;
2975 ID$=S$;
2976 RBUF$=S$;
2977 T1=0;
2978 T2=0;

2979 LOC1=0;
2980 LOC2=0;
2981 V=0;
2982 VLOC1=0;
2983 VLOC=0;
2984 TN=0;
2985 TT$=S$;
2986 HASH=0;
2987 ID$=S$;
2988 RBUF$=S$;
2989 T1=0;
2990 T2=0;

2991 LOC1=0;
2992 LOC2=0;
2993 V=0;
2994 VLOC1=0;
2995 VLOC=0;
2996 TN=0;
2997 TT$=S$;
2998 HASH=0;
2999 ID$=S$;
3000 RBUF$=S$;
3001 T1=0;
3002 T2=0;

```

```

WEND:
IF LEN(S$)>8 THEN S$=LEFT$(S$,8)
1480 ID$=S$+SPACE$(8-LEN(S$));
GOSUB 1360;
RETURN
1490 : DIGITS
1500 TN=0;
I1=10
1510 WHILE INSTR(IDIG$,CH$):
TN=TN*I1+INSTR(HD16$,CH$)-1;
GOSUB 1360;
WEND
1515 IF CH$="#" THEN I1=TN;
TN=0;
GOSUB 1360;
GOTO 1510 ELSE T=C;
RETURN
1520 T=ADD:
GOSUB 1360;
RETURN
1530 T=SUB:
GOSUB 1360;
IF CH$="-" THEN GOSUB 1280;
QDDB=R;
RETURN ELSE RETURN
1540 T=SEMICOLON:
GOSUB 1360;
RETURN
1550 T=COMMA:
GOSUB 1360;
RETURN
1560 T=LP;
GOSUB 1360;
RETURN
1570 T=RP;
GOSUB 1360;
RETURN
1580 T=ROT;
GOSUB 1360;

```

```

1360 GOSUB 1360;
RETURN
1750 GOSUB 1360;
IF CH$<>"." THEN E=11;
GOTO 5020
1760 GOSUB 1360;
GOSUB 1930;
TN=ASC(MID$(S$,2,1));
T=CH;
RETURN
1770 T=A$;
GOSUB 1360;
RETURN
1775 GOSUB 1360;
QDDB=R;
RETURN
1780 'READ DATA
1790 CH$=" ";
B=0;
LB=0;
ANS$=CH$;
LC$=CH$;
S$=CH$;
T=0;
T0=0;
X=0;
SP=0;
TSP=0;
LEXCH$=S$;
CP=0;
CB=0;
W=0;
I=0;
R2=0;
R1=0;
T3=0;
R0=16
1860 IF EOF(1) GOTO 1880 ELSE INPUT #1,T$;
IF LEN(T$)>8 THEN T$=LEFT$(T$,8)
1870 T$=T$+SPACE$(8-LEN(T$));
KEY$(I)=T$;
I=I+1;
GOTO 1860
1880 CLOSE 1;
KEY$(0)=" ";
KEY$(NKEY)=" ";
RETURN
1890 'LOOKUP KEYWORD
1900 HASH=MAP(INSTR(ALF$,LEFT$(ID$,1)))
1910 IF KEY$(HASH)=ID$ THEN T=HASH+26
ELSE IF ASC(KEY$(HASH))<ASC(ID$) THEN
T=ID ELSE HASH=HASH+1;
GOTO 1910
1920 RETURN
1930 'GET $
1940 S$=MID$(BUF$,OLDR-1,B-OLDR);
RETURN
1950 IF T=1 THEN RETURN
1955 E=4;
GOSUB 5110;
PRINT "REENTER+ ";
LINE INPUT T$;
BUF$=LEFT$(BUF$,B-1)+T$+CHR$(31);
GOSUB 1360;
GOSUB 1400;
GOTO 1950
1960 IF T=0 THEN GOSUB 1400;
RETURN ELSE GOSUB 1950;
GOSUB 1400;
RETURN

```

Augusta Part III

Listing Two

(Continued, text begins on page 13)

```

2235 T1$=MID$(T1$, 9);
    OFST=OFST-2;
    WEND
2240 RETURN

1970 'COMPILEATION
1980 GOSUB 2770
1990 IF T=KPROC THEN GOSUB 1400:
    GOSUB 2010;
    TO=SEMICOLON;
    GOSUB 1950
2000 RETURN

2010 'PARSE PROC
2020 GOSUB 5200
2030 KIND=2;
    PROC=PROC+1;
    CPROC=PROC;
    ADDR=PROC;
    X=ADDR;
    GOSUB 4280;
    GOSUB 3850;
    GOSUB 1400
2040 OFST=-FMSZ;
    IF T=KIS GOTO 2060
2050 GOSUB 2100;
    TO=KIS;
    GOSUB 1950
2060 'IS
2070 X=-(OFST+FMSZ);
    GOSUB 4280;
    GOSUB 1400;
    OFST=0;
    MXOF=0;
    GOSUB 2440;
    W=PRET;
    GOSUB 3990;
    GOSUB 5300;
    RETURN

2250 'SUBTYPEINDICATIONUNIT
2260 GOSUB 3890;
    IF KIND<>4 THEN E=8;
    GOTO 5020 ELSE IF PINFO=0 THEN KIND=1
    ELSE KIND=5
2280 IF TYPE<>0 THEN GOSUB 1400;
    RETURN

2285 GOSUB 2300;
    IF QBSIZ>255 THEN E=5;
    GOTO 5020 ELSE RETURN

2440 'BODYPART
2450 IF INSTR(DECLPARTS$, TT$) THEN GOSUB 2480
    CP=0;
    GOSUB 2790
2460 CB=GC;
2470 RETURN

2480 'DECLPART
2490 IF T=ID THEN T1$=ID$;
    K1=5;
    GOSUB 2560;
    GOTO 2540
2500 IF T=KPROC THEN GOSUB 1400;
    GOSUB 2010;
    GOTO 2540
2510 IF T=KFUNC THEN GOSUB 1400;
    GOSUB 2340;
    GOTO 2540
2520 IF T=KPRAGMA THEN GOSUB 2770;
    GOTO 2550

2294 GOSUB 3890;
    IF KIND=0 AND TYPE=1 THEN T=C;
    T2=CONST
2295 TYPE=T8;
    KIND=T3;
    PINFO=T4;
    CONST=T5;
    ORJSZ=T6;
    LL=T7
2297 T0-C;
    GOSUB 1960;
    RETURN

```

```

2100 'PROCFORMALPART
2110 T2$="";
T0=LP;
GOSUB 1960
2120 GOSUB 2160;
IF T=SEMICOLON THEN GOSUB 1400;
GOTO 2120
2130 T0=RP;
GOSUB 1960;
FOR I=0$ TO -FMSI-2 STEP 2;
T1$=LEFT$(T2$,17);
T2$=MID$(T2$,18);
IF (LEN(S$(SSP))+17)>255 THEN SSP=SSP+1
2140 S$(SSP)=LEFT$(T1$,14)+MK1$(I)+RIGHT$(T1$,1)+SS$(SSP);
NEXT I
2150 RETURN
2160 'PROCPARAMDECL
2170 T1$=""
2180 T0=ID;
GOSUB 1950;
T1$=T1$+ID$;
GOSUB 1400
2190 IF T=COMMA THEN GOSUB 1400;
GOTO 2180
2200 T0=COLON;
GOSUB 1960;
P1=1;
IF T=KOUT THEN P1=2;
GOSUB 1400;
GOTO 2220
2210 IF T=KIN THEN GOSUB 1400
2220 GOSUB 2250;
PINFO=P1
2230 WHILE LEN(T1$)>0;
T2$=T1$+LEFT$(T1$,8)+CHR$(TYPE)+CHR$(PINFO)+MK1$(CONST)+CHR$(OBJ$)+MK1$(0)+CHR$(LL),T3-T2+8)+CHR$(TYPE)+MID$(S$(T1$),T3-T2+10)
GOTO 5020
2385 S$(T1$)=LEFT$(S$(T1$),T3-T2+8)+
CHR$(KIND)+CHR$(PINFO)+MK1$(CONST)+CHR$(OBJ$)+MK1$(0)+CHR$(LL)
GOTO 2630

```

```

2300 'OBJJSZ
2310 GOSUB 1400;
IF T<>LP GOTO 2330 ELSE GOSUB 1400;
2320 GOSUB 2290;
OBJ$=TN+1;
T0=RP;
GOSUB 1960
2330 RETURN
2340 'PARSEFUNC
2350 GOSUB 5200;
KIND=3;
PROC=PROC+1;
CPROC=PROC;
ADDR=PROC;
X=ADDR;
GOSUB 4280;
GOSUB 3850;
Y=SSP;
GOSUB 4280;
X=LEN(S$(SSP))
2355 GOSUB 4280;
GOSUB 1400
T1$=T1$+ID$;
2370 0$ST=-FMSI$;
IF T=LP THEN GOSUB 2100
2380 T0=KRET;
GOSUB 1960;
GOSUB 2250;
T2=X;
GOSUB 4300;
T1=Y;
T3=LEN(S$(T1$));
IF KIND>5 OR OBJ$<>2 THEN E=16;
GOSUB 5020
2690 S$(T1$)=LEFT$(S$(T1$),T3-T2+8)+CHR$(TYPE)+CHR$(KIND)+CHR$(PINFO)+MK1$(CONST)+CHR$(OBJ$)+MK1$(0)+CHR$(LL),T3-T2+10)
GOSUB 1400;
GOTO 2630
2560 'OBJDECL
2570 GOSUB 1400
2580 IF T=COMMA THEN GOSUB 1400;
T0=ID;
GOSUB 1950;
T1$=T1$+ID$;
GOTO 2570
2590 T0=COLON;
GOSUB 1960
2600 IF T=KCONST GOTO 2650
2610 IF T=KARRAY GOTO 2700
2620 GOSUB 2250;
OBJ$SIZE=OBJ$Z
2630 PINFO=0;
KIND=K1;
WHILE LEN(T1$)>0;
ID$=LEFT$(T1$,8);
T1$=MID$(T1$,9);
ADDR=0$ST;
0$ST=0$ST+OBJ$SIZE;
GOSUB 3850;
WEND
2640 RETURN
2650 'CONSTANT
2670 K1=0;
OBJ$SIZE=0;
GOSUB 1400;
TO=COLONEQ;
GOSUB 1960;
IF T=ID THEN GOSUB 3890;
GOTO 2690 ELSE IF T=SUBT THEN T1=-1;
GOSUB 1400 ELSE T1=1
2680 CONST=INT(T1$;
IF T=C THEN TYPE=1 ELSE TYPE=2
2690 GOSUB 1400;
GOTO 2630

```

Augusta Part III

Listing Two

(Continued, text begins on page 13)

```

2930 'RETURN
2940 GOSUB 1400
2950 IF T>SEMICOLON THEN GOSUB 3100:
    TSP=TSP-1;
    M=PNP; ELSE M=PRET
2960 GOSUB 3990:
    GOSUB 3420:
        RETURN

2700 'ARRAY
2710 K1=1:
    GOSUB 1400:
        TO=LP;
        GOSUB 1960;
        T2=IN:
        GOSUB 2290;
        TO=RP;
        GOSUB 1960;
        TO=KOF;
        GOSUB 1960
2750 GOSUB 2250:
    CONST=T2;
    OBJSIZE=(T2+1)*OBJSZ;
    IF T2<0 OR T2>16383 THEN E=15;
    GOTO 5020 ELSE GOTO 2630
    X=LJJP;
    GOSUB 4280
2770 'PRAGMA
2780 IF T>#PRAGMA THEN RETURN ELSE GOSUB 4830:
    GOSUB 1280:
    GOSUB 1400;
    GOTO 2780
    Y=LJJP;
    GOSUB 4280
2790 'STMT
2800 TO=KBEGIN:
    GOSUB 1960:
    TO=END;
    GOSUB 1960;
    RETURN
2810 'SEDEFSTMTS
2820 I=INSTR(SINTSS$, TT$)
2825 IF I=0 THEN RETURN ELSE ON I GOSUB
    4320, 4320, 4320, 2850, 2890, 2930, 2970,
    4630, 2830, 3440, 2770;
    GOTO 2820

```

```

3100 'EXPR
3110 GOSUB 3190:
    LFJP=0;
    PREV=0
3120 IF INSTR(LOGICALOPS$, TT$)=0 THEN
    IF PREV GOTO 3180 ELSE RETURN
3125 X=T:
    GOSUB 1400:
        IF (X=AND AND T=KTHEN) THEN X=KAND+
        KTHEN ELSE IF (X=KOR AND T=KELSE) THEN
        X=KOR+KELSE
3130 IF PREV<>X THEN E=10:
    GOTO 5020
3140 IF X>KAND AND X<>KOR GOTO 3160
    GOSUB 4280;
3145 GOSUB 4280:
    GOSUB 3190:
        IF (TY(TSP)<>TBOL) OR (TY(TSP)<>TY(TSP-
        1)) THEN E=9;
    GOTO 5020
3147 TSP=TSP-1;
    GOSUB 4300:
        PREV=X;
        IF X=KAND THEN M=PAND ELSE M=POR
3150 GOSUB 3990:
    GOTO 3120
3160 GOSUB 4280:
    T1=Y;
    W=PDUP;
    GOSUB 3990:
        IF T1=KAND+KTHEN THEN M=PFJP ELSE M=PNOT;
    GOSUB 3990:
        W=PFJP;
        LFJP=CP;
        GOSUB 4030:
        GOSUB 1400;
        Y=LFJP;
        GOSUB 4280;
        GOSUB 3190
3000 IF T=KEND THEN GOSUB 3040:
    GOTO 3030
3010 IF T=RELSE THEN GOSUB 3060:
    GOSUB 3040:
    GOTO 2990
3020 TO=KELSE:
    GOSUB 1960;
    GOSUB 3040;
    GOSUB 3040;
    Y=LJJP;
    GOSUB 4280;
    GOSUB 2810;
    GOSUB 2810;
    GOSUB 4300;
    GOSUB 3190

```

(The Augusta Compiler listing will be continued in the July 1983 issue.)

Augusta Part III

Listing Three

Simple expression parser.

```

2830 'NULL;
2840 GOSUB 1400;
GOSUB 3420;
RETURN

2850 'BLOCK
GOSUB 4280;
OFST=OFST+2;
GOSUB 5400;
IF T=KDECLARE THEN GOSUB 1400;
GOSUB 2480
2880 GOSUB 2790;
GOSUB 5500;
GOSUB 5700;
GOSUB 4300;
OFST=X;
GOSUB 3420;
RETURN

3030 T0=TEND;
GOSUB 1940;
T0=KTF;
GOSUB 1940;
GOSUB 3080;
GOSUB 3420;
RETURN

2860 X=OFST;
GOSUB 4280;
OFST=OFST+2;
GOSUB 5400;
IF T=KDECLARE THEN GOSUB 1400;
GOSUB 2480
2880 GOSUB 2790;
GOSUB 5500;
GOSUB 5700;
GOSUB 4300;
OFST=X;
GOSUB 3420;
RETURN

3040 'FIX FJP
3050 GOSUB 4300;
T1=CP;
CP=X;
W=T1-X-2;
GOSUB 4030;
CP=T1;
RETURN

3060 'GEN LJP
3070 W=PUJP;
GOSUB 3990;
W=LJJP;
LJJP=CP;
GOSUB 4030;
RETURN

2890 'EXIT
2900 IF LPFLG=0 THEN E=14;
GOTO 5020
2910 GOSUB 1400;
IF T=SEMICOLON THEN W=PUJP;
GOSUB 3990;
GOTO 2925

2920 T0=KWHEN;
GOSUB 1940;
GOSUB 3100;
GOSUB 4930;
W=PNOT;
GOSUB 3990;
W=PFJP;
GOSUB 3990
2925 W=XITJP;
XITJP=CP;
GOSUB 4030;
GOSUB 3420;
RETURN

```

```

PROCEDURE PARSEEXFR IS
PROCEDURE EXPR IS
PROCEDURE SE IS
PROCEDURE FACTOR IS
BEGIN
  IF TOKEN='C' THEN
    GETTOKEN;
    RETURN;
  END IF;
  IF TOKEN='(' THEN ERROR ("SYNTAX ERROR");
  ELSE
    GETTOKEN;
    EXPR;
    IF TOKEN /= ')' THEN ERROR ("EXPECTED");
    END IF;
  END IF;
  END; --- OF FACTOR

BEGIN
  LOOP
    FACTOR;
    IF TOKEN='+' THEN
      NULL;
    ELSEIF TOKEN='-' THEN
      NULL;
    ELSE
      RETURN;
    END IF;
    GETTOKEN;
    END LOOP;
    END; --- OF SE

BEGIN
  LOOP
    SE;
    IF TOKEN='*' THEN
      NULL;
    ELSEIF TOKEN='/' THEN
      NULL;
    ELSE
      RETURN;
    END IF;
    GETTOKEN;
    END LOOP;
    END; --- OF EXPR

BEGIN
  GETTOKEN;
  EXPR;
END;

END Listing Three

```

(Continued on page 26, column 2)

(Continued on page 26, column 3)

(Listing Four begins on page 28)

Augusta Part III
Listing Four

Simple expression now with code annotation

(Text begins on page 13)

Simple expression parser with code generation.

Simple expression parser with code generation.

```

PROCEDURE PARSEEXPR IS
  PROCEDURE EXPR IS
    PROCEDURE SE IS
      PROCEDURE FACTOR IS
        BEGIN
          IF TOKEN='C' THEN
            EMITBYTE( 1 ); -- LDC1
            EMITWORD( TN ); -- the actual constant
            GETTOKEN; -- SCAN TO NEXT TOKEN
            RETURN; -- RETURN TO CALLER
          END IF;
          IF TOKEN=/='(' THEN ERROR ("SYNTAX ERROR");
          ELSE
            GETTOKEN;
            EXPR;
            IF TOKEN/= ')' THEN ERROR ("") EXPECTED");
            END IF;
          END IF;
        END;
      END;
    END;
  END;
END;

```

Augusta Part III

Listino six

(b) shows the code generated for the WHILE statement shown in (a).

```

(a) PROCEDURE DEMO IS
    I : INTEGER;
BEGIN
    I:=1;           WHILE I<10
    LOOP;          I:=I+1;
    END LOOP;
END;

```

(b)

```

SLAO 0           Load address of I
SLDC1           Short load constant 1
STO             Store, I:=1
-->SLDO 0       Load value of I
SLDC10          and compare to 10
TEST            Branch -1 if >10

```

```

BEGIN
LOOP
SE;
IF TOKEN='*' THEN
    EMITBYTE( * );
-- MFI
ELSEIF TOKEN='/.' THEN
    EMITBYTE( / );
-- DIVI
ELSE
    RETURN;
END;

```

Dr. Dobb's Journal, Number 79, May 1983

```

PROCEDURE Stmt IS
BEGIN
  IF TOKEN//='I' THEN ERROR ("ID EXPECTED");
  ELSEIF TOKEN//=? THEN ERROR ("= EXPECTED");
  ELSE
    GETTOKEN;
    EXPR;
    IF TOKEN//=? THEN ERROR ("; EXPECTED");
  END IF;
END;

```

Dr. Dobb's Journal, Number 79, May 1983

```

    ;<---- FJP -----| ; Jump if I>10
    | SLAO O          | ; Load address of I
    | SLDI O          | ; Load value of I
    | SLDC1           | ; and add 1 to it
    | ADI             |
    | STO             |
    | ;<-UJP -----| ; Jump back for more

```

End Listing Six

Augusta Part III

Listing Seven

Code generation for an IF-THEN-ELSE. (a) shows the IF-THEN-ELSE statement and (b) shows the corresponding compiled code.

End Listing Four

```

(a)
IF A=B THEN
  C=A*X2;
ELSE
  C=B+A;
END IF;

(b)
LDL    0      Load value of A
LDL    2      Load value of B
EQU1   --      Pop top 2 words and compare, push TRUE/FALSE
FJP   --      Jump if they are not equal to do ELSE part
LLA    4      Load address of C
LDL    0      Load value of A
SLDC2  --      Load constant 2 to multiply by
MPI    --      Multiply A*X2
STO    --      and set C:=A*X2
UJP   --      jump around the ELSE part
-->    LLA    4      Following code is ELSE part
-->    LDL    2      Load address of C
-->    LDL    0      Load value of B
-->    ADI    --      And load value of A
-->    STO   --      Add them together
-->

```

Augusta Part III
Listing Five

לטינית - י

Code generated for the FOR loop. At (a) is a sample FOR loop, and at (b) is the corresponding p-code translation.

a) FOR I IN 1..10
LOOP
.
.
.
<sequence of statements>

END LOOP;

End Listing Seven

(Continued on page 28, column 2)

Augusta Part III

Listing Eight

"Short-circuit" conditions are provided to evaluate only a portion of an expression. For example, the second expression ($J/N > 50$), will only be evaluated if the first part tests as true. Augusta provides two short circuit operators – AND THEN and OR ELSE.

```
(a)    IF (N /= 0) AND THEN (J / N > 50) THEN I:=0;
(b)    SLDU     8      ; Get value of N
          SLDCO   0      ; Load constant 0
          NEQI
          DUP
          <-FJP
          SLDU   10     ; see if not equal
          SLDCO   8      ; duplicate top of stack value
          STO
          SLDU   50     ; Jump around if equal
          DVI
          SLDC
          GTRI
          AND
          <-FJP
          SLAD
          SLDCO
          STO
          <->
```

Get value of N
Load constant 0
see if not equal
duplicate top of stack value
Jump around if equal
Load J
Load N
J / N
Load 50
> Comparison
AND with previous expression
and jump if false
Load address of I
Load constant 0
and store result

```
LDL XJP   2      ; Load value of I
          w1   w2   w3      ; Jump indirectly, using top of stack
          value,-w1, as an index in to the
          jump table at offset w3
          <-> <code for statements 1> ; This is case 7
          UJP ___ ; Jump past the jump table
          <-> <code for statements 2> ; This is case 11
          UJP ___ ; Jump past the jump table
          <-> <code for statements 3> ; This is case 19
          UJP ___ ; Jump past the jump table
          <-> <code for statements 4> ; This is case 23
          UJP ___ ; Jump past the jump table
          <-> <code for statements 5> ; The jump table follows
          UJP <-> ; address of OTHERS condition
          <-> ; address of case 7
          <-> ; address of case B (pointer to OTHERS)
          <-> ; address of case 9 (pointer to OTHERS)
          <-> ; address of case 10 (to OTHERS)
          <-> ; address of case 11
          <-> ; address of case 19
          <-> ; address of case 23
          <-> ; first statement following END CASE
```

End Listing Eight

Augusta Part III

Listing Nine

(b) shows the code generated for the CASE statement shown in (a).
The text describes the XJP instruction in greater detail.

(a)

```
CASE I OF
  WHEN 7 => <statements 1>
  WHEN 11 => <statements 2>
  WHEN 19 => <statements 3>
  WHEN 23 => <statements 4>
END CASE;
```

End Listing Nine

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hierarchy may be changed through the use of parenthesis.

Listing Control—allows listing of sections on the program with convenient assembly error detection overrides, along with assembly run time commands that may be used to dynamically change the listing mode during assembly.

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A Fast Circle Routine

In some instances it is necessary to draw partial circles or arcs and at other times complete circles are required. The algorithm presented here draws complete circles accurately and quickly and is relatively simple to program. The routine is written for the IBM Personal Computer using IBM's Macro Assembler, and can be implemented as an IBM Pascal procedure.

The Algorithm

Circle algorithms generally use a transformation from polar to rectangular coordinates by the following relationships:

by Daniel L. Lee

Daniel L. Lee, 1401 E. 55th Street, Apt. 601, Chicago, IL 60615.

Mr. Lee refuses to reserve any rights, commercial or otherwise, to this procedure.

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$$X = R \cosine(A)$$
$$Y = R \sin(A)$$

where the (X,Y) coordinates produce the locus of points for the given radius R as the angle A varies from 0 to 2 pi radians. This method requires trigonometric routines to compute sine and cosine values at least for the start and end points and for an angular increment.

A simpler method which does not require sine and cosine values is obtained by elementary calculus, which can be used to show that $dX/dY = -\tan(A) = -Y/X$. Also, $dY/dX = -\cot(A) = -X/Y$. In other words, the change in X, given a small change in Y, is simply the negative of the ratio of Y to X. Similarly, the change in Y, given a small change in X, is simply the negative of the ratio of X to Y.

Given these relationships, the circle procedure is straightforward. For simplicity it will be assumed that the circle center is at the origin of our coordinate system. It is not difficult to adjust for a change in origin.

The first point plotted is chosen as $Y=R$ and $X=0$, where R is the radius measured in screen column units. Y is incremented by one unit and X is decremented by Y/X units and the point plotted. This process is continued until $Y/X=1$. At that point (which corresponds to a 45 degree angle), we switch over to decreasing X by one unit and increasing Y by X/Y units. The process is continued until X/Y equals 0, i.e., when X is decremented to 0. At that point an arc from 0 to 90 degrees has been drawn. The complete circle can be obtained by using the symmetry of the circle to compute the corresponding points in each of the three remaining circle quadrants as we trace out the arc from 0 to 90 degrees.

The switch over when Y/X equals 1 is required because the tangent will approach infinity as the angle approaches 90 degrees, and overflow will occur somewhere prior to that point.

To actually implement the algorithm, we have to adjust for the origin located at the top left-hand corner of the video screen. Also we have to take into account the aspect ratio of the screen. When plotting 0 to 45 degrees, the X value must be multiplied by the inverse of the aspect ratio, and when plotting from 45 to 90 degrees, the Y value must be multiplied by the aspect ratio. Assuming a screen aspect ratio of 4/3, the aspect ratio to produce a circle in medium-resolution

graphics is 5/6 and is 5/12 for high-resolution graphics.

The Assembly Routine

The routine incorporates the elements of the algorithm just described. It is also necessary to scale the computations by some factor to retain numerical accuracy, and then to rescale to obtain the points to plot. A factor of 1000 gives sufficient accuracy for the PC's high-resolution mode.

As mentioned, the circle routine can be implemented as a Pascal procedure. Listing 1 (page 35) is the circle routine and Listing 2 (page 37) is a simple calling routine which generates an internal calling sequence as used by IBM Pascal.

Pascal sets up a frame on the stack for an active procedure which is invoked by a FAR call. First, the calling program pushes the procedure arguments from left to right onto the stack. Then the caller's segment address and offset are pushed onto the stack. When the procedure receives control, it should first save the caller's base pointer by pushing BP onto the stack. Then the BP register is set equal to the SP register (stack pointer) and the procedure can then reference the arguments by adding six bytes to the BP register — four for the caller's return address and two for the caller's base pointer which has been pushed onto the stack. Thus, assuming integer value arguments, the first variable on the stack can be addressed by BP+6, the second by BP+8, and so on.

To return, the procedure must restore the caller's base pointer in BP and the SP register must be restored by a RET x, where x is the number of bytes on the stack occupied by the arguments which were passed.

Note that the IBM-supplied BIOS routine is used to write the points to screen. That is accomplished by storing 12 in the AH register, the X coordinate in the CX register, the Y coordinate in the DX register, and then issuing an INT 10H instruction. In this routine, the INT 10H instruction is defined as the MACRO, BIOSCALL, the expansion of which is denoted by "+" in the listing.

DDJ

(Listing begins on page 35)

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The operator can use the arrows, etc. during data entry and conveniently jump back and forth between the input fields.

You can specify field lengths, or let ZIP default to the available space. Either way, text and prompts are protected no matter what kind of terminal you have, so the operator can't write over the fields and prompts.

NEW CUSTOMER					
CUSTOMER NAME:	;Customer\$		DATE: @Date		
MAIL ADDRESS:	;Addr1\$;30				
CITY:	;Addr2\$				
STATE:	;Addr3\$		ZIP:	;Zip1:5	
SHIP ADDRESS:	;Addr4\$;30				
CITY:	;Addr5\$				
STATE:	;Addr6\$		ZIP:	;Zip2:5	
DISCOUNT CATEGORY: ;Rate!					
THE CODE FOR THIS NEW CUSTOMER IS @CustCode.					

Row 17, Col 36

You get the MBASIC code for a "Talker" that you can use to pretty up your program prompts. And easy, one-line data validation is built in.

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Or if you'd like to end the paper chase sooner, just call 800-227-3747. (In California, call 213-937-0554, add 6% tax.)



Nexus
from
The man-machine connection.

Fast Circle Plot Listing One

```

MOV BP,SP           ;set proc frame ptr
MOV AX,[BP+10]      ;get aspect numer and
MOV BX,1000          ;scale it by 1000
IMUL BX
MOV CX,[BP+8]       ;get aspect denom
IDIV CX             ;AX=aspect*1000
PUSH AX
XCHG AX,CX          ;get denom in AX
MOV CX,[BP+10]      ;get numer in CX
IMUL BX             ;AX=denum*1000
IDIV CX             ;AX=inv aspect*1000
MOV [BP+8],AX        ;store it
POP AX              ;get aspect*1000
MOV [BP+10],AX        ;and store it
; start by incrementing Y by one unit and
; decrementing X by TAN unittiny aspect
; start at (RADIUS,Y) and plot to 45 deg
;

BIOSCALL MACRO
INT 10H             ;BIOS service id in AH
ENDM

TITLE 'Fast Circle Plot'

STACK SEGMENT PARA STACK 'STACK'
DB 64 DUP ('STACK')
;

0000 40 [ 53 54 41 43
4B 20 20 20 ] 1

0000 0200           STACK ENDS

CSEG SEGMENT PARA 'CODE'
;-----[PROCEDURE CIRCLE(X,Y,RADIUS,NUMER,DENOM,
;COLOR:INTEGER)
;Dan Lee July 1, 1982
;SourceWare
;-----[draws a circle at center (x,y) with aspect
;ratio numer/denom; radius in column units
;assumes entry via inter-segment call
;FRAME: VALUE X : BP+16
;       VALUE Y : BP+14
;       VALUE RADIUS: BP+12
;       VALUE NUMER : BP+10
;       VALUE DENOM : BP+8
;       VALUE COLOR : BP+6
;-----[CIRCLE PROC FAR
;ASSUME CS:CSEG, SS:STACK
PUBLIC CIRCLE
PUSH BP             ;caller's frame ptr
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```

Fast Circle Plot

Listing One (Listing One continued, text begins on page 32)

```

00C3 58 ;restore write dot paras
00C4 03 CF ;get 4th quad
00C6 03 CF ;X to plot
+          ;plot 4th quad point
BIOSCALL ;BIOS service id in AH
INT 10H ;DX=Y-Y origin
SUB DX,[BP+14] ;DX=Y-Y origin
NEG DX ;Y origin adjust
XCHG CX,DX
CX=Y
+          ;90 deg?
OR DI,DI ;yes,exit
JS CR11 ;get new X
DEC DI ;AX=X
MOV AX,DI ;BX=Aspect$1000
MOV BX,[BP+10] ;AX=Aspect$1000
IMUL BX ;AX=Aspect$1000$X
IDIV CX ;AX=Aspect$1000$COT
MOV SI,AX ;SI=change in Y
+          ;DX=hi word Y$1000
;AX=lo word Y$1000
POP DX ;POP AX
XOR BX,BX ;OR SI,SI
OR SI,SI ;for sign check
;positive
;negative carry
;AX=new X value
;AX=old X carry
;hi word carry
JMP SHORT CR8 ;plot next point
; exit
;

004D 2B CB ;get 2nd quad
004F 2B CB ;X-origin
0051 50     ;save write dot paras
PUSH AX ;write 2nd quad point
BIOSCALL ;BIOS service id in AH
INT 10H ;restore write dot paras
POP AX ;get 3rd quad
ADD DX,DI ;AX=origin
ADD DX,DI ;save write dot paras
PUSH AX ;plot 3rd quad point
BIOSCALL ;BIOS service id in AH
INT 10H ;restore write dot paras
POP AX ;get 4th quad
ADD CX,BX ;X-origin
ADD CX,BX ;plot 4th quad point
BIOSCALL ;BIOS service id in AH
INT 10H ;restore write dot paras
+          ;CX now at original point
; exit
;

005A CD 10
005C 58
005D 03 CB
005F 03 CB
0061 CD 10
0063 87 CB
0065 47
0066 8B C7
0068 8B 5E 08
006B F7 EB
006D F7 F9
006F 33 D2
0071 8B F0
0073 F7 FB
0075 3D 0001
0078 5A
0079 58
007A 73 08
007C F7 DE
007E BB FFFF
0081 03 C6
0083 13 D3
0085 EB A3

00C8 CD 10
00CA 2B 56 0E
00CD F7 DA
00CF 87 CA
00D1 0B FF
00D3 78 10
00D5 4F
00D6 BB C7
00D8 8B 5E 0A
00D9 F7 EB
00DD F7 F9
00DF 8B F0
00E1 5A
00E2 58
00E3 33 DB
00E5 0B F6
00E7 79 03
00E9 BB FFFF
00EC 03 C6
00EE 13 D3
00F0 EB 9F

00F2 83 C4 04
00F5 5D
00F6 CA 000C
00F9
00F9

CR11: ADD SP,4 ;adjust stack ptr
      POP BP ;caller's frame ptr
      RET 12 ;release paras
      CIRCLE ENDP
      CSEG ENDS
      END

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```

End Listing One

Listing Two

```

; plot 45 to 90 degrees
; now decrease X by one unit and
; increase Y by DOT units aspect ratio

0087 BB C7
0089 BB 03E8
008C F7 EB
008E BB F9
0090 4F

0091 50          ; get next Y to plot and
                  ; scale by 1000
0092 52          ;DX=AX=Y*1000
0093 33 DB
0095 05 01F4    ;DI=last X value
0098 13 D3      ;next X to plot
009A BB 03E8
009D F7 FB
009F BB D8
00A1 03 46 0E
00A4 BB 4E 10
00A7 03 CF
00A9 BB D0
00AB 9A 46 06
00AE B4 0C
00B0 50
00B1 CD 10
00B3 58

00B4 2B CF
00B6 2B CF
00B8 50

00B9 CD 10
00BB 58
00BC 2B D3
00BE 2B D3
00C0 50
00C1 CD 10

;CR7:   MOV AX,DI          ;get next Y to plot and
        MOV BX,1000          ;scale by 1000
        IMUL BX
        MOV DI,CX
        DEC DI
        MOV AX,[BP+14]        ;save hi word Y*1000
        PUSH AX
        MOV CX,[BP+16]        ;begin round process
        XOR BX,BX
        ADD AX,500
        ADC DX,BX
        MOV BX,1000
        IDIV BX
        MOV BX,AX
        ADD CX,DX
        ADD AX,[BP+14]        ;add Y origin
        MOV CX,[BP+16]        ;CX=X origin
        ADD CX,DI
        MOV DX,AX
        MOV AL,[BP+6]          ;get color
        MOV AH,12
        INT 10H
        INT 10H
        POP AX
        BIOSCALL
        INT 10H
        POP AX
        +                   ;get 2nd quad
        SUB CX,DI
        SUB CX,DI
        PUSH AX
        BIOSCALL
        INT 10H
        POP AX
        BIOSCALL
        SUB DX,BX
        SUB DX,BX
        PUSH AX
        BIOSCALL
        INT 10H
        +                   ;get 3rd quad
        SUB CX,DI
        SUB CX,DI
        PUSH AX
        BIOSCALL
        INT 10H
        +                   ;get 4th quad
        SUB CX,DI
        SUB CX,DI
        PUSH AX
        BIOSCALL
        INT 10H

```

(Continued on page 36, column 2)

Enhancing the C Screen Editor

In the January 1982 issue of Dr. Dobb's we presented Edward Ream's popular Small-C screen editor. Readers received a fine editor which, at the same time, was an excellent candidate for extension and modification. Alan Howard has provided us with an extensive set of enhancements. Those who wish to implement changes in the original editor should find guidance and inspiration from Mr. Howard's work.

Mr. Howard has chosen a modular approach to the problem and his changes should be easy to implement. Mr. Ream himself has attacked the situation by significant revision of the entire editor. We will be publishing Mr. Ream's revision, called RED, in the near future. Comparison of these two approaches should provide insight into programming techniques and implementation tradeoffs, and we feel fortunate to be able to publish both pieces of work.

While the January and May 1982 back issues are no longer available (the bound volume containing them should be out later this year), we understand the original editor may still be obtained from a variety of sources, including several computer bulletin boards. Edward Ream still distributes it himself for \$50 on 8-inch, CP/M diskettes, and can be reached at 1850 Summit Avenue, Madison, WI 53705. A BDS C version may be obtained from The C User's Group, Box 287, Yates Center, KS 66783 (which sells it for \$8 in a variety of formats) and Steve Passe's Cnode computer bulletin board (contact the C Users' group for details).

At the beginning of Edward Ream's article introducing his Small-C Screen Editor in *Dr. Dobb's Journal* (No. 63, January 1982), he asked whether our present editor lacked flexibility, transportability, and extensibility. Although my mainstay editor has been PIE from Software Toolworks, I have often been annoyed at its inability to edit large files or to make major structural changes in text. On the other hand, Ream's editor as presented did not go much beyond PIE and seemed clumsy in its Edit mode. It did, however, encourage adapting it to fit individual needs since it was presented in source code. Thus,

by Alan D. Howard

Alan D. Howard, Route 3, Box 680, Crozet, VA 22932.

after several nights of typing and several more nights of debugging, I had the C editor up and running (those of you who want to join me doing it the hard way, be sure to note the bug fixes in the May issue, No. 67). I have been modifying the editor during the last few months and it has evolved to the point that I feel some of Dr. Dobb's readers may be interested. The changes are of several types:

(1) Extensions to the file-handling and buffer management to permit editing large files, extracting parts of the text to the write file, and moving and copying portions of the text within the file.

(2) Changing the edit mode operation to recognize special keys on the H19 Terminal or H89 Computer instead of single-letter commands. As a bonus, the editor now assumes *eXchange* mode for any other typed key. The code can be easily modified for other terminal protocols.

(3) Addition of a few new commands to the edit-mode and some changes in rules of operation.

(4) Slight modifications to allow modular compilation using the Software Toolworks C/80 compiler.

(5) Enhancements to improve the speed of response in edit mode and during search and replace operations.

These enhancements are discussed below as explanations for the listing that accompanies this article. Only those variables, declarations, and functions that have been changed from the original listing are listed. A documentation file is included in the listing which summarizes the expanded features and all commands.

Changes to File Handling and Command-Mode Operation

Most of the changes to the command-mode are concerned with adding flexibility to file handling and major buffer alterations. Separate read and write files are now maintained, with *load <filename>* specifying the read file name, clearing the buffer, reading the file, and closing the read file if it all fits in the buffer. Otherwise as much as fits is read in, and the read file remains open. If a filename is specified in the command line when the editor is loaded, the filename is automatically passed to the *load* command. The *open <filename>* command opens the read file without clearing the buffer or reading anything in. More from the read file can be added to the buffer with either

the *rest* or *read <n>* commands. *Rest* reads in as much more as possible and gives the option of clearing the buffer, but *read <n>* reads only *<n>* more lines without clearing the buffer. The write file is specified by *name <filename>* for a new file, or *delname <filename>* for writing over an existing file (this replaces the *resave* command). *Rename <filename>* closes the existing write file and opens a new one. *Write <n>* writes *<n>* lines from the buffer to the write file, deleting those lines. To balance the existing *append <filename>* command, an *extract <from to>* command has been added to write the indicated line range to the write file without deleting those lines from the buffer. Finally, to allow flexibility, the *read* and *write* files can be closed by *closeread* and *closewrite*.

Major structural changes in the file can now be made using the *copy <from to n>* and *move <from to n>* commands, which take *n* lines from *<from>* and copy them to before line *<to>*. In addition, *move* deletes the *<n>* lines at *<from>*. For speed, these routines open up the new space, and then do the copying or moving. No *move* or *copy* is allowed if there is not room in the buffer. If buffer space is tight, or if the *move* or *copy* is to a part of the file not currently in memory, then use the *extract* and *append* commands, which use the disk as a buffer.

Because tabs are treated as ordinary characters, *showtab* has been included to allow tabs to be shown in reverse video. This mode is reset by *hidetab* (the default mode) where tabs are just blanks.

The following command-mode commands are unaltered from the original: *append*, *change*, *clear*, *delete*, *dos*, *find*, *g*, *list*, *search*, and *tabs*.

Changes to Edit and Insert Modes

The most important change to the edit mode is the replacement of the single-letter commands by escape-character sequences generated by the special keys on the H19/H89 keyboard. Similar keys, but with different escape sequences, are found on many other terminals and computers. This allows default use of the *eXchange* mode (replacing contents of cursor position with the typed keystroke) for normal keys, including the space bar. The *eXchange* mode has been speeded up considerably to allow rapid typing. Most special characters and edit mode commands retain their original functions (see the documentation portion of the listing

for details), but two new commands have been created for the edit mode: the *HOME* key moves alternately to the top and bottom line of the screen, and the *ERASE* key erases from the current cursor position to the end of the line. More subtle changes have also been made. The *RETURN* key moves to the beginning of the next line in edit mode, but acts as the *insert down* key in the insert mode. Both the *DC* and *DELETE* keys delete the character at the cursor, but *BACKSPACE* deletes the character to the left of the cursor, the same as the original *delete character* special key. The only commands remaining as control codes are *split* and *join*. There is some room for expansion: three keys are currently assigned to force command mode, but could be reassigned. Note that the *ESC* key must be pressed twice to be recognized, because it is also issued by the special character codes of the keyboard. My choice of key assignments and features has admittedly been influenced by the PIE editor.

Implementation Details

I'd like to start out with an unsolicited endorsement of the Software Toolworks C/80 Compiler, now in version 2.0 and incorporating almost all of the C language. It's a descendent of Small-C and is inex-

pensive, fast, and powerful. Its salient feature in the context of the editor is support for modular compilation using Microsoft M80 and L80. Since the editor is broken into nine fairly independent modules, changes can be made in one without having to recompile everything. Also, SID can be used for debugging using global variable and function names. The disadvantage is that variable and function names must be distinct at six characters' length. This has necessitated renaming several functions in the editor, as listed in Figure 1 (page 42). (I have not listed each occurrence of the altered names in the listing; I leave that as a test of the capabilities of your present editor.) Also, to force the main buffer to be at the end of the program, the short program MBUFFER.MAC must be assembled by M80 and be the *last* module linked (after the C/80 CLIBRARY module).

Most details of the changes are explained in the listing on page 43. The functions that are completely unchanged are not included, but a note indicates their position. In general, I have listed the entire function if there has been any change.

Concluding Remarks

This editor is a very useful addition to my stable of inexpensive editors, and is

particularly suited to editing large files, interleaving text from other files, and breaking up a file or portions of a file into smaller pieces. It doesn't do everything, and reading and writing to files are considerably slower than for editors written in assembly language. I suggest those with H89's go through the bothersome task of reassembling their BIOS with the type-ahead buffer option so that rapid typing will not lose characters.

When I move on to my next system, I will feel secure that I have its first editor waiting in the wings, and it won't cost me twice as much as my previous editor for the same features. The modifications to the editor are in the public domain, and a somewhat earlier version has been submitted to the SIG/M User Group (Amateur Computer Group of New Jersey, Box 97, Iselin, NJ 08830).

DDJ

(Figure 1 on page 42)

(Listing begins on page 43)

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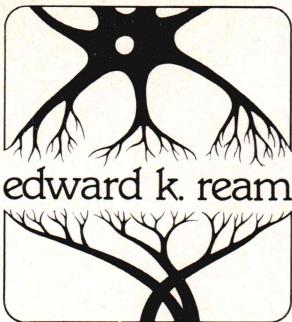
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Circle no. 1 on reader service card.

Figure 1. (Text begins on page 38)

save	<from to>	Save the buffer into the write file; buffer is unchanged
search	<n>	Print all lines that match a pattern
showtab	<n>	Show tabs in reverse video
tabs	<n>	Set tab stops at every <n>columns
write	(blue key) (IC key)	Write <n>lines from front of buffer to the write file; the lines are deleted from the buffer
EDIT AND INSERT MODE KEY COMMANDS: (Commands marked † not valid in insert mode; Commands marked + force edit mode)		
Original	New	Original
search1	ssearch	pmtcol1
ougetxs	outxget	sysmovdn
outgety	outyget	sysmovup
outhasdns	outhdnhas	bufdeltn
outhasup	outhuphas	buf1nout
pmtmode1	pmt1mode	bufupmov
pmtfile1	pmt1file	bufdnmov
pmtline1	pmtline	mbuffer
		Action
		† Scroll down until any key pressed
		† Move to end of line
		† Go to line <n>
		† Move to beginning of line
		† Scroll up until any key pressed
		† Erase from cursor to end of line
		† Move from cursor to end of line
		Abort changes to current line
		Enter command mode
		Delete character before cursor
		Delete character at cursor; same as (BACKSPACE) at end of line
		Insert mode: add line below current line, move to the new line
		Edit mode: move to next line
		Insert at cursor: move current line down, enter insert mode
		Enter insert mode
		Delete character at cursor; same as (BACKSPACE) at end of line
		Insert mode: add line below current line, move to the new line
		Edit mode: move to next line
		Delete current line
		† (Zero on auxiliary keypad); search for character and move cursor to character
		† (Period on auxiliary keypad); delete characters from cursor up to but not including <char>
		Enter command mode
		† Move cursor alternately to top and bottom of screen
		Split line at cursor
		Join current line with line above (if room)
		Enter command mode
		+ Up one line
		+ Down one line
		Left one character
		Right one character
		(All others)
		Any other printing key (and TAB key) act as follows:
		Edit mode: replace cursor with character;
		Insert mode: insert character
DOCUMENTATION FOR "C" EDITOR – FEBRUARY 28, 1983		
CP/M-H19-H89 Version written by Edward K. Ream; Dr. Dobb's Journal, January 1982, V. 7, Issue 1; modified by Alan D. Howard.		
COMMAND MODE COMMANDS: (May be entered in upper or lower case)		
Command	Arguments	Function
append	<filename>	Insert the named file into buffer at cursor
change	<from to>	Make indicated changes in lines in range <from to>
clear		Erase the buffer
closeread		Close the read file
closewrite		Copy <n> lines from <from> to before <to>
copy	<from to n>	Delete all lines in the range <from to>
delete	<from to>	Make the indicated file the write file; no error if file already exists
delname	<filename>	Exit from editor to operating system
dos	<from to>	Write the designated lines to write file
extract		Search for a pattern; enter edit mode
find	<n>	Go to line <n> and enter edit mode
g		List the command mode commands to the screen
help		Do not show tabs in reverse video (default)
hidetab		List the indicated lines to printer
list		Open the indicated file as the read file; clear the buffer; load the buffer from the file; close the read file if entire file read
load	<filename>	Move <n> lines from <from> to before <to>
move	<from to n>	Make the indicated file the write file; error if file already exists
name	<filename>	Open the indicated file as the read file
open	<n>	Read <n> lines from read file to end of buffer; close the read file if entire file read
read	<filename>	Clear the write file; open new write file
rename		Close the write file; open the buffer from read file; close the read file if entire file read
rest		Clear the buffer if requested; load the buffer from read file; close the read file if entire file read

(Continued on page 43, column

End Figure 1

(Continued on page 42, column 2)

Screen Editor Enhancements

(Text begins on page 38)

```
/* EHO.C */

/* INCLUSION FILE */
#define MAXLEN 80 /* 80 character video screen */
#define MAXLENi 81 /* MAXLEN+1 */
#define SYSFNMAX 15

#define EOS 0
#define OK 1
#define ERR -1
#define EOF -2
#define YES 1
#define NO 0
#define CR 13
#define LF 10
#define TAB 9
#define HUGE 32000

/* EPI.CCC */

/* INCLUSION FILE */
#define UFL 10
#define DOWN1 13
#define DOWN2 4
#define LEFT1 25
#define RIGHT1 18
#define INS1 14
#define EDIT1 5
#define DEL1 27
#define ZAF1 26
#define ABT1 24
#define SFTL1 19
#define JOIN1 16
#define BTCH 1
#define GTOCH 2
#define HOME 6
#define LSTRT 11
#define GOTD 7
#define RSCRL 17
#define LEND 12
#define USCRU 15
#define ERASE 20
#define SCRNU 81
#define SCRNU1 80
#define SCRNL 24
#define SCRNL1 23
#define SCRNL2 22
#define SCRNL3 13
#define LISTW 80
#define LFTDEL 8

/* INCLUSION FILE */
#define MAXLEN 80 /* 80 character video screen */
#define MAXLENi 81 /* MAXLEN+1 */
#define SYSFNMAX 15

#define EOS 0
#define OK 1
#define ERR -1
#define EOF -2
#define YES 1
#define NO 0
#define CR 13
#define LF 10
#define TAB 9
#define HUGE 32000

/* EPI.C */

#include edo.c
#include edi.ccc

#define SIGNON "E.K. Ream/Dr Dobb's Editor - H89/H19 enhancement: Feb. 20, 1983"
#define HELP1 "open <f/n> ; name <f/n> ; rename <f/n> ; delname <f/n> ; read <n> ; write <n> ; save"
#define HELP2 "load <f/n> ; append <f/n> ; rest <f/n> ; closehead ; closewrite"
#define HELP3 "extract <f/t> ; closehead ; closewrite"

main(argc,argv) char *argsv[]; {
    int mode;
    int i;

    syscout(ESC1); /* kaltternate keyboard mode*/
    fntassn(NO);
    fntset(B);
    fnttab();
    outclr();
    outxy(0,SCRNL1);
    message(SIGNON);
    outxy(0,1);
    bufnew();
    mode=CMNDMODE;
    if (*argsv>1) {
        open(argv[1],NO);
        rest(argsv[0]);
        outxy(0,1);
    }
}

/* In main() if (argsv>1) {
    edsetln();
    while(1){
        if (mode == EXITMODE) {
            break;
        }
        else if (mode==CMNDMODE) {
            mode=command();
        }
        else if (mode==EDITMODE) {
            mode=edit();
        }
        else if (mode==INSMODE) {
            mode=insert();
        }
        else {
            serr("main: no mode");
            mode=EDITMODE;
        }
    }
}

char sbuffer[SCRNL1];
int v;
int xy;
char c;
pmedit();
while(1<c=syscin()> {
    if (c==ESC1) { /* to lower eliminated */
        return(CMNDMODE);
    }
    else if (c==INS1) { /* enter insert mode */
        return(INSMODE);
    }
    else if (c==UPL) { /* DOWN1 now treated separately */
        if (c==UPL) {
            if (special(c)==YES) {
                return(INSMODE);
            }
            else if (c==UPL) {
                continue;
            }
        }
        else if (c==DOWN1) { /* DOWN1 now does not */
    }
}
```

(Continued on page 43, column 2)

(Continued on page 44, column 1)

Screen Editor Enhancements

(Listing continued, text begins on page 38)

```
ednsplit();
fmtline();
return(YES);

} /* enter insert mode but */
   /* just advances to next line */

} /* else if (c==RIGHT) { */
   /* so to end of line */
   edright();
   fmtcol();
}

} /* else if (c==ERASE) { */
   /* erase to end of line */
   ederase();
   fmtcol();

} /* else if (c==HOME) { */
   /* move cursor alternatively to */
   /* top and bottom of screen */
   edhome();
   fmtline();

} /* else if (c==ISTRTR) { */
   /* move to beginning of line */
   edbsinr();
   fmtcol();

} /* else if (c==DSCROL) { */
   /* scroll down */
   fmtmode("edit; scroll");
   while (bufrobot()==NO) {
      if (chkes()==YES) {
         break;
      }
      if (eddn()==ERR) {
         break;
      }
   }
   fntedit();
}

} /* else if (c==END) { */
   /* move to end of line */
   edend();
   fmtcol();

} /* else if (c==GOTO) { */
   /* so to line entered */
   x=outxset();
   y=outyset();
   fmtcmd("edit; soto; \"$buffer\"");
   if (numbs(buffer,&x)) {
      edso(>x);
   }
   else {
      outxy(x,y);
   }
   fntedit();

} /* else if (c==DTOCH){ */
   /* kill to character entered */
   c=sscinr();
   if ((special(c)==NO) &
       (control(c)==NO)) {
      edkill(c);
   }
   fntedit();

} /* else if (c==GTOCH){ */
   /* search to character entered */
   c=sscinr();
   if ((special(c)==NO) &
       (control(c)==NO)) {
      edsrch(c);
   }
   fntedit();
```

```

        fmtcrlf();
        fmtnode("command"); */
        setcmd(args,0);
        fmtcrlf();
        fmtnode();
        c=args[0];
        if ((chkey() == YES) {
            break;
        }
        if (edup() == ERR) {
            break;
        }
    }

    fmtedit();

    else {
        /* editor now exchanges any other character */
        if ((special(c) == NO) & /* with cursor */
            (control(c) == NO)) {
            edching(c);
        }
        fmtcol(); /* only need to update column number */
    }

    else {
        /* editor now exchanges any other character */
        if ((special(c) == NO) & /* with cursor */
            (control(c) == NO)) {
            edching(c);
        }
        fmtcol(); /* only need to update column number */
    }

    insert();
    char c;
    fmtnode("insert");
    while(1) {
        c=special();
        if (c==ESC1) {
            return(CMNDMODE);
        }
        else if (c==EDIT1) {
            return(EDITMODE);
        }
        else if (c==INS1) {
            ;
        }
        else if (c==DOWN1) { /* in insert mode DOWN1 treated */
            edrewdn();
            /* differently from edit mode */
            /* inserts new line */
            continue;
        }
        else if (special(c)==YES) {
            if ((c==UP2) & (c==DOWN2)) {
                return(EDITMODE);
            }
            else {
                continue;
            }
        }
        else if (control(c)==YES) {
            continue;
        }
    }
    else {
        edins(c);
        fmtcol();
    }

    **** control(c) unchanged *****
    special(c) char c;
    int k;
    if (c==JOIN1) {
        edjoin();
        fmtline();
        return(YES);
    }
    if (c==SPLT1) {

```

(Continued on page 44, column 2)

(Continued on page 46, column 1)

Screen Editor Enhancements

(Listing continued, text begins on page 38)

```
        }
        while ((bufatbot() == NO) & (bufin() <= to)) {
            /* do the writing */
            n=bufatIn(databuf,MAXLEN);
            if (pushline(writefile,databuf,n)==ERR)
                break;
        }
        if (bufdn() == ERR) {
            break;
        }
        bufsa(oldline);
    }
    open (args,flag) char *args; int flag;
    /* open a file for reading */
    char locfn [SYSFNMAX];
    int n;
    int file;
    int toline;
    if (readfile > 0) {
        /* check for open readfile */
        message("read file still open");
        return;
    }
    if (flag==YES) {
        if (name1(args,locfn)==ERR)
            return;
    }
    else {
        if (name3(args,locfn)==ERR)
            return;
    }
}
if (locfn[0]==EOS) {
    message("no file argument");
    return;
}
if (chkbuf() == NO) {
    return;
}
if ((file==sysopen(locfn,"r"))==ERR) {
    message("file not found");
    return;
}
sysopen(locfn, rfilename);
readfile = file;
ptrfile(rfilename);
bufnew();
}
setit (args) char *args;
/* new: add n lines to buffer if room */
int n;
int toline;
int nlines,npoint;
if (readfile == -1) {
    message ("no read file");
    return;
}
ptrfile(rfilename);
if (setsar(args,&nlines)==ERR)
    return;
if (nlines<1)
    return;
if (bufsa(HUGE)==ERR)
    return;
npoint=1;
while (npoint<=nlines) {
    /* add the lines */
    if ((nreadline(readfile,databuf,MAXLEN))>=0) {
        /* new */
        extract(args);
    }
    else if (lookup(args,"extract"))
        /* new */
    }
}
```

```

else {
    message("command not found.");
}

/* **** lookup() and setcmd() unchanged **** */

/* ED.C */
#include ed0.c
#include ed1.ccc
int readfile = -1; /* separate read and write files */
int writefile = -1; /* -1 for inactive, file channel no. for active */
char filename[SYSNAME]; /* file names */
char filename[SYSNAME]; /* single buffer replaces redundant buffers */
char databuf[MAXLEN]; /* initializes filenames to zero length */
fileclear()
{
    rfilename[0] = EOS;
    wfilename[0] = EOS;
}

/* **** append() unchanged ****/
/* **** the only alterations to chrcse() are in amatch() references
   1st reference was: if(amatch(oldline,oldpart),NO)==YES {
      is now: if (amatch(oldline,oldpart),NO)==0 {
   2nd reference was: if (amatch(oldline,oldpart,col++)==YES) {
      is now: if ((col==amatch(oldline,oldpart,YES))>=0) {
****/

clear()
{
    if (chrbuff()==YES)
        outclr();
    outsys(0,SCRNL1);
    bnew();
    message("buffer cleared");
    return(YES);
}
else
    return(NO);
}

/* **** delete() unchanged ****/
find()
{
    return(suarch(bufIn(1,HUGE,YES));
}

/* **** list() unchanged ****/
extract(aras) char *aras; /* write indicated line range to writefile */
int oldline, from, to; /* if (writefile == -1) {
   message("file not opened");
}
oldline = bufIn();
if (se12ars(aras,&from,&to)==ERR) /* from and to are line range */
    return;
if (bufso(from)==ERR)
    return;
if (bufso(to)==ERR)
    return;
}

if (bufdn()==ERR) {
    message("line truncated");
    n=MAXLEN;
}
if (bufIns(databuf,n)==ERR) {
    toline=max(1,bufIn()-SCRNL2);
    bufout(toline,2,SCRNL2);
    return;
}
if (bufdn()==ERR) {
    break;
}
}
else {
    npoint = HUGE; /* reached end of file - close it */
    sysclose(readfile);
    readfile = -1;
    rfilename[0] = EOS;
    fmtrFile(rfilename);
}
bufso(1);
toline=max(1,bufIn()-SCRNL2);
bufout(toline,2,SCRNL2);
bufso(toline);

rest (args) char *args; /* new: try to read rest of readfile into buffer */
{
    int n;
    int toline;
    if (readfile == -1) {
        message ("no read file");
        return;
    }
    fmtrFile(rfilename);
    /* if buffer has been cleared and not saved, give option of clearing */
    /* otherwise add to end of buffer */
    if (bufins() == YES) {
        fintout('buffer not saved, clear? ',0);
        fintline();
        if (tolower(fscout(sscinc())=='y')) {
            outclr();
            outsys(0,SCRNL1);
            bnew();
            message("buffer cleared");
        }
    }
    else {
        outclr();
        outsys(0,SCRNL1);
        bnew();
        message("buffer cleared");
    }
}
else {
    outclr();
    outsys(0,SCRNL1);
    bnew();
    message("buffer cleared");
}

while (n=readline(readfile,databuf,MAXLEN))>=0 {
    if (n>MAXLEN) {
        message("line truncated");
        n=MAXLEN;
    }
    if (bufins(databuf,n)==ERR) {
        toline=max(1,bufIn()-SCRNL2);
        bufout(toline,2,SCRNL2);
        bufso(toline);
        return;
    }
    if (bufdn()==ERR) {
        break;
    }
}

(Continued on page 46, column 2)
(Continued on page 50, column 1)

```

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Automatic Indent/Indent	Yes	No	No
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Repeat function key	Yes	Yes	No
Text move and copy	Yes	Yes	Yes
Scratchpad buffers	10	Only 1	No
Load/Save buffers on disk	Yes	No	No
Flexible command mode	Yes	Yes	No
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Screen Editor Enhancements

(Listing continued, text begins on page 38)

```
        }
    }

    sysclose(readfile); /* close file if all read */
    readfile = -1;
    rfilename[0] = EOS;
    pmtwfile(rfilename);
    bufso(1);
    topline=max(1,bufIn()-SCRNL2);
    bufso(topline);
    bufso(topline);

} rename(args) char *args; /* new: change name of writefile */
{
    if (writefile == -1) {
        message("no write file - call name");*
        return;
    }

    sysclose(writefile);
    writefile = -1;
    rfilename[0]=EOS;
    if (name0(args,wfilename)==ERR) {
        return;
    }

    pmtwfile(wfilename);

} name0(args) char *args; /* altered definition: name the writefile */
{
    if (writefile > 0) {
        message("write file open");
        return;
    }

    if (name0(args,wfilename)==ERR) {
        return;
    }

    pmtwfile(wfilename);

} name0(args,wfilename) char *args, *wfilename; /* names new writefile */
int file;
{
    args=skipargs(args);
    args=skipbl(args);
    if (sschfn(args)==ERR) {
        return(ERR);
    }

    syscfn(args,wfilename);
    if ((ffilesysopen(wfilename,'r'))!=ERR) {
        sysclose(file);
        message("disk file exists");
        return(ERR);
    }

    if ((file=sysopen(wfilename,'w'))==ERR) {
        return(ERR);
    }

    writefile = file;
    return(OK);
}

delname(args) char *args; /* new: similar to old resave command, */
/* but doesn't automatically write file */
{
    if (writefile > 0) {
        message("write file open");
        return;
    }

    if (name2(args,wfilename)==ERR) {
        return;
    }

    pmtwfile(wfilename);
}

oldline=bufIn(); /* close the writefile if error */
if (bufso(1)==ERR) {
    sysclose(writefile);
    writefile = -1;
    rfilename[0]=EOS;
    pmtwfile(wfilename);
    return;
}

while (bufatbot()==NO) {
    n=bufsetin(dbtobuf,MAXLEN);
    n=min(n,MAXLEN);
    if (pushline(writefile,dbtobuf,n)==ERR) {
        break;
    }
    if (bufdn()==ERR) {
        if (bufatdn()==ERR) {
            break;
        }
    }
}

bufso(oldline); /* writefile no longer closed */

bufso(oldline); /* new: close the readfile */

closeread() /* new: close the readfile */
{
    if (readfile != -1) {
        sysclose(readfile);
        readfile = -1;
        rfilename[0]=EOS;
        pmtwfile(rfilename);
    }
}

closewrite() /* new: close the writefile */
{
    if (writefile != -1) {
        sysclose(writefile);
        writefile = -1;
        rfilename[0]=EOS;
        pmtwfile(wfilename);
    }
}

***** search() unchanged *****

***** the only alterations to search() are in amatch() references
1st reference was: if (amatch(line,pat1,0)==YES) {
    is now: if (amatch(line,pat1,NO)==0) {
2nd reference was: if (amatch(line,pat1,NO)==YES) {
    is now: if ((col==amatch(line,pat1,YES))>0) {
Also - search() is now called sourch()

***** tabs() and chkbuff() are unchanged *****/

setargs(args,val) char *args; int *val; /* new: set a single argument */
{
    args=skipargs(args);
    args=skipbl(args);
    if (*args==EOS) {
        *val=1;
        return(ERR);
    }

    if (number(args,val)==NO) {
        message("bad argument");
        return(ERR);
    }

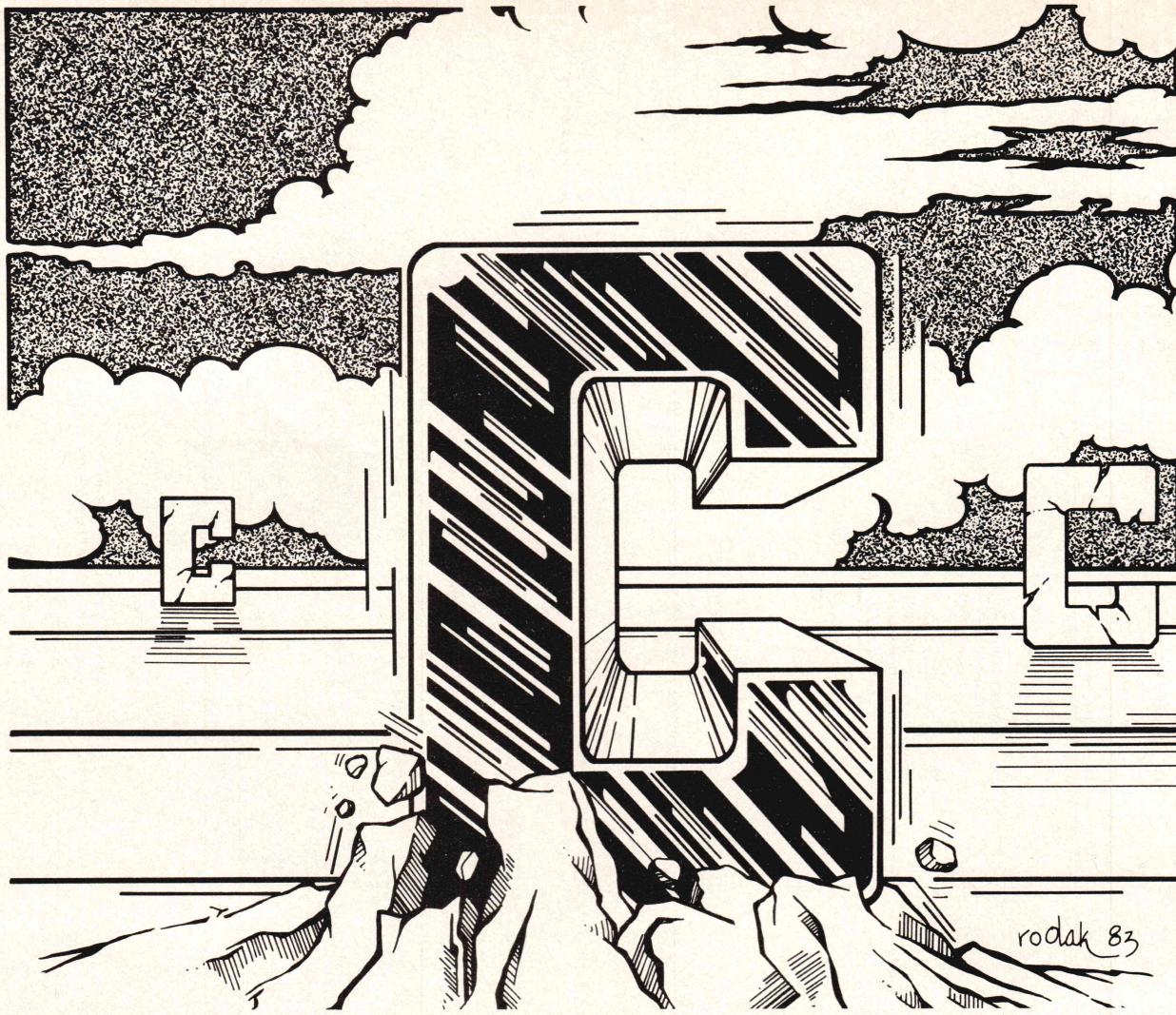
    return(OK);
}

/* new: set three arguments
set3args(args,val1,val2,val3) char *args; int *val1, *val2, *val3;
*/

```

(Continued on page 50, column 2)

(Continued on page 54, column 1)



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Documentation	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Ease of Use	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Error Handling	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>

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Screen Editor Enhancements

(Listing continued, text begins on page 38)

```
*include ed0.c           *include edi.ccc
int fmtrev;             int fmtrev;
int fmttab;              int fmttab;
int fmtdrv;              int fmtdrv;
int fmctab[MAXLEN];     int fmctab; /* new - sets mode to show tabs in reverse video */
int fmctab();            fmctab();
fmctab(); /* new - sets mode to show tabs in reverse video */

moveit (ars) char *ars; /* new: moves *len* lines from *from* */
{                                /* to *to* - same as copyit except deletes */
    int from,to,len;          /* old lines */
    int torline;
    if (set3ars(ars,&from,&to,&len)==ERR) {
        return;
    }
    if (to == from) {
        return;
    }
    if ((to>from)&(to<=(from+len))) {
        message("interleaving not permitted");
        return;
    }
    bufcopy((from,to,len));
    if (to>from) {
        bufso(from);
        bufndel(len);
    }
    else {
        bufso(frontlen);
        bufndel(len);
    }
    bufsol1;
    torline=max(1,bufln(~SCRNL2));
    bufout(torline,2,SCRNL2);
    buso(torline);
}

/* ED4.C */
#include ed0.c
#include edi.ccc
char editbuf[MAXLEN];
int editf;
int editmax;
int edcflas;
int edrep1;

/****** edabt() and edbstat() unchanged *****

ederase() /* new: erase from cursor to end of line */
{
    if (edit==editmax) {
        return;
    }
    edcflas=YES;
    editmax=editp;
    eddraw();
}

edhome() /* new: home cursor. subsequent calls alternate */
{
    int ypos;
    if (edrep1()==OK) {
        return;
    }
    if ((ypos>editp)<SCRNL3) {
        while((ypos<SCRNL1)&&(!bufnrbt())) {
            if (bufdn()==OK) {
                return(ERR);
            }
            ypos++;
        }
    }
}
```

```

    }
    else {
        while((ypos>1)&&(bufattop())){
            if (bufp!=0) {
                return(0);
            }
            ypos--;
        }
        edsetin();
        outxy(edxpos(),ypos);
    }
    edchns(c) char c;
    char oldc;
    int k;
    if (editp>editpmax) {
        edinst(c);
        return();
    }
    oldc=editbuf[editp];
    editbuf[editp]=c;
    fmtadv(editbuf,editp,editpmax);
    k=fmtlen(editbuf,editpmax);
    if (k>SCRNMAX) {
        editbuf[editp]=oldc;
        fmtadv(editbuf,editp,editpmax);
    }
    else {
        edcf1ag=YES;
        editp++;
        if (c==TAB) {
            /* only need to redraw if TAB char */
            edredraw();
        }
        else {
            fmtchdev(c);
        }
    }
}
/* **** eddel() in original version now called ed1del(): no change in code *****/
eddel() /* deletes character at cursor - ed1del() deletes prior character */
{
    int k;
    if (edexPos() < outxset()) {
        outxy(outxset()-1,outxset());
        return();
    }
    edcf1ag=YES;
    if (editp == editpmax) {
        if (editp==0) {
            return();
        }
        editp--;
        editpmax--;
        edredraw();
        return();
    }
    k=editp;
    while (k<(editpmax-1)) {
        editbuf[k]=editbuf[k+1];
        k++;
    }
    editpmax--;
    edredraw();
}
/* **** all remaining ED4.C functions unchanged ****/
/* ED5.C */

```

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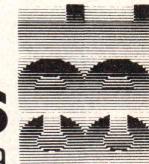
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Screen Editor Enhancements

(Listing continued, text begins on page 38)

```

        outxy(0,0);
        outdelln();
        fmtline();
        pmcol(x);
        pmfile(s); /* add writefile */
        pmimode(pmmln);
        outxy(x,y);

    } else {
        pmwfile(s) char *s; /* new: writefile */
        int x,y;
        x=outxget();
        y=outyget();
        outxy(0,0);
        outdelln();
        fmtline();
        pmcol(x);
        pmfile(pmtrfn);
        pmfile(s);
        pmimode(pmmln);
        outxy(x,y);

    }
    pmedit();
    {
        pmmod("edit: ");
        /* add blanks to assure erasing "command" */

    }
    /* ED7.C */
    #include ed0.c
    #include ed1.ccc
    char pmtnMAXLEN; /* now both read and write filenames */
    char pmtnFSNMAXJ;
    pmess(s1,s2) char *s1, *s2;
    int x,y;
    x=outxget();
    y=outyget();
    outxy(0,0);
    outdelln();
    fmtline();
    pmcol(x);
    pmfile(pmtrfn);
    pmfile(pmmln);
    pmfile(pmtrfn);
    pmmod(pmmln);
    outxy(x,y);

    pmmod(s) char *s;
    int x,y;
    x=outxget();
    y=outyget();
    outxy(0,0);
    outdelln();
    fmtline();
    pmcol(x);
    pmfile(pmtrfn);
    pmfile(pmtrfn);
    pmmod(pmmln);
    outxy(x,y);

    pmmod(s) char *s;
    int x,y;
    x=outxget();
    y=outyget();
    outxy(25,0);
    if (*s==E05) {
        if (*s==E05) {
            fmtout("no rdfile",25);
        }
        else {
            fmtout(s,25);
        }
        i=0;
        while (pmtn[i++]= *s++) {
            if (*s==E05) {
                s++;
            }
        }
    }
    pmrfile(s) char *s; /*changed: readfile */
    int x,y;
    x=outxget();
    y=outyget();
}

```

(Continued on page 58, column 2)

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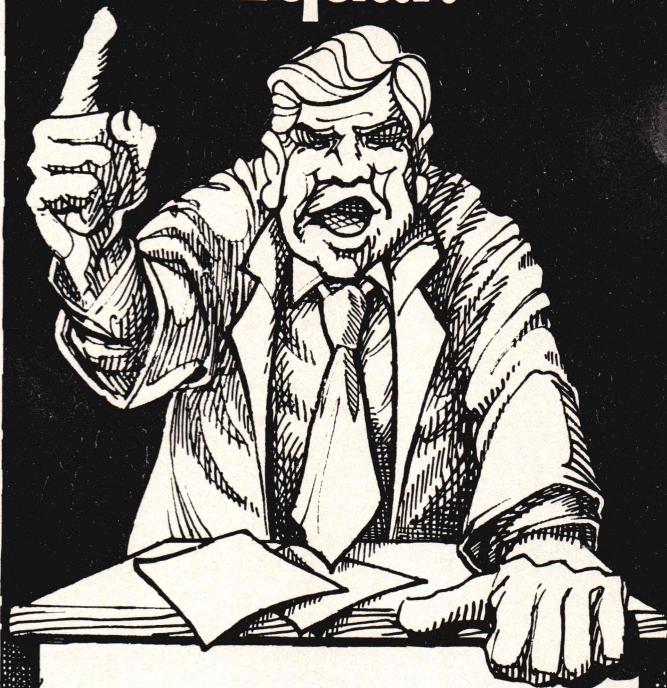
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Screen Editor Enhancements

(Listing continued, text begins on page 38)

```
    } else if (c=='LF') { /* map LF to CR for Software Toolworks */
        return(CR);
    } else {
        return(c);
    }
}

syschck(c,file) char c; int file;
{
    if (c==CR) { /* map CR to LF for Software Toolworks */
        if (putc(LF,file)==-1) {
            error("disk write failed");
            return(ERR);
        }
    } else if (putc(c,file)==-1) {
        error("disk write failed");
        return(ERR);
    } else {
        return(c);
    }
}

/* *****
 * Pmt2file(s) char *s; /* new: write out writefile name or "no wrtfile" */
int i;
outxy(40,0);
if (*s==EOS) {
    fmtout("no wrtfile",40);
}
else {
    fmtout(s,40);
}
i=0;
while (fmtwfn[i]==*s++) {
}
}

/* *****
 * Pmtline(), Pmtcol() changed in name to Pmtline(), Pmtcol() *****/
-----
```

```
/* END.C */
/* altered for Software Toolworks compiler with M80 option */
#include edi.c
#include cmcall.h
char c;
/* new: cmcall absent from original listing */
asm
P0P H           ; RETURN
P0P D           ; CHAR C -- INPUT OR OUTPUT
P0P B           ; INT 0 -- OFFCORE
PUSH B
PUSH D
PUSH H           ; REPLACE RETURN
CALL 5
MOV L,A
MOV H,O
RET
/* endasm
 * added */
ssstat()
{
    int c;
    c=cmcall(6,-1);
    if (c==0){ /* correction of original version */
        return(-1);
    }
    else {
        return(c);
    }
}

/* *****
 * extensivly altered to recognize escape sequences */
int src;
srcin()
{
    /* extensivly altered to recognize escape sequences */
    /* from HI9/H99 special keys */
    while ((c=cmcall(6,-1))==0) {
        if (c==ESC1) {
            if (c==ESC1) {
                while ((c=cmcall(6,-1))==0) {
                    if (c=='?') {

```

```
else if (c=='LF') { /* map LF to CR for Software Toolworks */
    return(CR);
} else {
    return(c);
}
}

syschck(c,file) char c; int file;
{
    if (c==CR) { /* map CR to LF for Software Toolworks */
        if (putc(LF,file)==-1) {
            error("disk write failed");
            return(ERR);
        }
    } else if (putc(c,file)==-1) {
        error("disk write failed");
        return(ERR);
    } else {
        return(c);
    }
}

/* *****
 * syschkfn() unchanged *****/
-----
```

```
syschkfn(char *args)
{
    if (args[0] == EOS) { /* add check for zero-length file name */
        message("no file name");
        return(ERR);
    } else {
        return(OK);
    }
}

/* *****
 * sysdmove(n,r,s) int n,r,s;
 * extensivly altered to allow LDUR for Z80 processors using parity check
 * for processor type. Also checks for zero-length move to avoid moving
 * everything */
#asm
P0P PSW          ; RETURN
P0P H           ; SOURCE
P0P D           ; DESTINATION
P0P B           ; LENGTH
PUSH FSW         ; RESTORE RETURN
MOV A,C
ORA R
JZ SYSIN3
HVI A,2
INR JPE
SYSIN1 DB
OEHQ08H JMF
SYSIN3
;
SYSIN1: MOV A,B
ORA C
SYSIN3
JZ SYSIN3
MOV A,M
STAX D
DCX H
-----
```

(Continued on page 62, column 1)

(Continued on page 60, column 2)

Screen Editor Enhancements

(Listing continued, text begins on page 38)

```

return;
    }
    if (bufatbot() {
        outdeol();
    }
    else {
        c=fmtsout(buf,0);
        if (c<=SCRN1) {
            outdeol();
        }
    }
}

***** bufext() unchanged *****/

```

```

F8W
LINAD
SHLD
XCHG
SHLD
FATAD
A,O
HVI
ORA
JNZ
MTCH1
ORI
B
FSW
#Z FLAG IS 'NO'
LXI
B,O
MOV
A,M
CPI
0
MTCH3
#ZERO LENGTH MATCH STRING - NOMATCH
JZ
MTCH2
#SUCCESS SO FAR
D
LIXX
CMP
H
MTCH2
JZ
CPI
O
#END OF LINE
JZ
MTCH3
#NOMATCH
FOP
FSW
PUSH
JZ
MTCH3
LINAD
LHLD
H
INX
B
LINAD
SHLD
XCHG
FATAD
LHLD
A,M
MOV
MTCH5
JMP
2
FATAD
DS
2
MTCH2:
INX
H
INX
D
MOV
A,M
HOU
CPI
0
MTCH4
#TOTAL SUCCESS
JZ
MTCH5:
LXI
B,-1
FOP
FSW
HOU
H,B
L,C
MOV
HOU
*endsm
}
sysdelay()
{
    /* sets scrolling rate for scroll up or down - modify by
       changing number loaded into D register - smaller value for slower scroll */
asm
    ANA
    A
    LXI
    D,10
    H,O
    LDI
    D
    D0M0R
    JNC
*endsm
}

-----*
/* ED9.C */
#include ed0.c
#include edi.ccc

```

```

    if (bufatbot() {
        outdeol();
    }
    else {
        c=fmtsout(buf,0);
        if (c<=SCRN1) {
            outdeol();
        }
    }
}

***** bufext() unchanged *****/

```

```

/* c= added */
/* now allows full screen */

```

```

bufstreq is bufext modified to allow entering beyond buffer with warning */
bufstreq(lenth) int lenth;
{
    if ((buffmax+lenth)>=bufend) {
        bufdmov(buf,buffer,buffmax+lenth);
        buffmax+=buffmax+lenth;
        message("Caution: main buffer is full");
        return(ERR);
    }
    bufdmov(buf,buffer,buffmax+lenth);
    buffmax+=buffmax+lenth;
    return(OK);
}

buflenth(first,n) int first; /* new: returns length of n lines from */
/* first line specified */
char *start;
int i,lenth;
{
    char *start;
    if (n<=0) {
        len=0;
        return(len);
    }
    if (bufso(first)==ERR) {
        len=0;
        return(len);
    }
    start=bufp;
    i=1;
    line = first;
    while (i++<n) {
        if (bufso(i+line)==ERR) {
            len=0;
            return(len);
        }
    }
    len=bufp-start;
    return(len);
}

bufcopy(from,to,n) int from,to,n;
{
    int lenth;
    char *source,*dest;
    if (from==to) {
        return;
    }
    if ((lenth=buflenth(from,n))!=0) {
        if (bufso(from)==ERR) {
            return;
        }
        source=bufp;
        if (bufso(to)==ERR) {
            return;
        }
    }
}

```

```

***** END.C functions unchanged *****/
/* END.C */

#include b:ed;c
#include b:ed;.ccc
int bufclst;
char *bufp;
char *bufmax;
char *bufend;
int bufmaxln;
/* buffer is external for Software Toolworks compiler to force it beyond
code sections */
extern char *buffer[1];
bufnew()
{
    bufp=bufmax=bufbuffer+1;
    /* need more room for Software Toolworks stack and buffers */
    bufend=ssyrend-1500;
    bufline=1;
    bufmaxln=0;
    buffer[0]=CR;
    buffer[1]=NO;
}

/****** bufln() *bufchc() *bufsaved() *buffree() *bufgo() *bufup() *bufdn() */

bufins() unchanged *****/
bufins(p,n) char *p; int n;
{
    int k, x;
    x = 0;
    if(bufstrch(n+1)==ERR)
        x = 1;
    k=0;
    while (k<n)
        *(bufp+k)=*(p+k);
    if ((r=n)&(bufp+k)==CR)
        bufmaxln++;
    if ((r=n)&(bufp+k)<bufbot())
        return(ER);
    else {
        bufcflas=YES;
        if (x == 0)
            return(DR);
        else
            return(ERR);
    }
}

/****** bufdel(), bufrefl(), bufsetln(), bufdmov(), bufupmov(),
bufatbot(), bufattop(), bufatbot(), bufattop(), bufout() unchanged *****/
buflnout(line) int line;
int c;
if (bufso(line)==ERR)
    fmtsout("disk error: line deleted",0);
    outdeol();
}

```

End Listing

(Continued on page 62, column 2)

Shifts and Rotations on the Z80

The shift and rotation commands on the Z80 are powerful commands. They allow simple division and multiplication. Along with the BIT command, they provide powerful control over the eight bits in each byte. But for all the brilliant applications, how does one know which specific type of shift or rotation one needs? Since I could not find an explanation of the discrete differences between these commands, I compiled the following with a little research and experimentation. Hopefully it will prove helpful when employing these powerful commands in any future assembly/machine language endeavors. Note that whenever the command is "xxx r," the "r" refers to any single, unpaired Z80 register other than "F" and a couple of other things. Thus, r can be A, B, C, D, E, H, L, (HL), (IX+d), or (IY+d).

(1) RR r — Rotate Right thru Carry. This command moves each bit in the chosen register to the right. The bit in position 0 goes to the carry flag, and the carry flag status is placed into bit position 7. This command is valuable because it allows for examining each individual bit's status. Each time RR r is performed, the next bit is placed in the carry, allowing appropriate action — e.g., "JR C,BITON." An example of this command may be found in the program in Figure 1 at right.

(2) RLC r — Rotate Left Circular. This moves each bit in the specified register to the left. The bit in position 7 of the register is put in position 0. The use of this rotate is probably less common. It can multiply a number by two if the result is less than 255. If the result is greater than 255, the status of bit 8 (256th's place, which is really the ninth bit and so normally not there) will be stored in bit position 0. Bit position 0 will hence equal 1 if the result is greater than 255, and 0 if the result is less than or equal to 255. Figure 2 at right shows a program using RLC. Notice that it uses the "A" register but it could use any of the "r" values.

(3) RL r — Rotate Left thru Carry. This moves each bit to the left, but the bit in position 7 goes to the carry flag and the carry flag status is put into bit 0. This

command can be used like RR r, except that this rotates the other direction. If it is used for multiplication and the result is greater than 255, the carry flag will be set.

(4) RRC r — Rotate Right Circular.

This moves each bit to the right. The bit in position 0 of the byte is put into position 7. This command works like RLC r except it rotates the other direction and hence it divides.

(5) SLA r — Shift Left Arithmetic.

With this command all of the bits are shifted to the left. The status of bit 0 is always 0 after this shift is used, and bit 7 is lost. This is best described in Table 1 on page 66. This command has a somewhat more useful multiply ability for most applications. It multiplies by two and leaves 0 at bit position 0. After multiplying any binary number by two, bit position 0 should equal 0 because the answer is even.

(6) SRA r — Shift Right Arithmetic.

All of the bits are shifted to the right. The status of bit 7 remains unchanged, but bit 0 is lost. Although this command can be used for division, the main problem is that it leaves bit 7 on and hence it will

give an inaccurate result if the number to be divided is greater than 127. There may be some practical reason for leaving bit 7 on, but I am unaware of it.

(7) SRL r — Shift Right Logical.

This command is very similar to SRA r. All of the bits are again shifted to the right, and bit 0 is lost. But with this command, the status of bit 7 is always set to 0 after a shift. This makes this command far more practical for division than SRAr. Unlike SRAr, which will only give an accurate result if the number being divided is less than 128, this will give an accurate answer if the number to be divided is less than or equal to 255 (which will always be the case if it is being stored in an 8-bit register).

Table 1 shows how each of the commands operates. Each one is performed several times on a number to provide easy visualization of the command's results.

DDJ

(Table 1. on page 66)

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```
00100 ; THIS PROGRAM WILL PRINT THE BINARY REPRESENTATION
00110 ; OF THE 'A' REGISTER ON A TRS-80 SCREEN AT
00120 ; SCREEN LOCATION 3C00H (15360 DECIMAL).
00130 ;
00140 PRINT LD B,8
00150 LD HL,3C07H
00160 LOOP RRA
00170 LD A,48
00180 JR NC,ZERO
00190 LD A,49
00200 ZERO LD (HL),A
00210 DEC HL
00220 DJNZ LOOP
00230 RET
```

Figure 1.

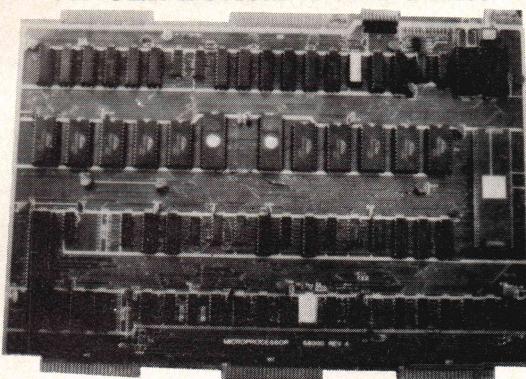
```
00100 ; PROGRAM TO MULTIPLY 'A' BY 2. PRODUCT MUST BE
00110 ; LESS THAN OR EQUAL TO 128.
00120 ; THE COMMAND RLCA DOES SAME THING AS RLC A
00130 ;
00140 MULT2 RLC A
00150 RES 0,A
00160 RET
```

Figure 2.

by Ron Goodman

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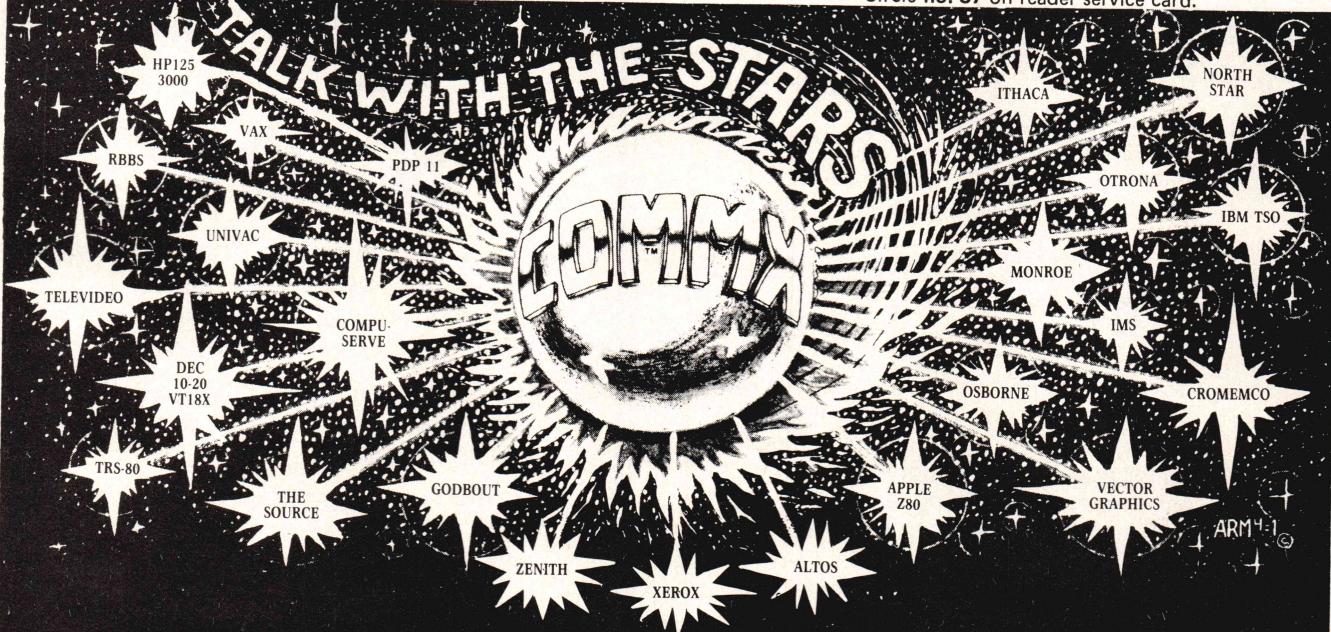
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RR	THE BYTE	CARRY	RRC	THE BYTE	SRA	THE BYTE	SRL	THE BYTE
	1 1 1 1 1 1 1 1	0		0 0 0 0 0 0 0 1		1 0 1 0 0 0 0 0		1 0 1 0 0 0 0 0
	0 1 1 1 1 1 1 1	1		1 0 0 0 0 0 0 0		1 1 0 1 0 0 0 0		0 1 0 1 0 0 0 0
	1 0 1 1 1 1 1 1	1		0 1 0 0 0 0 0 0		1 1 1 0 1 0 0 0		0 0 1 0 1 0 0 0
	1 1 0 1 1 1 1 1	1		0 0 1 0 0 0 0 0		1 1 1 1 0 1 0 0		0 0 0 1 0 1 0 0
	1 1 1 0 1 1 1 1	1		0 0 0 1 0 0 0 0		1 1 1 1 1 0 1 0		0 0 0 0 1 0 1 0
	1 1 1 1 0 1 1 1	1		0 0 0 0 1 0 0 0		1 1 1 1 1 1 0 1		0 0 0 0 0 1 0 1
	1 1 1 1 1 0 1 1	1		0 0 0 0 0 1 0 0		1 1 1 1 1 1 1 0		0 0 0 0 0 0 1 0
	1 1 1 1 1 1 0 1	1		0 0 0 0 0 0 1 0		1 1 1 1 1 1 1 1		0 0 0 0 0 0 0 1
	1 1 1 1 1 1 1 0	1		0 0 0 0 0 0 0 1		1 1 1 1 1 1 1 1		0 0 0 0 0 0 0 0
	1 1 1 1 1 1 1 1	0		1 0 0 0 0 0 0 0		1 1 1 1 1 1 1 1		
RL	THE BYTE	CARRY	RLC	THE BYTE	SLA	THE BYTE		
	0 0 0 0 0 0 1	0		0 0 0 0 0 0 1		1 1 1 1 1 1 1 1		
	0 0 0 0 0 0 1 0	0		0 0 0 0 0 0 1 0		1 1 1 1 1 1 1 0		
	0 0 0 0 0 1 0 0	0		0 0 0 0 0 1 0 0		1 1 1 1 1 1 0 0		
	0 0 0 0 1 0 0 0	0		0 0 0 0 1 0 0 0		1 1 1 1 1 0 0 0		
	0 0 0 1 0 0 0 0	0		0 0 0 1 0 0 0 0		1 1 1 1 0 0 0 0		
	0 0 1 0 0 0 0 0	0		0 0 1 0 0 0 0 0		1 1 1 0 0 0 0 0		
	0 1 0 0 0 0 0 0	0		0 1 0 0 0 0 0 0		1 1 0 0 0 0 0 0		
	1 0 0 0 0 0 0 0	0		1 0 0 0 0 0 0 0		1 0 0 0 0 0 0 0		
	0 0 0 0 0 0 0 0	1		0 0 0 0 0 0 1		0 0 0 0 0 0 0 0		
	0 0 0 0 0 0 0 1	0		0 0 0 0 0 0 1 0		0 0 0 0 0 0 0 0		

Table 1.

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SBC, TSX and TXS

Instructions of the 6800 and 6502

The 6800 and 6502 microprocessors have very similar architectures and instruction sets. The common instructions of these two microprocessors have been listed elsewhere^{1,2} and the purpose of this note is to draw attention to three common instructions which have subtle but important differences in their operation on the two MPUs. These are:

- SBC** Subtract with carry (on the 6800 the mnemonic is followed by A or B indicating the accumulator concerned)
- TSX** Transfer stack pointer to index register
- TXS** Transfer index register to stack pointer

SBC

The differences in the carry-bit operation for the subtract instruction of the 1802 and Z80 microprocessors have been pointed out by Merrin.³ Likewise, the differences in SBC between the 6800 and 6502 are due to the different ways in which the two MPUs utilize the carry bit of their status registers. Though SBC is nominally described as subtract with carry, a better description would be subtract with borrow.

The add with carry instruction, ADC, is also common to the 6800 and 6502, and functions identically on them. The operation can be represented as

$$A + M + C$$

where A is the contents of the accumulator, M the operand, and C the carry bit of the status register. The result is put into the accumulator and the resulting carry bit is stored in C. The resulting carry bit will be 1 only if the result exceeds 255. In the case of the 6502, ADC is the only addition instruction, and if an add without carry is required, the carry bit must be cleared first by preceding ADC with CLC (clear carry bit).

To understand the SBC instruction better, let us assume that the status registers of both MPUs have another status bit called the borrow bit. The operation of the SBC instruction can then be represented as

$$A - M - B$$

by B.T.G. Tan

B. T. G. Tan, Department of Physics,
National University of Singapore.

where B is the borrow bit. B, as well as M, is subtracted from A, and the result is put into the accumulator. The borrow bit resulting from the operation will be stored in B. The resulting borrow bit will be 1 only if the absolute value of (M + B) is greater than that of A. The result of the operation goes into the accumulator. The function of B is to facilitate multi-byte subtraction when borrow bits from one 8-bit subtraction have to be utilized in the subtraction for the next higher 8-bits.

If the subtraction without borrow is required, the borrow bit must be cleared before SBC. We can invent two pseudo-instructions

SEB	Set borrow bit
CLB	Clear borrow bit

to enable us to manipulate B, analogously to SEC and CLC for C. Hence we must precede SBC with CLB for a subtract without borrow.

In the 6800 and 6502 MPUs, there is no separate borrow bit in their status registers. The carry bit C does double duty as a borrow bit, so that it is better described as a carry/borrow bit. In the 6800, the borrow bit is identical with the carry bit, so that the SBC operation can be represented as

$$A - M - C$$

and the borrow bit generated is directly put into C. The instructions SEB and CLB correspond to SEC and CLC respectively.

In the 6502, the borrow bit is the inverse of the carry bit, so that SBC is

$$A - M - \bar{C}$$

and the borrow bit generated is inverted before being put into C. Hence the SEB instruction corresponds to CLC and CLB to SEC. For a subtraction without bor-

row, SBC would have to be preceded by SEC.

The difference is thus best understood by considering the borrow bit B as entirely separate from C and then taking B = C for the 6800 and B = \bar{C} for the 6502. The implementation of the borrow bit for the 6502 is different from the 6800 in order that arithmetic operations on the 6502 would give more consistent results for C.

For example, the arithmetic operations 5 - 3 and 5 + (-3), using the two's complement in the second operation, would lead to different values for C in the case of the 6800. These operations would give the same values for C in the case of the 6502, since its borrow bit is the inverse of C. In Table 1 (below), the two operations are listed for each MPU, showing the corresponding values of the accumulator and the carry bit after each instruction. These operations were verified on SWTPC 6800 and Apple II microcomputers. The 6809, which is the upgraded version of the 6800, appears to implement the borrow bit in a similar manner to the 6800, that is with the borrow bit the same as the carry bit.

TSX and TXS

The transfer instructions between the stack pointer SP and index register X, TSX and TXS are common to both 6800 and 6502 but have important differences. In both MPUs, the current value of SP points to the location one address below the bottom of the stack. These registers are 16-bits long in the 6800, and 8-bits long in the 6502.

In the 6800, TSX increments the contents of SP by 1 and then transfers them to X. The reason for this appears to be to make X point to the actual bottom

6800				6502			
	ACCA	C	B=C		ACC	C	B=
LDAA #\$05	05	D	D	LDA #\$05	05	D	D
CLC	05	0	0	SEC	05	1	0
SBCA #\$03	02	0	0	SBC #\$03	02	1	0
LDAA #\$05	05	D	D	LDA #\$05	05	D	D
CLC	05	0	0	CLC	05	0	1
ADCA #\$FD	02	1	1	ADC #\$FD	02	1	0

Table 1.
5 minus 3 subtraction operations for 6800 and 6502. D indicates don't care state.

of the stack. This is presumably for cases when indexing operations on the stack are to be carried out. Conversely, TXS decrements the contents of X by 1 before transferring them to SP, so that if X pointed to the bottom of the stack, SP would then point to the next location below the bottom.

The 6502's TSX and TXS instructions do not perform the increment and decrement operations; they are just straightforward data transfer instructions. Why is this so? SP is an 8-bit register, and its contents ZZ are always interpreted as pointing to the page 1 of memory, \$01ZZ. The effective address in indexed addressing is found by adding the 8-bit contents of X to the operand. In the zero-page indexed mode, the effective address is in page zero; in the absolute indexed mode, the page of the effective address depends on the 16-bit operand.

Because of these complications, the TSX and TXS instructions of the 6502 do not appear to be intended to facilitate indexed operations on the stack, as the transfers are effected without incrementing and decrementing. The primary function of TSX and TXS appears to be to effect data transfer to and from SP via X. This is in fact the only way to do so, since the 6502 lacks LDS (load to stack pointer) and STS (store from stack point-

er) instructions. These instructions are part of the 6800's instruction set.

The 6809 MPU, which is the upgraded version of the 6800, has the instructions TFR S,X and TFR X,S which are equivalent to TSX and TXS, respectively. In the 6809, the stack pointer points to the bottom of the stack, and not to the location below the bottom as in the case of the 6800. Thus, the two transfer instructions do not perform an increment or decrement before the transfer, and are more like their 6502 equivalents, but for a quite different reason.

Conclusions

The 6800 and 6502 MPUs are so similar that it is worth looking at both together when studying one of them. I have taken this approach in my microprocessor teaching, using a MPU model consisting of the common features of the two MPUs.² This MPU model has one 8-bit accumulator, one 8-bit index register, one 16-bit program counter, one 8-bit stack pointer, and one 8-bit status register. It has the same six addressing modes of the 6800, except that indexed addressing is restricted to the zero page only. The instruction set is made up of the 43 instructions common to both 6800 and

(Continued on page 85)

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A=L;	L1:	MOV A,L
M(BC)=A;	L2:	STAX B

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SOFTWARE REVIEW

ZAS Z8000

Software Development Package
By Western Wares, P.O. Box C, Norwood,
CO 81423
CP/M Version \$395
ISIS Version \$495
Reviewed by Terry R. Dettmann
Circle No. 151 on Reader Service Card.

What is there to say about an assembler? It either assembles good code or it doesn't. ZAS assembles functioning Z8000 code (consistent with your own ability to design such code in the first place!).

Is there any more to say? Should a review really continue after this point? Well in some cases I'd say no it shouldn't, but in this case there is more to the package that is important to know about. More than the package can do. What more should I say? Well, let's look and see just what you get when you get the assembler package.

First there's the ZAS assembler itself. It is a "full-featured, relocatable cross assembler for the Z8000." Its instruction mnemonics are compatible with those developed by Zilog. Comparing it to Matosian's book on the Z8000 (SYBEX), it didn't always agree on how some things should be done, but it was easy to learn.

ZAS also includes the "extended" instructions needed for some Z8000 units. It wasn't hard to use. It generated good code for the Z8000, but has only really been tested on limited development systems. I haven't tried to code a large project.

ZAS is a two-pass assembler. It has all the standard things you expect to find in a full-featured assembler such as conditional assembly, named sections, segmented and non-segmented addressing support, nested include files, up-to-64-character symbol names, and standard directives and pseudo-ops.

After the assembler comes the ZLK task builder. ZLK is a flexible linking program that allows complicated processes to be set up as overlays and so forth, while allowing the programmer complete control over memory placement of the code modules.

ZLD is an absolute file loader program. Its basic purpose is to load a Z8000 module under CP/M or ISIS so that it can be executed with the ZEX run-time monitor. ZEX allows the user's system to provide run-time support as needed for a Z8000 task. ZEX allows bus-switching between 8080-type CPUs and Z8000 CPUs, as on the IEEE 696 (S-100) bus or the Intel Multibus. It even allows the

program to make CP/M or ISIS calls as needed.

In this kind of mode, the Z8000 CPU is acting as if it were a temporary bus master device and the 8080 CPU is the bus master. All I/O is handled by the bus master (the 8080) through the CP/M system.

ZEX is configured by the user to the particular environment. Detailed instructions are provided to allow proper configuration by anyone with reasonable technical knowledge. This isn't a system for the novice Z8000 programmer, but it is a useful tool for serious system development.

Editor's note: Just before press time, we were informed that version 2.0 of the ZAS Z-8000 package had been released. Some of the enhancements will be of particular interest to those familiar with older versions of ZAS. The following new features were added in the new version, which was received too late for review in this issue: new macro pseudo-operations (MACRO, IRP, IRPC, EXITM, REPT, ENDM, LOCAL), other new pseudo-ops (DZ - define zero, DA - define address), new directives (\$BASE - set default number base; \$LIST, MACON, MACOFF - control macro expansion listing), new comparison operators taking string or numerical arguments, extended instruction enhancement, more symbol table space and faster searching of tables, more flexibility for input, command-line switches specified by - or /, and ZAS and ZLK now use the same command-line syntax.

6809 Cross Assembler

Available from Avocet Systems Inc.,
804 South State Street, Dover,
DE 19901

\$200

Reviewed by Terry R. Dettmann
Circle No. 153 on Reader Service Card.

It wasn't long ago that I came up with an acute need for a 6809 assembler. Not just a small one either. Further, I was lazy and I wanted full-screen editing ability, flexible printing, and large capacity. In short, I wanted a large system! In fact, all I had was a small system.

It was then that the Avocet ad in some of the computer magazines caught

my eye. As a CP/M cross assembler for the 6809, my dreams were fulfilled. I could have all the powerful CP/M tools I wanted and still be able to put things together for a 6809. I could document my code in as much detail as I wanted. I could modularize as much as I wanted. I could work with 6809 code in a convenient way.

It wasn't long before I started putting together some 6809 software including some communications packages and a simple Forth system. I found out just how lazy I had become when I tried to go back to the little 6809 assembler I had. It was good, but just wasn't made for the kind of programming I was doing.

Avocet produces a whole line of cross assemblers which include assemblers for the following processors: 6805, 6809, RCA1802 (COSMAC), Intel MCS-48 & UPI-41 families, Intel 8051, MOS 6502 & 650X family, Motorola 6800/6801 family, NEC 7500 family, Fairchild F8, MOS-TEK F8/3870 family, National COP400, and the Zilog Z8 family. Quite a list.

No assembler is worth its salt, though, unless it can produce good code. The Avocet 6809 assembler does as far as I have gone with it.

So far, I haven't run across any errors not traceable to my own programming errors. If the assembler would only assemble what I want instead of what I type. But the point is that it does do what I type!

The basic assembler syntax is roughly the same as for a standard 8080 assembler including standard pseudo-ops such as DW, DB, etc. The write-up with the system says that all the assemblers stay with these conventions. I'm not sure I agree that this is the best though I'm sure it makes development and maintenance easier for the Avocet people.

Personally, I would rather have seen the pseudo-ops closer to those used by the standard system for the 6809, but I can live with anything as long as it produces the code I want.

The assembler produces code in one of two output formats, either Intel Hex format or MIKBUG format. I use the Intel Hex format mostly out of habit.

This is a nice assembler with reasonable documentation for the experienced programmer. It carries out the job I want it to do; after all, isn't that what it should do?

DBJ

BCPL — The Language and Its Compiler
by Martin Richards
and Colin Whitby-Strevens
Cambridge University Press
173 pages, \$10.95
Reviewed by George W. Jolly

They say you can't tell a book by its cover, but sometimes I'm not so sure. The cover of *BCPL* is a gentle interplay of two colors, blue and green, producing results you might think would require three or more inks. *BCPL* is a language very much in that style, favoring careful design over expensive implementation.

BCPL (Basic CPL) is a systems programming language designed by Martin Richards in 1967. The language, developed mostly over the subsequent five years, has been installed on at least twenty-five different computer systems. It has been used for applications including operating systems, compilers, editors, databases — in other words, for a broad spectrum of systems work.

In *The C Programming Language*, Kernighan and Ritchie credit the language *BCPL* for many of C's most important ideas. *BCPL* influenced the language B written for UNIX by Ken Thompson in 1970, and B passed this influence on to C. However, *BCPL* and C are quite different, perhaps most importantly in the handling of data types.

Many modern languages have a variety of data types built into the compiler. Such types might be integer, floating point, character, pointer, and so on. *BCPL*, on the other hand, has only one type, the machine word. The compiler wastes no time identifying the legality of a construct for the data items involved, because all data items are alike. In *BCPL*, address variables, or pointers, are easy to use because indirect addressing is a fundamental operator. Any data item may contain an address.

One interesting result of this inexpensive philosophy is that the elements of an array need not all be the same "type." (Try having integers, characters and pointers in the same array in standard FORTRAN!) Other languages do provide this, but as an extra feature. In *BCPL* it is a consequence of the underlying design. (Funny how FORTH comes to mind at this point.)

Example *BCPL* routines illustrate the power of the language. One example given is a formatted output procedure ac-

cepting a format string (analogous to a FORTRAN format) as the first argument, and the data list as subsequent arguments. A procedure receives its arguments as an array of addresses. It is a simple matter to scan this array with a pointer variable, accessing each parameter in turn. Another pointer variable holds the address of an appropriate editing procedure as indicated by the format string. This is a simple procedure with elegant results.

Other example programs include an interactive debugging program and the lexical/syntax scanner from the *BCPL* compiler. The latter is discussed in detail, with a page of program facing a page of explanatory text. The applicative expression tree produced by the scanner is an interesting topic in itself.

Another interesting topic of this book is language portability. The authors illustrate a method of bootstrapping the compiler onto a new system by generating a simplified pseudo-code on an existing installation. They also discuss common library procedures that exist in most *BCPL* installations. These topics may be of interest to implementors of other languages.

In summary, *BCPL* is a well-written book serving several possible needs. It describes an influential (and useful) computer language, illustrates an elegant programming philosophy, and gives practical examples of compiler techniques. The authors' straightforward explanations, like the cover design, complement the elegant simplicity of *BCPL*.

User's Guidebook to Digital CMOS Integrated Circuits
by Eugene R. Hnatek
McGraw-Hill
339 pages, \$29.00
Reviewed by Eunice B. Stetson

User's Guide to Digital CMOS Integrated Circuits by Eugene R. Hnatek is not a book for the uninitiated. It is most likely to appeal to design engineers and sophisticated hobbyists. It assumes some knowledge of semiconductors, small- and medium-scale logic circuits, conversions between digital and analog signals, and computer hardware. Very little knowledge of mathematics beyond the ability to read complex graphs is assumed. The book is very practical in orientation.

Many applications, with schematics, manufacturers' names, and part numbers, are included.

The author makes clear the electrical characteristics of CMOS as compared with other IC logic families. He does not pretend to discuss all CMOS integrated circuits but concentrates instead on medium- and large-scale IC's developed since 1977.

The author uses many schematics and graphs and some charts and timing diagrams to explain the exciting and promising developments in CMOS and SMOS/SOS (silicon on sapphire) technology and applications in telecommunications, computers and process controllers. He envisions many new uses for CMOS large-scale integration, particularly in the interfacing of analog and digital signals on a single IC. Currently, applications of CMOS microprocessors are concentrated in the automotive, games, and TV industries rather than in the computer field.

The author does an outstanding job of making his graphs understandable with labelling and notes on the graphs. Circuit diagrams for MSI and LSI integrated circuits are arranged in a logical sequence with explanation.

In discussing flip-flops, memory cells, and shift registers, the author uses schematics with FET symbols rather than logic symbols. Later he treats counters, timers, multiplexers, comparators, decoders, VARTs, and microprocessors as black boxes. As a teacher I would have preferred a more orderly progression from FETs to logic symbols to flip-flops to shift registers, multiplexers, etc. as the basic building blocks of which circuits are composed. However, in that this book is intended as a reference book and not a text book, this should not be a problem. As with all books which use manufacturers' data and diagrams, there are differences in symbols and conventions with which the reader is used to coping.

Generally, the author's explanations are clear, though as with most authors what follows "it can be seen that . . ." is not always obvious. The author clearly knows his subject well. At times he makes beautifully clarifying statements. This book, compared with others in its field, is clearly written, supplemented with many excellent pictorials, and does an authoritative job in the areas covered.

(Continued on page 72)

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Book Reviews

(Continued from page 70)

Highfalutin' Computin' with Bob Orrfelt
On your Timex Sinclair 1000™
Computer
By Bob Orrfelt, 3436 Bay Road,
Redwood City, CA 94063
\$9.95, 116 pages
Reviewed by David S. Lacey

Bob Orrfelt's book on the TS-1000 brings back an occasion when I submitted a somewhat hastily put together paper in a college English course, and got it back with a fairly good grade, but with a notation from the professor, "This is so good I wish it were better." He explained that this was his way of saying he liked my ideas, but felt the presentation just wasn't up to the content. If I had taken more pains in the preparation, I could have had a strong "A" instead of a marginal "B+." I think something very similar applies to *Highfalutin' Computin'*.

The book has lots to offer — things to instruct or amuse readers with a variety of viewpoints and interests. It has much for readers who are just getting an introduction to computers via the TS-1000. It also has considerable material for others already more versed in computers and their technology but interested in the particulars of the TS-1000 which, at under \$100, is so attractive for experimenting. Unfortunately, the book's diversity is one of the underlying faults that brings the "grade" down. Mr. Orrfelt does not seem to have settled very firmly in his own mind on a particular audience for his effort. He shifts, sometimes rather abruptly, from one level of technical sophistication to another and may leave some less knowledgeable readers feeling rather baffled from time to time. I believe, from my own experience, that if readers persist through areas that seem to have them out of their depth, they will reach places in which they feel at ease again, and can profit from doing so. However, the book would have been more satisfactory if it had either stuck to one level, or built steadily from the simplest to the most sophisticated levels.

There is definitely more technical information in this book than others I have encountered on the TS-1000 or its predecessors, the Sinclair ZX80 and ZX81. This includes some fairly elementary but very practical hardware coverage. For those who can't abide the membrane keyboard, there are instructions on how to get the case open and connect a "real" keyboard in place of the membrane one. There is a circuit for obtaining a composite video signal to drive a monitor (instead of the modulated RF carrier normally output for connection to the antenna terminals of a TV receiver). Also presented is the complete circuit of an automatic control for a cassette re-

corder (the normal storage medium for the TS-1000). This information includes interfacing, both hardware and software, to make it entirely workable. They are all very simple but practical circuits, and can be regarded as introductions to some of the exploratory possibilities of the computer.

There is a chapter that comprises a fairly detailed explanation of the internal organization of the computer, its operating cycles and its files. Programs for printing out the contents of the files are given, and the whole approach is designed to help enhance the reader's ability to write more compact, faster-running programs in BASIC.

There are a number of program listings of varying complexity throughout the book. These include games (a simple arcade-type game, a demonstration of moving graphics, a crap game, and blackjack); "useful" programs (length, weight, and volume converters, etc.); and what may be called "general interest" programs (such as two clock programs — one a dial face and one a digital clock with large numerals). None of these listings are presented for their own sake, rather as part of a tutorial approach that starts with a brief explanation of Sinclair BASIC. Throughout the rest of the book, the programs are explained in considerable detail regarding how desired results are obtained and with repeated emphasis on compacting — an essential when working with only 2K of RAM. The programming material culminates in a glimpse at machine code programming, though more than a glimpse is beyond the scope of this book.

Unfortunately, the subject of programs brings me to the second important fault which I find in Orrfelt's effort. There are several errors in programs. True, I found no true programming errors. Rather, they are typographical in nature, but they make some of the programs run faultily, or not at all. I can say that I, who am not much beyond the novice level, have been able to correct all the errors I found without too much difficulty. I may have learned valuable lessons in the essential art of debugging, but that isn't why they are there. Programs that do not run right do not belong in a "grade A" computer book.

Despite the faults mentioned, and weighing them against the virtues of a considerable amount of good technical information and explanation, I feel that readers who are in the earlier stages of learning the TS-1000 can, through a little persistence, gain their full money's worth from *Highfalutin' Computin'*.

We are informed that the book now comes with an addendum which corrects typographical errors such as those noted by the reviewer. — Ed.



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CP/M EXCHANGE

by Robert Blum

Digital Research created a stir when they announced CP/M Plus (alias CP/M 3) several months ago. Preliminary speculation on the system's content ranged from a simple maintenance release to full concurrent operation.

If you consider yourself a hacker, a unique individual who treats inadequacy as an opportunity, CP/M Plus will be bittersweet. Many of its performance options are and have been available in the public domain for years. If nothing else, CP/M Plus verifies that when hackers speak, the manufacturers listen. It just takes them a little while to react.

Even though I had hoped for more, the official release is anything but disappointing. It is chock-full of new features geared to performance and versatility, and is a major departure from the CP/M we have grown to love and respect. DR views CP/M Plus as a new product and it will be maintained and marketed separately. At least for now, it will not obsolete CP/M 2.2.

Over the next few months I will be covering various aspects of CP/M Plus: does it work as advertised; how difficult is it to implement; is it worth a \$350 investment? Getting to know a new product cannot be effectively accomplished alone. Share your experiences and thoughts with the rest of us by dropping me a note or calling me in the evening. Your input will be appreciated by one and all.

Before getting into this month's topic, I need some help. Is there a patch for CP/M 2.2's CCP that will automatically search user area 0 for a .COM file that was not found in the current user area? If you have one, please send it along or point me to where I can find it.

After a long wait, CP/M Plus is now being shipped. By the time this column is published, it will probably be available through mail order and possibly from a few manufacturers. This estimate may be a little optimistic because implementation of all CP/M Plus's new features requires considerable attention to both hardware and software.

Even though I have not received my own distribution version of CP/M Plus, I have been able to gather up two of the three manuals and spend a few hours using it. What a pleasant surprise! The documentation has been completely rewritten and downsized to wedgy format. Each topic is thoroughly discussed in plain English, with only minimal use of

buzz words, and is accompanied by plenty of examples.

Sitting at the keyboard for the first time was disappointing. I had hoped for concurrent operation, or at least a built-in print spooler. CP/M Plus remains single user and still prompts with the familiar A>. I soon learned, however, that all other aspects of the system are new, and verified that this is a completely new product. After only a few minutes at the keyboard I was comfortable with the operator interface and swear by the additional line-editing features. For those who are new to CP/M, the HELP command, which displays summarized information on all the commands, will undoubtedly prove invaluable.

The actual performance of the system, and which features can be implemented, is predicated on how the system is generated. As a replacement for CP/M 2.2 in a standard 64K or less environment (nonbanked), CP/M Plus offers less than 96K of banked memory when available.

Command-line editing on nonbanked systems remains the same as CP/M 2.2, but is greatly improved on banked systems. In the banked environment it is no longer necessary to retype a command if a mistake is made. Simply move the cursor to the error and correct it by inserting or deleting characters. Even after hitting return, commands can be redisplayed and edited provided a transient program was not executed. Refer to Table 1 on page 76 for a complete comparison of the available editing functions.

A host of new commands has been added to those that are carried over from older versions of CP/M. DIRectory, ERAse, REName, TYPe, and USEr are improved while DIRSys has been added to display system files which previously required the use of STAT. Depending on command-line options, transient programs may be automatically loaded to assist the built-in commands of the CCP. I have summarized the built-in commands in Table 2 (page 76). The descriptions used for each command are excerpts from DR's manuals. I do this so that you can sample the flavor of the new documentation.

Enhanced command editing is only a part of the beauty of CP/M Plus. Many new utilities have been added and those that were retained from older versions are now more useful. Also included is the entire RMAC program development package which previously cost \$200. Refer to

Table 3 (page 78) for a summary of the utility programs.

Before generating the system, you are faced with a dilemma. The minimum system configuration has been increased to 32K and CP/M Plus requires 8.5K. By the time you add in the size of your BIOS a great deal of memory has been taken up, even when a full 64K is available. To make full use of the system requires at least 96K of bank-select memory. Through the use of this additional memory, it is entirely possible to have a 62K TPA because only a minimum BIOS and BDOS are resident in the application bank. The real controlling logic is placed in the other bank along with directory hash tables and sector buffers. Because of this, I would anticipate the introduction of more 8-bit machines with 256K of memory in the not-too-distant future.

Disk space reserved for the system tracks has been reduced by placing the operating system components into regular disk files. This will be especially important for those systems that require very large BIOSs. When booting the system, a small loader from the system tracks is read into memory, which then completes the loading process. In light of more common use of hard disks, individual file size has been increased to 33 megabytes and disk capacities of 512 megabytes are possible.

Generation of the system is handled by a transient program called GENCPM which combines the various system components with your BIOS. The generation process is remarkably similar to that used with MP/M, as are most other features of CP/M Plus.

CP/M Plus is not for everyone. Its initial cost is high and it requires expanded hardware resources if it is to be used to its fullest extent. But the costs associated with its implementation will not seem nearly as high if you need all the development tools which are included. Functionism is the keyword to describing this new offering from Digital Research.

Next month I will continue with the internals of CP/M Plus and any experiences that I hear from you. You can reach me by phone at (404) 449-8948. **DDJ**

(Tables I and II on page 76)

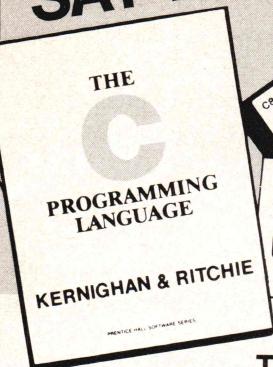
(Table III on page 78)

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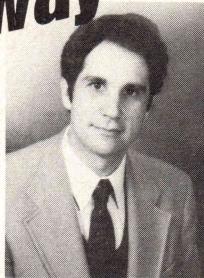
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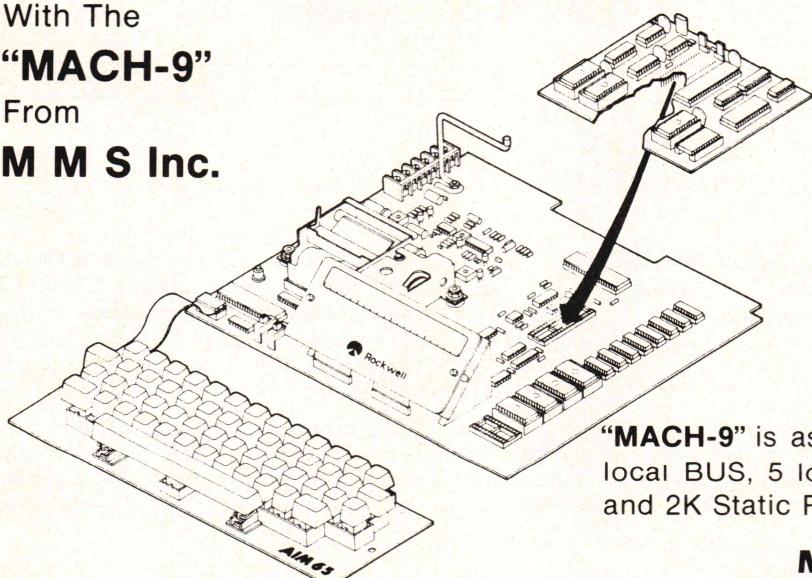
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TABLE I

Nonbanked Command Editing

- Ctrl-E** Forces a physical carriage return but does not send the command line to CP/M 3. Moves the cursor to the beginning of the next line without erasing your previous input.
- Ctrl-H** Deletes a character and moves the cursor left one character position.
- Ctrl-I** Moves the cursor to the next tab stop. Tab stops are automatically set at each eighth column. Has the same effect as pressing the tab key.
- Ctrl-J** Sends the command line to CP/M 3 and returns the cursor to the left of the current line. Has the same effect as a RETURN or a Ctrl-M.
- Ctrl-M** Sends the command line to CP/M 3 and returns the cursor to the left of the current line. Has the same effect as a RETURN or a Ctrl-J.
- Ctrl-R** Places a # at the current cursor location, moves the cursor to the next line, and displays any partial command you typed so far.
- Ctrl-U** Discards all characters in the command line, places a # at the current cursor position, and moves the cursor to the next command line.
- Ctrl-X** Discards all the characters in the command line, and moves the cursor to the beginning of the current line.

Banked Command Editing

- Ctrl-A** Moves the cursor one character to the left.
- Ctrl-B** Moves the cursor to the beginning of the command line without having any effect on the contents of the line. If the cursor is at the beginning, Ctrl-B moves it to the end of the line.
- Ctrl-E** Forces a physical carriage return but does not send the command line to CP/M 3. Moves the cursor to the beginning of the next line without erasing the previous input.

Ctrl-F Moves the cursor one character to the right.

Ctrl-G Deletes the character indicated by the cursor. The cursor does not move.

Ctrl-H Deletes a character and moves the cursor left one character position.

Ctrl-I Moves the cursor to the next tab stop. Tab stops are automatically set at each eighth column. Has the same effect as pressing the tab key.

Ctrl-J Sends the command line to CP/M 3 and returns the cursor to the beginning of a new line. Has the same effect as a RETURN or a Ctrl-M keystroke.

Ctrl-K Deletes to the end of the line from the cursor.

Ctrl-M Sends the command line to CP/M 3 and returns the cursor to the beginning of a new line. Has the same effect as a RETURN or a Ctrl-J keystroke.

Ctrl-R Retypes the command line. Places a # at the current cursor location, moves the cursor to the next line, and retypes any partial command you typed so far.

Ctrl-U Discards all the characters in the command line, places a # at the current cursor position, and moves the cursor to the next command line. However, you can use a Ctrl-W to recall any characters that were to the left of the cursor when you pressed Ctrl-U.

Ctrl-W Recalls and displays previously entered command line both at the operating system level and within executing programs, if Ctrl-W is the first character entered after the prompt. Ctrl-J, Ctrl-M, Ctrl-U, and RETURN define the command line you can recall. If the command line contains characters, Ctrl-W moves the cursor to the end of the command line. If you press RETURN, CP/M 3 executes the recalled command.

Ctrl-X Discards all the characters left of the cursor and moves the cursor to the beginning of the current line. Ctrl-X saves any characters right of the cursor.

TABLE II
Built-in Commands

DIR The DIR command displays the names of files and the attributes associated with the files. DIR and DIRSYS are built-in utilities; DIR with options is a transient utility.

ERASE The ERASE command removes one or more files from a disk's directory in the current user number. Wildcard characters are accepted in the filespec. Directory and data space are automatically reclaimed for later use by another file. The ERASE command can be abbreviated to ERA.

RENAME The RENAME command lets you change the name of a file that is cataloged in the directory of a disk. It also lets you change several filenames if you use wildcards in the filespecs. You can abbreviate RENAME to REN.

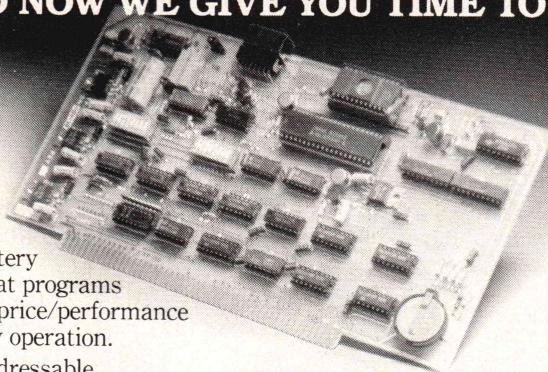
TYPE The TYPE command displays the contents of an ASCII character file on your screen. The PAGE option displays the console listing in paginal mode, which means that the console listing stops automatically after listing N lines of text, where N is usually the system default of 24 lines per page.

USER The USER command sets the current user number. When you start CP/M 3, 0 is the current user number. You can use a USER command to change the current user number to another in the range 0-15.

(Table III begins on page 78)

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TABLE III
Utilities

COPYSYS The COPYSYS command copies the CP/M 3 system from a CP/M 3 system disk to another disk with the same format as the original system disk. For example, if the system disk is single density, the disk you copy onto must also be in single-density format.

DATE The DATE command is a transient utility that lets you display and set the date and time of day. When you start CP/M 3, the date and time are set to the creation date of your CP/M 3 system. Use DATE to change this initial value to the current date and time.

DEVICE The DEVICE command is a transient utility that displays current assignments of logical devices and the names of physical devices. This command also sets the communications protocol and speed of a peripheral device.

DUMP DUMP displays the contents of a file in hexadecimal and ASCII format.

ED The ED utility is a line-oriented context editor. This means that you create and change character files line-by-line, or by referencing individual characters within a line.

GENCOM The GENCOM command is a transient utility that creates a special COM file with attached RSX files. RSX files are used as Resident System eXtensions. GENCOM places a special header at the beginning of the output program file to indicate to the system that RSX loading is required. It can also set a flag to keep the program loader active.

GET The GET command is a transient utility that directs CP/M 3 to take console input from a file. The file can contain CP/M 3 system commands and/or input for a user program. If you use the SYSTEM option, GET immediately takes the next system command from the file.

HELP The HELP command is a transient utility that provides information for all of the CP/M 3 commands described in the manual. In the distributed CP/M 3 system, HELP presents *general* information on a command as a topic, and *detailed* information as a subtopic. HELP with no command tail displays a list of all the available topics. HELP with a topic in the command tail displays information about that topic, followed by any available subtopics. HELP with a topic and a subtopic displays information about the specific subtopic.

HEXCOM The HEXCOM command is a transient utility that generates a command file (filetype COM) from a hex input file. It names the output file with the same filename as the input file but with filetype COM. HEXCOM always looks for a file with filetype HEX.

INITDIR The INITDIR command can initialize a disk directory to allow date and time stamping of files on that disk or remove date and time stamps.

LIB The LIB command is used to create and maintain a library of relocatable object modules. Use the LIB utility to create libraries and to append, replace, select, or delete modules from an existing library. You can also use LIB to obtain information about the contents of library files.

LINK The LINK command combines relocatable object modules such as those produced from RMAC into

a .COM file ready for execution. Relocatable files can contain external references and publics. Relocatable files can reference modules in library files. LINK searches the library files and includes the referenced modules in the output file.

MAC The MAC utility assembles .ASM disk files into .HEX object files. It also has the ability to process macros.

PATCH The PATCH command displays or installs patch N to the CP/M 3 system or command files.

PIP PIP is a transient utility that copies one or more files from one disk and/or user number to another. PIP can rename a file after copying it; combine two or more files into one file; and copy a character file from one disk to the printer or other auxiliary logical output device. PIP can create a file on disk of input from the console or other logical input device. PIP can also transfer data from a logical input device to a logical output device, thus the name Peripheral Interchange Program.

PUT The PUT command is a transient utility that lets you direct console output or printer output to a file. PUT allows you to direct the system to put console output or printer output to a file for the next system command or user program entered at the console. Or PUT directs all subsequent console or printer output to a file when you include the SYSTEM option.

RMAC The RMAC utility is operationally comparable to MAC except its output is in .REL form.

SAVE The SAVE command copies the contents of memory to a disk file. To use the SAVE utility, first issue the SAVE command, then run your program which reads a file into memory. When your program exits, it exits to the SAVE utility. The SAVE utility prompts you for the filespec to which the memory is to be copied, and the beginning and ending address of the memory to be saved.

SET The SET command initiates password protection and time stamping of files in the CP/M 3 system. It also sets file and device attributes, such as the Read-Only, SYS, and user-definable attributes. It lets you label a disk and password-protect the label.

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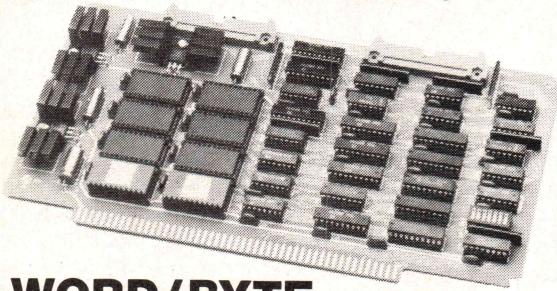
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16-BIT SOFTWARE TOOLBOX

by Ray Duncan

MS-DOS vs. CP/M-86

Jim Howell of San Jose, California, has written me a couple of interesting letters regarding my coverage of the two major operating systems for the 8086/88 microcomputer family. I have taken the liberty of editing some of his comments and my replies into the form of a dialogue for this column, and hope he will forgive me for the slight rearrangements and changes of wording that were necessary.

JH: In the December issue of *DDJ*, you say that the reason for "excessive" coverage of the 8086/88 is a table that indicates more 8086/88s in use than other 16-bit processors. First, the numbers in that table do not necessarily indicate the level of interest in the various processors. For example, I am sure there are many, including myself, who do not have a 68000-based computer, but are nevertheless interested in that processor. In any case, if you are going to use the numbers in that table to justify the contents of your column, you should be devoting 78% of your column to the 8086/88, 15% to the 68000, and 7% to the Z8000. (What about the TI-9900 and National's 16000?).

Second, if you are going to devote 100% of your column to the 8086/88 (which is the case so far), perhaps the column should be called "8086/88 Toolbox" or even "IBM PC Toolbox." The title "16-Bit Software Toolbox" promises broader coverage than just a single processor. Actually, I suspect that the real reason for covering only the 8086/88 is that you own (or have a lot of access to) an IBM PC and have little or no access to systems that use other 16-bit processors.

RD: You certainly have penetrated right to the heart of the matter, since it is true that until very recently I didn't have any access to a 68000 system. But again, this is relevant to the content of the column and is not a coincidence. The long-range objective of the "16-Bit Software Toolbox" is to provide useful subroutines and utilities for the most commonly used 16-bit microprocessors. Although Motorola's 68000 architecture and instruction set are elegant, their decision to make a total break with the architecture of their 8-bit microprocessor family has caused a tremendous lag in the appearance of affordable operating systems and software development tools for 68000-based personal computers.

I do plan to provide increasing coverage of the 68000 in the coming months,

and now have both a Cromemco Dual Z80/68000 CPU and a Godbout 68000 CPU to work with. But I'm still waiting for my copy of CP/M-68K, which was announced and demonstrated at the CP/M-83 show in January but is still not being shipped to customers.

JH: I have noticed that your recent columns have criticized various aspects of MS-DOS (apparently you are "pushing" CP/M-86, for some reason). We have used an IBM PC at work for the past several months, using only PC-DOS, and have had no problems with it.

RD: I do not have any vested interest in either CP/M-86 or MS-DOS, and am not trying to "push" one operating system in preference to another. Since MS-DOS presently dominates the 8086/88 user base, I do feel that its problems (and ways of dealing with them) should receive more space in the column. It is true that I feel the Digital Research family of operating systems and language compilers is much more "robust" than the Microsoft products; this is only an opinion, but it is based on many published comparisons and compiler benchmarks, and on my own extensive experience implementing systems tools on CP/M, CP/M-86, and MS-DOS.

JH: Benchmarks published in the November 1982 column showed PC-DOS outperforming CP/M-86 on two out of three disk-intensive tests. This was followed immediately by several paragraphs entitled "And Now the Bad News" about how changing disks in the middle of an operation under PC-DOS could clobber data on a disk, but with CP/M-86 all you get is a harmless little error message. You neglected to state that any editing you may have done to the file under CP/M-86 is lost. Perhaps that seemed too obvious to mention, but it would have balanced the presentation a little.

In January 1983, a letter from a reader appeared on the above subject and on how to recover most of the data on the damaged disk. This section had the rather negative title: "Coping with PC-DOS." The most eye-catching line in Isaac Davidian's letter was: "Unfortunately, I had no backup...." If this reader had had a backup, he would have had much less of a problem. I have not used CP/M-86, but I used CP/M-80 at a previous job and found its READ-ONLY DISK error rather annoying. I always wondered why the system couldn't automatically do

whatever was required to make the disk not read-only, instead of making the user start over. CP/M-86 apparently handles new disks as READ-ONLY also.

RD: Mr. Howell correctly points out that, if you switch disks at an inappropriate time under CP/M-86, you will lose the contents of the file you are currently working on. However, this is all you will lose. The integrity of the other files on both the new and previous disks is carefully protected by the operating system. The inexcusable part of PC-DOS's handling of new disks is not that you can lose your currently accessed file, but you can lose *all* of the files on the new disk; and the directory is trashed in such a manner that it is nearly impossible to reconstruct it.

This is in direct contradiction to the PC-DOS manual, page D-12, under Function 10H Close File, which states: "This function must be called after file writes to insure [sic] all directory information is updated. On entry, DS:DX point to an opened FCB. The disk directory is searched and if the file is found, its position is compared with that kept in the FCB. If the file is not found in its correct position in the directory, it is assumed the diskette was changed and [register] AL returns X'FF'. Otherwise, the directory is updated to reflect the status in the FCB and AL returns 00." If PC-DOS really handled file closure the way the manual states, everything would be peachy.

As for Mr. Davidian's lack of backups, we are all human and I am sure we have all failed at one time or another to make backup disks as often as would be desirable. The operating system ought to help protect us from our frailties, not exploit them!

JH: My own feelings on CP/M-86 vs. PC-DOS are based on Dave Cortesi's column of several months ago, on my four years of working with CP/M-80, and on six months of working with PC-DOS. CP/M-86 is, as I understand, not much more than a direct copy of CP/M-80. PC-DOS is based on CP/M-80, but not as heavily as CP/M-86. Microsoft and previous authors attempted, with PC-DOS, to make some improvements in moving to the 8086/88, such as:

- Time and date stamping of disk files.
- Changing the name PIP to COPY and making it a built-in command.
- Putting the parameters of COPY and RENAME in the "right" order.
- Allowing disk read and write in blocks

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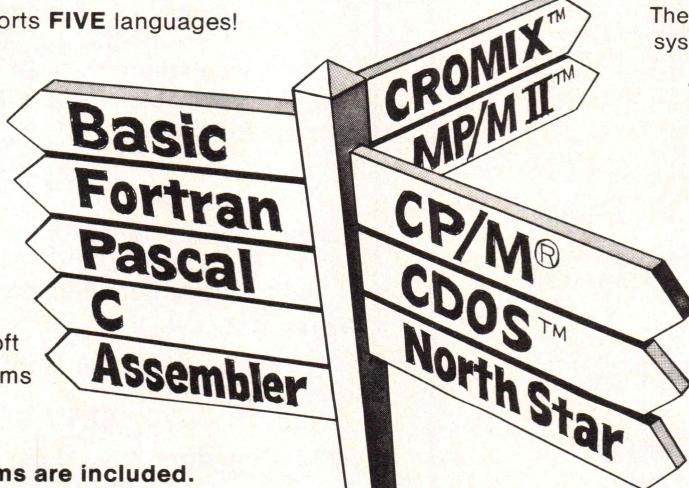
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¹ Iterative loop on CompuPro/Hudson CP/M system (8085 @ 6MHz and 8088/87 @ 5MHz).

² FORTH with 8087 64-bit floating point on IBM P.C., Dr. Dobb's J., Nov. 1982, p. 46.

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[record sizes] of anything the user wishes, not just 128 bytes as with CP/M. There is still much room for improvement here, but it sometimes will allow reading or writing a file with one call to the system (not counting open and close), instead of having to write 128 bytes at a time in some sort of a loop.

- Showing the exact size of the file in the directory listing.
- The ability to use device names, such as CON: and LPT1: as the "filename" in an FCB, which gives some degree of device independence. Again, there is room for more improvement here, since real device independence is what one would really like.
- The batch facility of PC-DOS is much better than CP/M's SUBMIT, which is a real kludge. For example, the AUTOEXEC facility of PC-DOS allows executing a program or series of programs on power up. We use this at work to read the date and time from a battery-powered clock to set the system's date and time. Doing this under CP/M-86 requires, as far as I know, modifying CP/M's boot file (or giving the appropriate com-

mand manually each time the system is booted).

RD: Points granted, with the following reservations: Concurrent CP/M and the new CP/M Plus support time and date stamping of files, and additionally offer the convenience of password protection and "user areas." The new CP/Ms also allow file access in "burst mode" where up to sixteen 128-byte records can be transferred with a single operating system call, but this is still much weaker than the PC-DOS block read/write functions where the record size can be from 1 to 65,535 bytes. Concurrent CP/M has the capability to execute a user-supplied batch file at cold start.

Whether or not PC-DOS's COPY and RENAME parameters are in the "right" order is arguable. CP/M's format (which mimics an assignment statement) certainly seems natural to programmers, but PC-DOS's conventions are easier for new computer users to remember.

In general, I think we will all benefit from the intense rivalry between MS-DOS (and its clones) and CP/M-86. Both systems already show significant improvements over their 8-bit counterparts, and the competition for market share is

bound to bring us many additional enhancements in future versions. MS-DOS 2.0, which is scheduled for release soon, incorporates some sophisticated new features including the ability to load user-designed peripheral device drivers without patching the operating system.

IBM PC Character Set Linker

Patrick Banchy, of New York City, wrote in with several suggested modifications to the CLINK utility that was published in an earlier column.

In order to get the program to work at all, when assembled with the Microsoft Assembler to form an EXE file, several pseudo-commands to define segment start/ends must be inserted. Additionally, the instruction:

MOV 14 [BX],1024

must be modified to:

MOV WORD PTR 14 [BX],1024

(the Microsoft Assembler does not check the magnitude of the immediate data to determine whether a 16-bit transfer is necessary). On his system, instructions also had to be added to initialize byte 32 of the file control block to zero before the file access, although on my PC this

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happens automatically when the program is loaded.

Patrick then made some improvements which can be summarized as follows:

All segment registers of a COM program are set to the start of the Program Segment Prefix (PSP), so the first three instructions of CLINK as listed in DDJ are unnecessary.

DOS function #37 can be used to set the interrupt vector address.

To decrease the amount of memory made unavailable for use by other programs, he put the code that reads in the file after an ORG 480H statement, and used the default disk transfer address (80H). This results in a bigger load module, but smaller memory loss (1152 bytes for my version vs. 1424 bytes for Patrick's).

Finally, he noted that if CLINK is invoked multiple times, it will reserve a new block of memory each time. This could become quite wasteful, so he changed CLINK to check the contents of the vector location at 007CH. If the link is zero, CLINK reads the character (INT 27H); if the link is nonzero, CLINK points the disk transfer address to the previously loaded table area, reads the new table, and then makes a "normal" exit (DOS function 0).

FLIP Utility for the IBM PC

Simson L. Garfinkel sent in a program listing with the following comments:

"I own an IBM PC with both the color/graphics and monochrome adapters installed. IBM makes no effort to support users who have both of these interfaces installed, other than a brief note on page I-8 of the BASIC manual on how to switch from one display to another.

"I have written a small program (see listing on opposite page) which flips from one display to the other. The program is designed to be converted into a COM file with EXE2BIN program, and for this reason has no stack segment (this generates an error when linked which should be ignored).

"When FLIP.COM is executed, if the monochrome display was being used, the color screen is cleared and becomes the system display, and conversely."

Users who have a high-resolution color monitor will probably want to alter the program slightly so that it selects the 80x25 BW text mode when selecting the graphics interface.

DDJ

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Program FLIP

```
comment $  
  
PROGRAM FLIP  
  
This program flips from the color-adapter to the monochrome-adapter  
and back again. The program has no stack segment so that it can be  
converted into a COM file. This generates an error upon linking which  
should be ignored.  
  
Simson L. Garfinkel, 1983  
  
$  
  
data segment at 40h  
equip db ?  
data ends  
  
cseg segment para 'code'  
start proc far  
assume cs:cseg,ss:cseg,ds:data  
  
mov ax,cs  
mov ss,ax  
mov sp,stacke ;get end of stack  
  
push ds  
xor ax,ax  
push ax  
mov ax,data  
mov ds,ax  
mov ah, equip  
and ah,30h  
cmp ah,30h  
jne mono  
  
mono:  
jmp color  
or equip,30h  
mov ax,2  
int 10h  
mov ah,1  
mov cx,12*256+13  
int 10h  
ret  
  
color:  
and equip,0cfh  
or equip,010h  
mov ax,4  
int 10h  
mov al,0  
int 10h  
mov ah,1  
mov cx,6*256+7  
int 10h  
ret  
  
start endp  
db 16 dup (?)  
stacke label near  
cseg ends  
end start
```

SBC, TSX and TXS

(Continued from page 68)

6502. These instructions include SBC, TSX, and TXS, and it is important that those using both the 6800 and 6502 understand the small but important differences in these instructions.

References

¹ Levanthal, Lance R. *6502 Assembly Language Programming*, Osborne/McGraw-Hill, 1979, p. 3-107.

² Tan, B.T.G. "Common instructions of the 6800 and 6502," *Dr. Dobb's Journal of Computer Calisthenics & Orthodontia*, Volume 4, Number 9, October 1979, pp. 38-39.

³ Merrin, Stephen. "Addition and Subtraction: The 1802 Versus the Z80," *Byte*, Volume 6, Number 3, March 1981, pp. 224-228.

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Dynalogic's Hyperion is a truly portable, self-contained 16-bit computer that is compatible with the IBM PC running MS-DOS, and costs under \$3400. The "entry-level" 18-pound Hyperion has 256K RAM, one 320K 5½-inch drive (that reads both single- and double-density IBM PC diskettes), MS-DOS, Microsoft Advanced Disk BASIC with graphics, a 7-inch non-glare amber phosphor screen of 80 columns by 25 lines and five pages of display memory, 256 display characters, bidirectional scrolling, soft-key labels on the 25th display line, serial and parallel ports, and a low-profile, detachable keyboard with 84 keys, including ten function keys and numeric keypad and optional audible key click. Options include a second floppy drive for \$650; a 300-baud, built-in, direct-connect Bell 103J compatible modem with autoanswer and autodial for \$495; Dynalogic's IN:SCRIBE text editor for \$175; Microsoft's Multiplan spreadsheet for \$257; and a soft vinyl travelling case with accessory pockets for \$90. These options are standard in the \$4995 **Hyperion Plus**, which also includes a time and date clock with

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IBM Talks to Phone

I've mentioned **PConnection**, from **Microperipheral**, before: a plug-in IBM PC modem card — direct connect, Bell 103/113 compatible, with autodial (Touch Tone or pulse) — that answers automatically in both originate and answer modes, and fits inside the PC

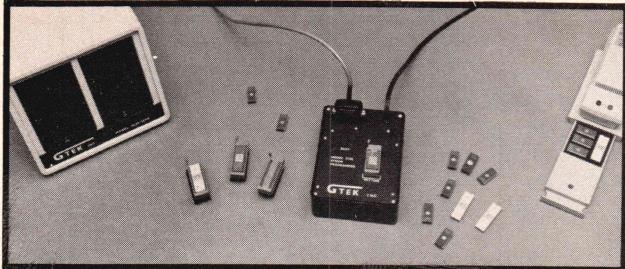


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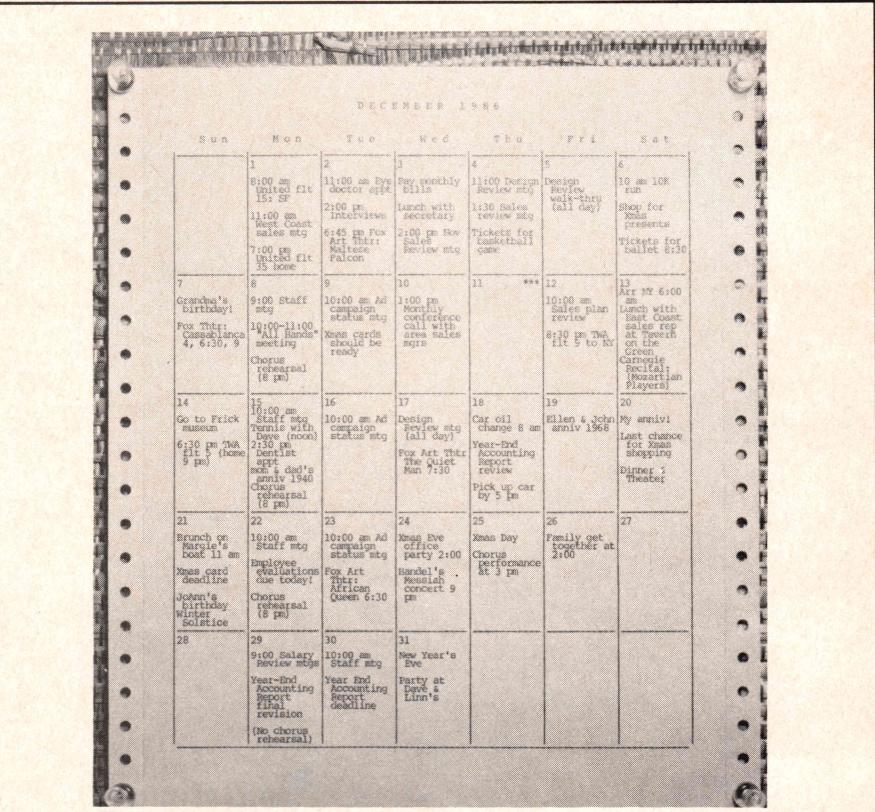
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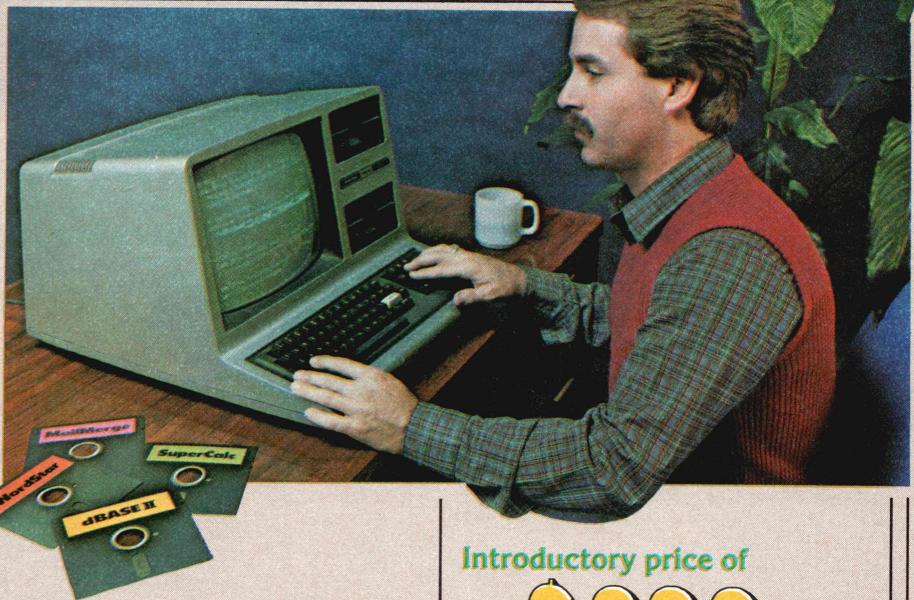
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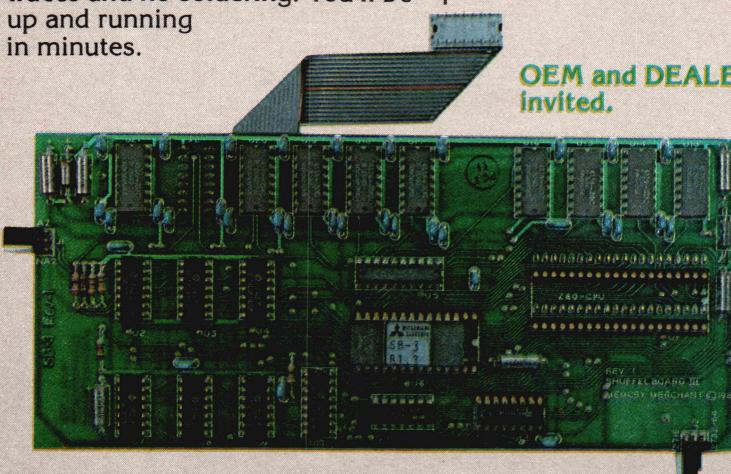
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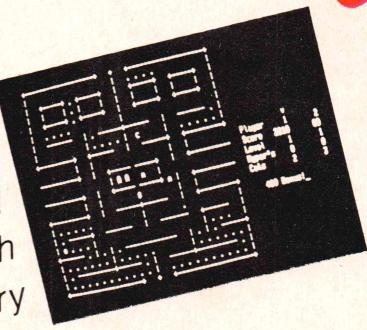
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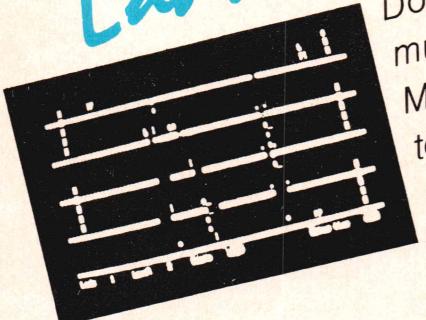
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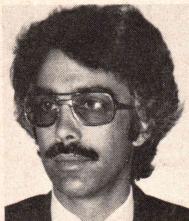
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Image achieved by DGS' CAT 1600 Series color video graphic workstation. Picture courtesy of Digital Graphic Systems, Inc. See story below.

GRAPHICS: NOW MAX-IMIZED

CANOGA PARK — March 30, 1983 — The decreasing costs and increasing density of memory made possible the present boom in digital graphics. Graphic systems designers are now able to take another major step with the introduction of MAX-M, a one megabyte memory board for \$1983. As large size system memory and multi-megabyte Virtual Disk, MAX-M opens up major new low cost implementations.



Wayne Maw, Director of R&D for RGB Dynamics, Salt Lake City, Utah, reports, "My application is dependent on speed. With the Macrotech dynamic board, I have the needed speed." The RGB system is a Z80-based, high resolution color directory system for shopping malls, due for April release.

Empirical Research Group of Kent, Washington, creates a state-of-the-art high resolution color video graphics system by integrating their fast 68000 computer, Macrotech system memory, and the color video image processor from Digital Graphic Systems, Inc., Palo Alto, California. Radcliffe Goddard of Digital Graphics states, "High speed image processing requires large system memory to provide instantaneous display frame paging."

The demand for MAX-M by the graphics industry was nearly instantaneous following the initial Macrotech announcement. ■

MAX—256K to 1M S-100 Memory

CANOGA PARK — March 30, 1983 — Mike Pelkey, Macrotech International president, today released details of the revolutionary MAX line of S-100 memory boards. Pelkey stated: "IEEE-696 now has a new standard for dynamic memory. The MAX product line offers 256K to 1M, at a price that ranges down to less than \$0.00023 per bit." Pelkey continued, "The MI product line now includes our ultra fast (70 ns) 128K static memory, with battery backup capability, plus the 150 ns dynamic memories—in every 128K step from 256K through 1M (1024K) bytes, and add-on kits to permit field upgrade of sizes."

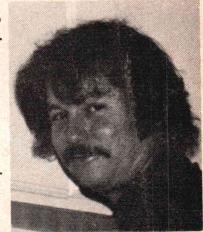
The extreme density of the MAX family is made possible through the use of proprietary PALs (programmable array logic). Also stated as available for add-on to any size MAX is

Macrotech's popular M³ memory mapping architecture. M³ permits the 16-bit address space of an 8-bit processor to be dynamically mapped in 4K pages into as much as 16 megabytes of physical memory.

Parity error detection and 8/16 bit data transfer capabilities are provided as standard on the MAX series memory board. ■

Software for M³ Available

BURBANK — March 30, 1983 — "M³ bank switching for 8-bit processors is much more useful with the new creative systems programs," states Dan West of Westcom Systems Inc. MP/M II* disk intensive applications are greatly improved with the new Virtual Disk routines now available through Macrotech OEM's and dealers for their M³ memory boards.



Westcom Systems, as the software consulting firm for Macrotech, has also provided subroutine listings to easily incorporate M³ mapping into the new CP/M 3.0* (CP/M Plus*) Bios module. The advantages of CP/M 3.0* with disk buffering, hashed directories, and user program expansion go hand in hand with Macrotech's flexible "bank switched" memory capabilities.

All Macrotech software and manuals are available through Dan West's Compuserve account #70250,102. Leave comments/questions as E-Mail.

These new techniques can combine the above features with custom needs of the future, such as printer buffering, multi-page display and memory-intensive graphics displays.

The software listings are included in the Macrotech memory board manuals and are optionally available on 8" diskettes. ■